



SERVICE MANUAL

VHF/UHF FM TRANSCEIVER

IC-91A

VHF/UHF DIGITAL TRANSCEIVER

IC-91AD

S-14230HZ-C1

May. 2006

Icom Inc.

INTRODUCTION

This service manual describes the latest service information for the **IC-91A** VHF/UHF FM TRANSCEIVER, **IC-91AD** VHF/UHF DIGITAL TRANSCEIVER at the time of publication.

MODEL	VERSION	SYMBOL
IC-91AD	U.S.A.	USA
	Export	EXP
IC-91A	U.S.A.	USA-1
	Taiwan	TPE
	Korea	KOR
	Australia	AUS
	Export	
		EXP-2

To upgrade quality, all electrical or mechanical parts and internal circuits are subject to change without notice or obligation.

CAUTION

NEVER connect the transceiver to an AC outlet or to a DC power supply 3.7 V. Such a connection could cause a fire or electric hazard.

DO NOT expose the transceiver to rain, snow or any liquids.

DO NOT reverse the polarities of the power supply when connecting the transceiver.

DO NOT apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front end.

ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit Icom parts number
2. Component name and informations
3. Equipment model name and unit name
4. Quantity required

<SAMPLE ORDER>

5030002880 LCD HLM7972-010100 IC-91A Main unit 5 pieces
8810009560 Screw PH BT M2×6 ZK IC-91A Chassis 10 pieces

Addresses are provided on the inside back cover for your convenience.

REPAIR NOTES

1. Make sure the problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated turning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a signal generator or a sweep generator.
7. **ALWAYS** connect a 30 dB to 40 dB attenuator between the transceiver and a deviation meter or spectrum analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting equipment to the transceiver.


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TABLE OF CONTENTS

SECTION 1 SPECIFICATIONS

SECTION 2 INSIDE VIEWS

SECTION 3 DISASSEMBLY INSTRUCTIONS

SECTION 4 UT-121 UNINSTALLATION

SECTION 5 CIRCUIT DESCRIPTION

5-1	RECEIVER CIRCUITS	5-1
5-2	TRANSMITTER CIRCUITS	5-2
5-3	PLL CIRCUITS	5-4
5-4	OTHER CIRCUITS	5-5
5-5	POWER SUPPLY CIRCUITS	5-5
5-6	PORT ALLOCATION	5-5

SECTION 6 ADJUSTMENT PROCEDURES

6-1	PREPARATION	6-1
6-2	FREQUENCY AND TRANSMIT ADJUSTMENT	6-5

SECTION 7 PARTS LIST

SECTION 8 MECHANICAL PARTS AND DISASSEMBLY

SECTION 9 SEMICONDUCTOR INFORMATION

SECTION 10 BOARD LAYOUTS

10-1	LOGIC UNIT	10-1
10-2	MAIN UNIT	10-1
10-3	RF UNIT	10-1
10-4	UT-121 (Optional product; CODEC UNIT for IC-91AD)	10-1
10-5	VCO UNIT	10-1

SECTION 11 BLOCK DIAGRAM

SECTION 12 VOLTAGE DIAGRAMS

12-1	LOGIC UNIT	12-1
12-2	MAIN UNIT	12-2
12-3	RF UNIT	12-3
12-4	VCO UNIT	12-4
12-5	UT-121 (Optional product; CODEC UNIT for IC-91AD)	12-5

SECTION 1 SPECIFICATIONS

GENERAL

- Frequency coverage : (unit: MHz)

Version	A band	B band
U.S.A.	Tx: 144–148, 420–450* ¹ Rx: 0.495–823.995* ^{1, *2} , 849–868.995, 894–999.990	Tx: 144–148, 420–450* ¹ Rx: 118–174* ² , 350–470* ¹
Taiwan	144–146, 430–432	144–146, 430–432
Korean	144–146, 430–440	144–146, 430–440
Australian	Tx: 144–148, 420–450* ³ Rx: 0.495–999.990* ^{2, *3}	Tx: 144–148, 420–450* ³ Rx: 118–174* ² , 350–470* ³
Export	Tx: 137–174* ² , 400–470* ³ Rx: 0.495–999.990* ^{2, *3}	Tx: 137–174* ² , 400–470* ³ Rx: 118–174* ² , 350–470* ³

*¹Guaranteed 440–450 MHz only, *²Guaranteed 144–148 MHz only,
*³Guaranteed 430–440 MHz only

- Operating mode : FM, AM*, WFM*, DV[†] (*RX only)
- Memory channels : 1304 channels
(Incl. 100 scan edges and 4 call ch.)
- Frequency stability : ±2.5 ppm
(–20°C to +60°C; –4°F to 140°F)
- Tuning steps : 5[†]/6.25[†]/8.33[†]/9[†]/10/12.5/15/20/
25/30/50/100/125 and 200 kHz
- Antenna impedance : 50 Ω (SMA type)
- Power supply : 10.0–16.0 V DC for external DC
power, or specified Icom's battery
pack
- Digital transmission speed : 4.8 kbps[†]
- Voice coding speed : 2.4 kbps[†]
- Current drain (at 7.4 V DC) :

TRANSMIT	VHF	High	2.1 A typ.
		Low	0.8 A (approx.)
	UHF	High	2.2 A typ.
		Low	0.8 A (approx.)
RECEIVE	Stand-by		170 mA typ. (dualwatch; FM/DV [†])
	Max. audio		340 mA typ. (dualwatch; FM/DV [†])
- Operating temp. range : –20°C to +60°C; –4°F to 140°F
- Dimensions : 58.4 (W) × 103 (H) × 34.2 (D) mm
(projections not included) 2 9/32 (W) × 4 1/16 (H) × 1 11/32 (D) in
- Weight (approx.) : 300 g; 10.6 oz
(antenna, BP-217 included)

TRANSMITTER

- Output power (at 7.4 V DC) : 5.0 W (High)
0.5 W (Low; approx.)
- Modulation system FM : Variable reactance frequency
modulation
DV[†] : GMSK reactance frequency
modulation
- Maximum deviation : ±5.0 kHz (FM wide: approx.)
±2.5 kHz (FM narrow: approx.)
- Spurious emissions : Less than –60 dB
- Ext. mic. connector : 3-conductor 2.5 (d) mm; (1/10")/2 kΩ

RECEIVER

- Receiving system :

FM/AM/DV [†]	Double-conversion superheterodyne system
WFM	Triple-conversion superheterodyne system
- Intermediate frequencies :

1st A band	61.65/59.25 MHz (WFM)
B band	46.35 MHz
2nd	450 kHz/13.35 MHz (WFM)
3rd	1.95 MHz (WFM only)
- Sensitivity : (except spurious points)
 - AM (1 kHz/30% Mod.; 10 dB S/N)

0.495–4.995 MHz	1.3 μV typ.
5.000–29.995 MHz	0.56 μV typ.
118.000–137.000 MHz	0.5 μV typ.
222.000–246.995 MHz	0.79 μV typ.
247.000–329.995 MHz	1 μV typ.
 - FM (1 kHz/3.5 kHz Dev.; 12 dB SINAD)

VHF (Amateur band only)	0.14 μV typ.
UHF (Amateur band only)	0.16 μV typ.
1.625–29.995 MHz	0.4 μV typ.
30.000–117.995 MHz	0.25 μV typ.
118.000–173.995 MHz	0.18 μV typ.
174.000–349.995 MHz	0.32 μV typ.
350.000–469.995 MHz	0.22 μV typ.
470.000–599.995 MHz	0.32 μV typ.
600.000–999.990 MHz	0.56 μV typ.
 - WFM (1 kHz/52.5 kHz Dev.; 12 dB SINAD)

76.000–108.000 MHz	1 μV typ.
175.000–221.995 MHz	1.8 μV typ.
470.000–770.000 MHz	2.5 μV typ.
 - DV (digital/PN9 4.8 kbps; BER 1%)[†]

Amateur bands	0.22 μV typ.
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- Selectivity :

FM (Wide), AM	More than 50 dB
FM (Narrow), DV [†]	More than 45 dB
WFM	More than 300 kHz/–3 dB Less than 700 kHz/–20 dB
- Spurious image rejection :

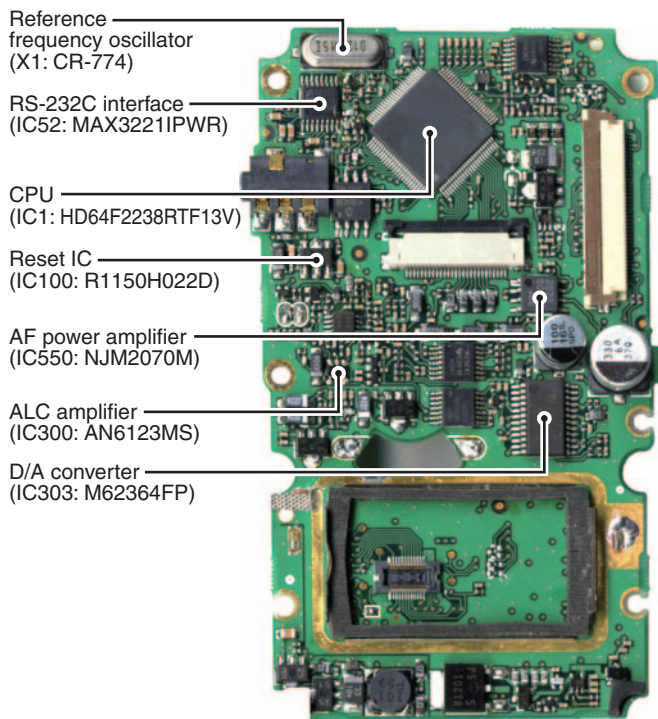
VHF	More than 60 dB
UHF	More than 50 dB (IF; More than 60 dB)
- Audio output power : More than 200 mW
(at 10% distortion with an 8 Ω load)
- Ext. speaker connector : 3-conductor 3.5 (d) mm; (1/8")/8 Ω

[†]Available for the IC-91AD or when UT-121 is installed into the IC-91A.

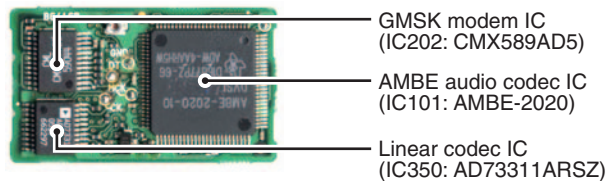
All stated specifications are subject to change without notice or obligation.

SECTION 2 INSIDE VIEWS

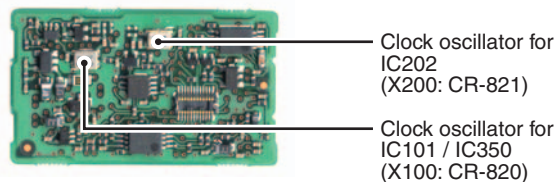
• LOGIC UNIT



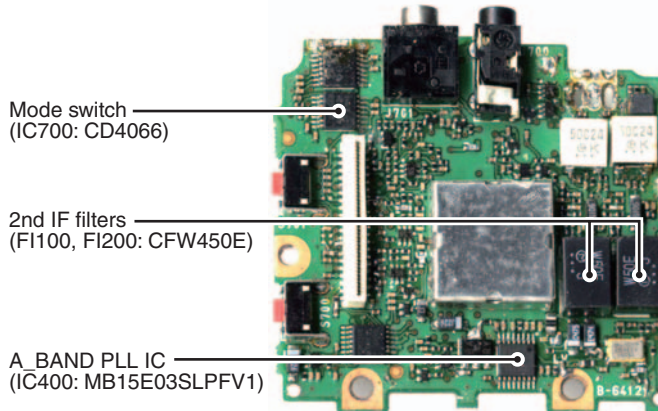
• UT-121 (Optional Product ; CODEC UNIT for IC-91AD) (TOP VIEW)



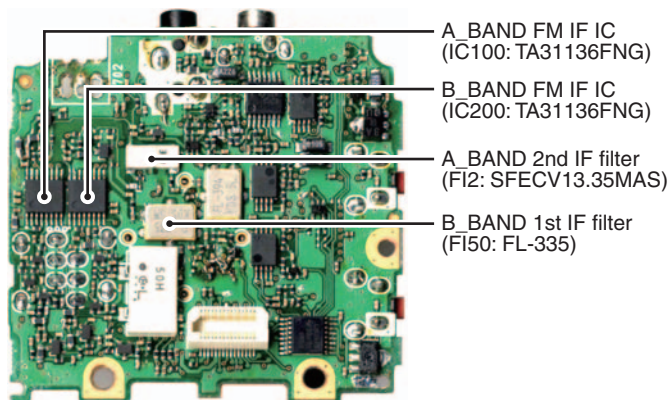
• UT-121 (Optional Product ; CODEC UNIT for IC-91AD) (BOTTOM VIEW)



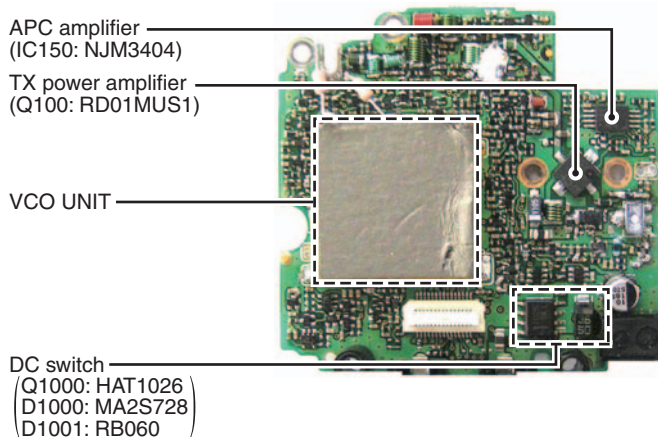
• MAIN UNIT (TOP VIEW)



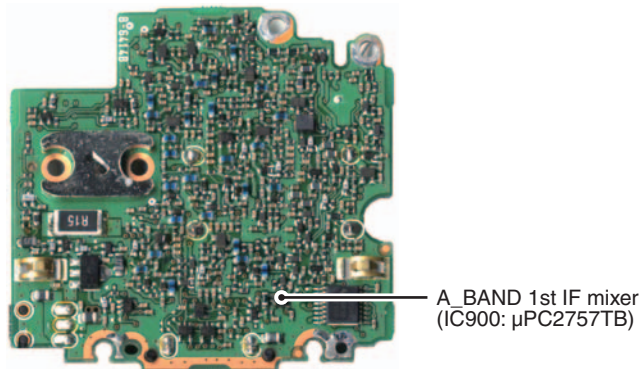
• MAIN UNIT (BOTTOM VIEW)



• RF UNIT (TOP VIEW)



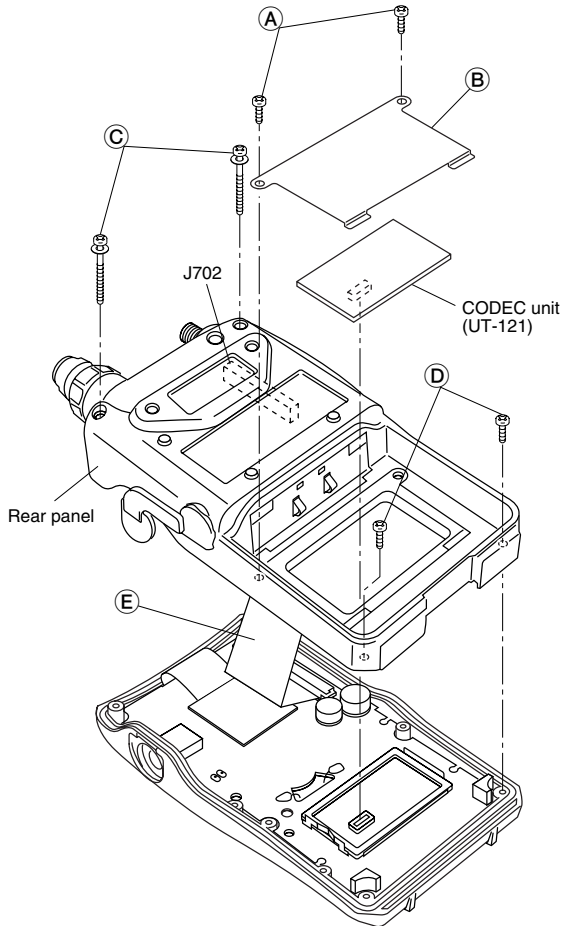
• RF UNIT (BOTTOM VIEW)



SECTION 3 DISASSEMBLY INSTRUCTIONS

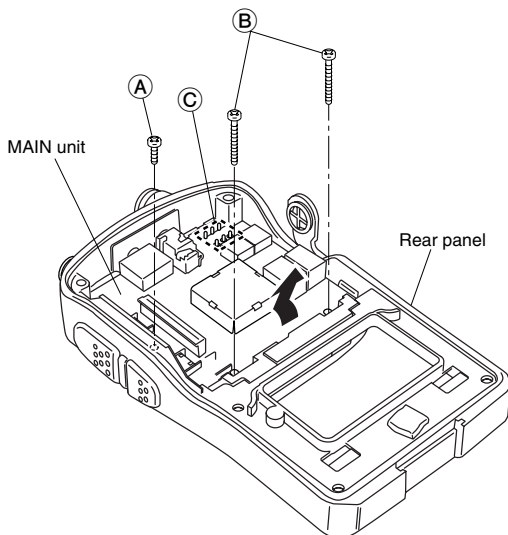
• Removing the rear panel

- ① Unscrew 2 screws (A) and remove the plate (B).
- ② Remove the CODEC unit (UT-121) if installed. (See the page 4-1 "2" for uninstallation)
- ③ Unscrew 2 screws (C) and 2 screws (D).
- ④ Disconnect the flat cable (E) from MAIN unit J702.
- ⑤ Remove the rear panel.



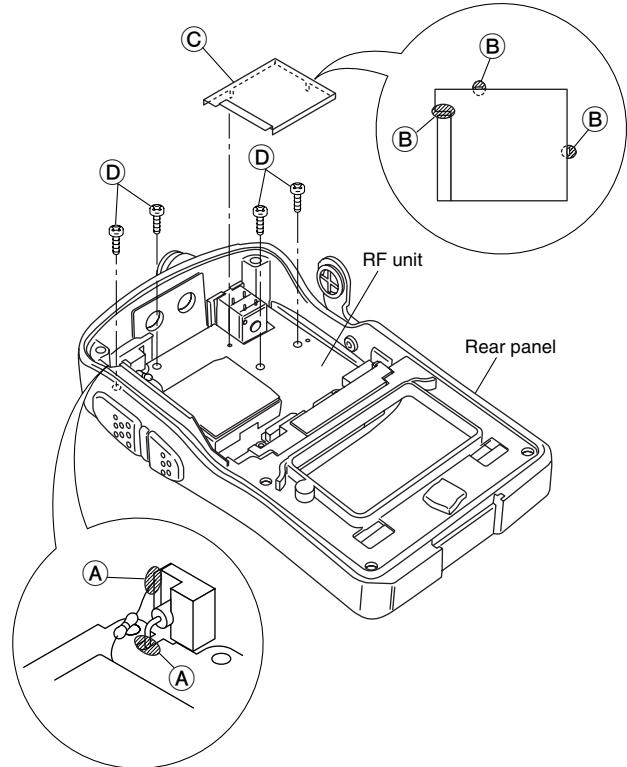
• Removing the MAIN unit

- ① Unscrew the screw (A) and 2 screws (B).
- ② Unsolder 6 points (C).
- ③ Remove the MAIN unit from the rear panel in the direction of the arrow.



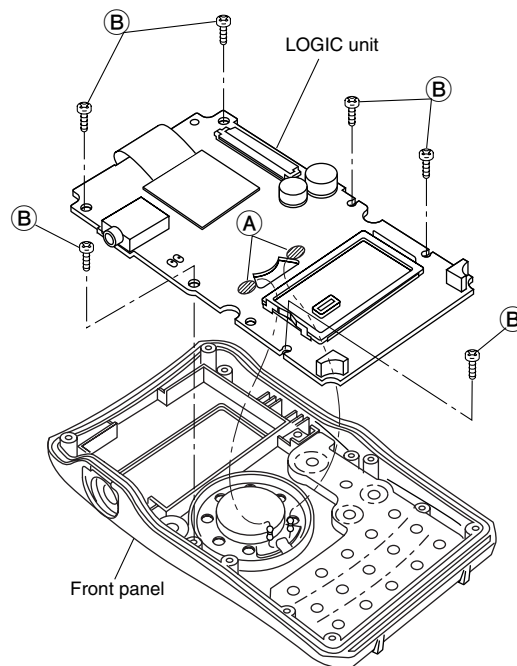
• Removing the RF unit

- ① Unsolder 2 points (A).
- ② Unsolder 3 points (B) and remove the shield plate (C).
- ③ Unscrew 4 screws (D) and remove the RF unit from the rear panel



• Removing the LOGIC unit

- ① Unsolder 2 points (A).
- ② Unscrew 6 screws (B) and remove the LOGIC unit from the front panel.

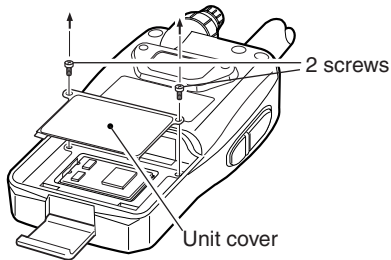


SECTION 4 UT-121 UNINSTALLATION

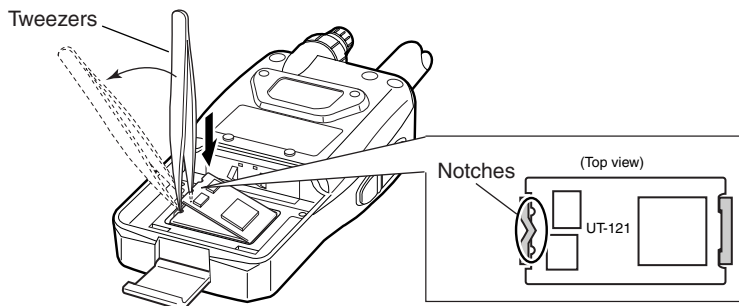
RECOMMENDATION:

Critical technique is necessary for the UT-121 uninstallation, therefore, we recommend you to uninstall it at your dealer or service center.

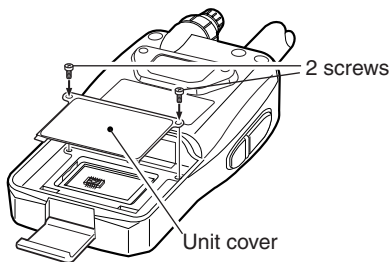
- ① Turn the power OFF, and remove the battery pack.
- ② Unscrew 2 screws to remove the unit cover.



- ③ Insert the tweezers into the notches of the UT-121, and remove it as shown below.



- ④ Screw 2 screws to assemble the unit cover.



SECTION 5 CIRCUIT DESCRIPTION

5-1 RECEIVER CIRCUITS

This transceiver has two receiving lines called A_BAND and B_BAND for dualwatch capability. A_BAND corresponds to FM/WFM/AM mode receiving within 0.495–999 MHz range, and B_BAND corresponds to FM/FM-N/AM/DV mode receiving within 118–174 MHz and 350–470 MHz ranges.

5-1-1 RF CIRCUITS (RF UNIT)

This transceiver has six RF circuits to provide wide receiving range. The received signals from the antenna connector (CHASSIS; J1) are applied to each RF circuit for the frequency coverage, and amplified within the frequency coverage.

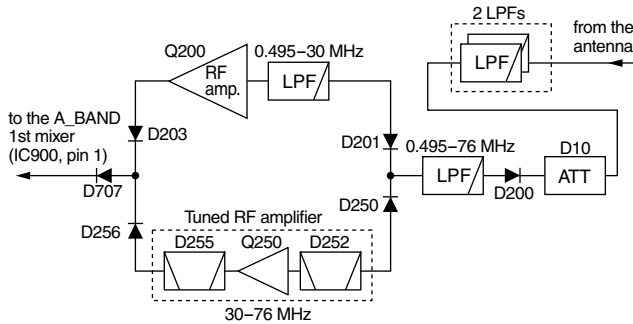
• While receiving 0.495–76 MHz

The received signals of 76 MHz and below are passed through the low-pass filter (LPF; L200, L201, C201–C204) via the two LPFs (L1–L3, C1–C6; L5–L7, C10–C14), attenuator (D10) and band switch (D200).

The 0.495–30 MHz band signals are passed through the band switch (D201), then applied to the RF amplifier (Q200) via the low-pass filter (L202, L203, C205–C209). The 30–76 MHz band signals are applied to the tuned RF amplifier (Q250, D252, D255) via the band switch (D250).

The amplified signals are applied to the A_BAND 1st mixer (IC900, pin 1) via the band switches (D203 or D256, D707).

- 0.495–76 MHz -



• While receiving 76–118 MHz or 174–260 MHz

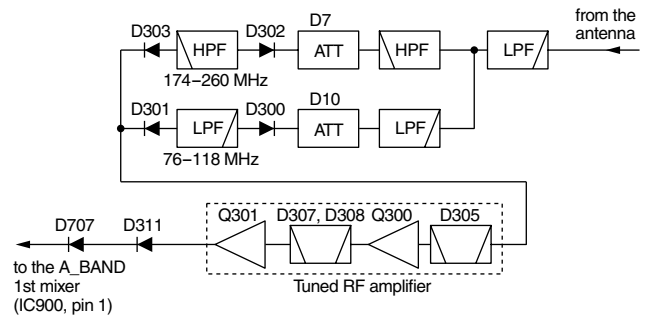
The 76–118 MHz band signals are passed through the two LPFs (L1–L3, C1–C6; L5–L7, C10–C14), attenuator (D10) and low-pass filter (L300, L301, C301–C305) via the band switch (D300).

The 174–260 MHz band signals are passed through the LPF (L1–L3, C1–C6), HPF (L4, C7–C9), attenuator (D7) and high-pass filter (HPF; L302, L303, C306–C310) via the band switch (D302).

The filtered signals are applied to the tuned RF amplifier (Q300, Q301, D305, D307, D308) via the band switch (D301/D303).

The amplified signals are applied to the A_BAND 1st mixer (IC900, pin 1) via the band switches (D311, D707).

- 76–118 MHz or 174–260 MHz -



• While receiving 118–174 MHz

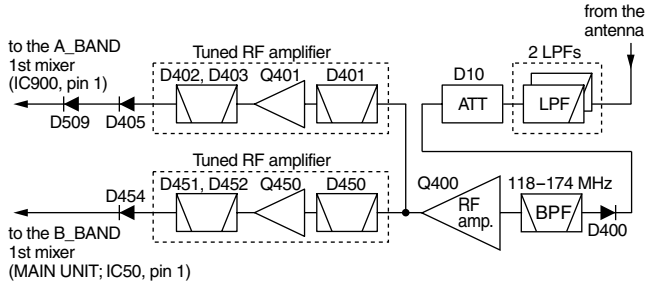
The 118–174 MHz band signals are passed through the two LPFs (L1–L3, C1–C6; L5–L7, C10–C14) and attenuator (D10), then applied to the RF amplifier (Q400) via the band switch (D400) and bandpass filter (BPF; L400, L405, C400, C401).

The amplified signals are applied to the tuned RF amplifiers for A_BAND (Q401, D401–D403) and B_BAND (Q450, D450–D452).

The amplified signals by Q401 are then applied to the A_BAND 1st mixer (IC900, pin 1) via the band switches (D405, D509).

The amplified signals by Q450 are then applied to the B_BAND 1st mixer (MAIN UNIT; IC50, pin 1) via the band switch (D454) and J1001 (pin 15).

- 118–174 MHz -

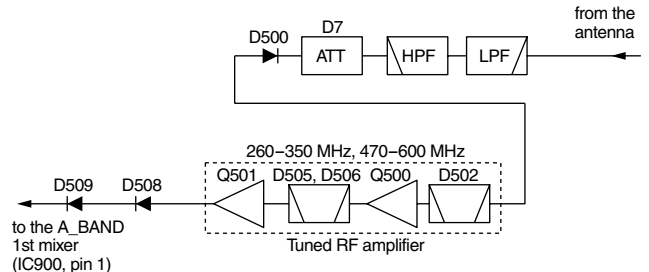


• While receiving 260–350 MHz or 470–600 MHz

The 260–350 MHz and 470–600 MHz band signals passed through the LPF (L1–L3, C1–C6) and HPF (L4, C7–C9), then applied to the tuned RF amplifier (Q500, Q501, D502, D505, D506) via the attenuator (D7) and band switch (D500).

The amplified signals are applied to the A_BAND 1st mixer (IC900, pin 1) via the band switches (D508, D509).

- 260–350 MHz or 470–600 MHz -



• While receiving 350–470 MHz

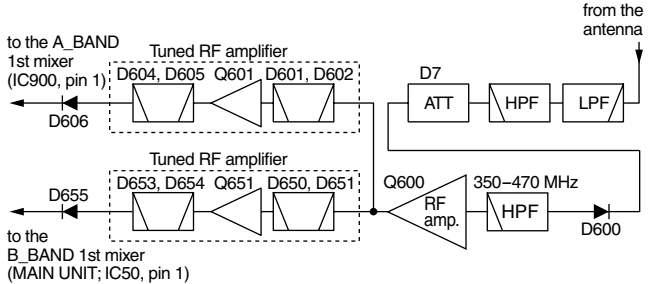
The 350–470 MHz band signals are passed through the LPF (L1–L3, C1–C6), HPF (L4, C7–C9) and attenuator (D7), then applied to the RF amplifier (Q600) via the band switch (D600) and HPF (L600, L601, L608, L611, C600–C602, C630, C631, C633).

The amplified signals are applied to the tuned RF amplifiers for A_BAND (Q601, D601, D602, D604, D605) and B_BAND (Q651, D650, D651, D653, D654).

The amplified signals by Q601 are then applied to the A_BAND 1st mixer (IC900, pin 1) via the band switch (D606).

The amplified signals by Q651 are then applied to the B_BAND 1st mixer (MAIN UNIT; IC50, pin 1) via the band switch (D655) and J1001 (pin 15).

- 350–470 MHz -

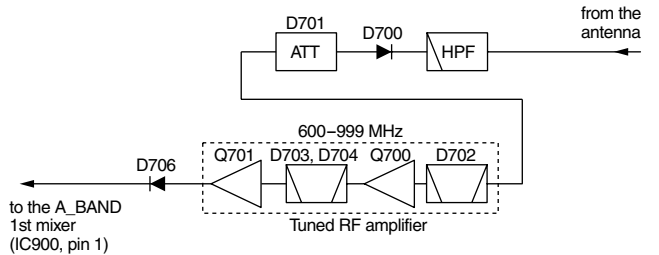


• While receiving 600–999 MHz

The 600–999 MHz band signals are passed through the HPF (L8, L9, C16–C18) and band switch (D700), then applied to the tuned RF amplifier (Q700, Q701, D702–D704) via the attenuator (D701).

The amplified signals are applied to the A_BAND 1st mixer (IC900, pin 1) via the band switch (D706).

- 600–999 MHz -



5-1-2 1ST IF CIRCUITS (RF AND MAIN UNITS)

The 1st IF circuits contain the 1st mixer, 1st IF amplifier and the 1st IF filter. The 1st IF mixer converts the received signals into a fixed frequency of the 1st Intermediate Frequency (IF) signal. The converted 1st IF signal is filtered at the 1st IF filters and amplified at the IF amplifier.

• A_BAND (0.495–999 MHz)

The received signals from the RF circuits are applied to the A_BAND 1st IF mixer (RF UNIT; IC900, pin 1), and converted into the 61.65 MHz (FM/AM)/59.25 MHz (WFM) 1st IF signal by being mixed with 1st Local Oscillator (LO) signals from the VCOs (VCO UNIT).

While receiving 0.495–76 MHz band signals, the 1st LO signals are generated at BC BAND VCO (VCO UNIT; Q1, D1, D3, D5, D6), and for receiving 76–260 MHz band signals, the 1st LO signals are generated at VHF BAND VCO (VCO UNIT; Q51, D51, D54).

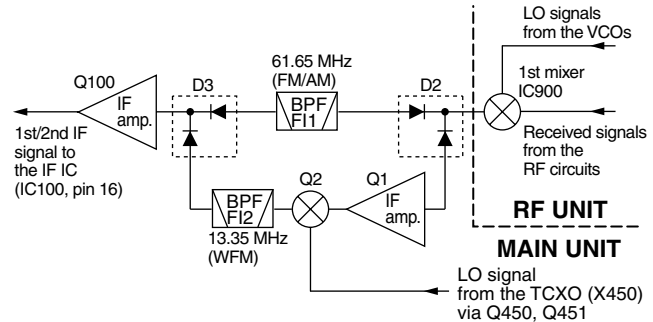
While receiving 260–999 MHz band signals, the 1st LO signals are generated at UHF BAND VCO (VCO UNIT; Q101, D101, D104). If the receiving frequency is 600 MHz or higher, the VCO output signal is doubled at the doubler circuit (RF UNIT; Q850) before being applied to the 1st mixer (RF UNIT; IC900, pin 3).

In FM/AM mode, the converted IF signal is applied to the 1st IF filter (MAIN UNIT; F11) via the mode switch (D2) to filter out the unwanted signals, then applied to the IF amplifier (MAIN UNIT; Q100).

In WFM mode, the converted IF signal is applied to the 1st IF amplifier (MAIN UNIT; Q1) via the mode switch (D2). The amplified 1st IF signal is applied to the 2nd IF mixer (MAIN UNIT; Q2), and converted into the 13.35 MHz IF signal by being mixed with the 45.95 MHz Lo signal. The converted IF signal is passed through the IF filter (MAIN UNIT; F12) to filter out the unwanted signal, then applied to the IF amplifier (MAIN UNIT; Q100).

The amplified 1st or 2nd IF signal from the IF amplifier (MAIN UNIT; Q100) is then applied to the A_BAND IF IC (MAIN UNIT; IC100, pin 16).

• A_BAND 1st IF CIRCUIT



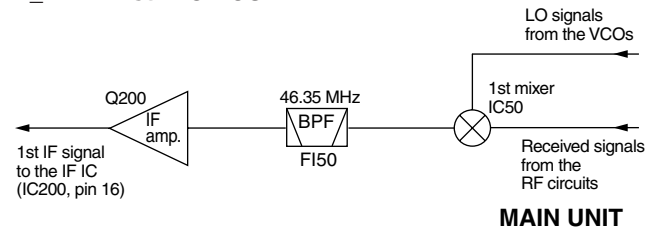
• B_BAND (118–174 MHz, 350–470 MHz.)

The received signals from the RF circuits are applied to the B_BAND 1st IF mixer (MAIN UNIT; IC50, pin 1), and converted into the 46.35 MHz 1st IF signal by being mixed with 1st Local Oscillator (LO) signals from the VCOs.

While receiving 118–174 MHz band signals, the 1st LO signals are generated at B_BAND VBVCO (MAIN UNIT; Q301, D300, D301), and for receiving 350–470 MHz band signals, the 1st LO signals are generated at B_BAND UBVCO (MAIN UNIT; Q351, D350, D352).

The converted 1st IF signal is passed through the 1st IF filter (MAIN UNIT; F150) to filter out the unwanted signal, then applied to the 1st IF amplifier (MAIN UNIT; Q200). The amplified 1st IF signal is then applied to the B_BAND IF IC (MAIN UNIT; IC200, pin 16).

• B_BAND 1st IF CIRCUIT



5-1-3 2ND IF AND DEMODULATOR CIRCUITS (MAIN UNIT)

The 1st IF signal is converted into the 2nd IF signal and demodulated in the IF IC. The IF IC contains 2nd mixer, limiter amplifier, quadrature detector, etc. in its package.

5-1-3-1 2ND IF CIRCUITS

- A_BAND (0.495–999 MHz) -

• FM/AM mode

The 1st IF signal from the IF amplifier (Q100) is applied to the 2nd mixer in the A_BAND IF IC (IC100, pin 16), and converted into the 450 kHz 2nd IF signal by being mixed with the 61.2 MHz 2nd LO signal from the reference frequency oscillator (X450) quadrupled by the quadruplicator (Q452).

The converted 2nd IF signal is output from pin 3, and passed through the 2nd IF filter (F1100) via the mode switch (D102) to suppress sideband noise.

In FM mode, the filtered 2nd IF signal is applied to the limiter amplifier (IC100, pin 5) via the mode switch (D103).

In AM mode, the filtered 2nd IF signal is applied to the IF amplifier (Q103) via the mode switch (D103).

• WFM mode

The 2nd IF signal from the IF amplifier (Q100) is applied to the 3rd mixer in the A_BAND IF IC (IC100, pin 16), and converted into the 1.95 MHz 3rd IF signal by being mixed with the 15.3 MHz 2nd LO signal from the reference frequency oscillator (X450).

The converted IF signal is output from pin 3, and applied to the limiter amplifier (IC100, pin 5) via the mode switches (D102, D103).

5-1-3-2 DEMODULATOR CIRCUITS

• FM/WFM mode

The amplified IF signal from the limiter amplifier in the IF IC is FM-demodulated at the quadrature detector (IC100, pins, 10, 11, X100) and output from pin 9. The demodulated AF signals are applied to the AF amplifier circuits.

• AM mode

The amplified IF signal from the IF amplifier (Q103) is applied to the AM demodulator (Q104, Q105).

The demodulated AF signals are applied to the AF amplifier circuits.

- B_BAND (118–174 MHz, 350–470 MHz) -

The 1st IF signal from the 1st IF amplifier (Q200) is applied to the 2nd mixer in the B_BAND IF IC (IC200, pin 16), and converted into the 2nd IF signal by being mixed with the 2nd LO signal from the reference frequency oscillator (X450) tripled by the tripler (Q451).

The converted 2nd IF signal is output from pin 3, and passed through the 2nd IF filter (FM/AM mode: F1200; FM-N mode: F1201) via the mode switch (D202) to suppress sideband noise.

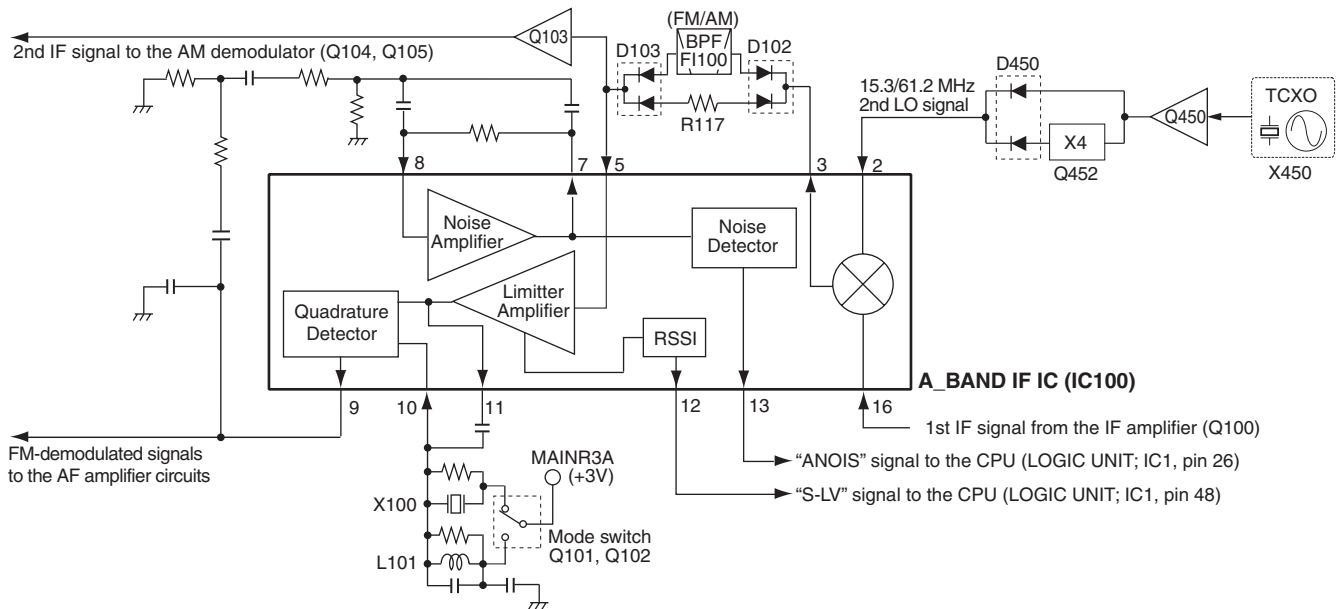
• FM/FM-N mode

The filtered 2nd IF signal is applied to the limiter amplifier in the B_BAND IF IC (IC200, pin 5) via the mode switch (D203). The amplified 2nd IF signal is FM-demodulated at the quadrature detector (IC200, pins, 10, 11, X200) and output from pin 9. The demodulated AF signals are applied to the AF amplifier circuits.

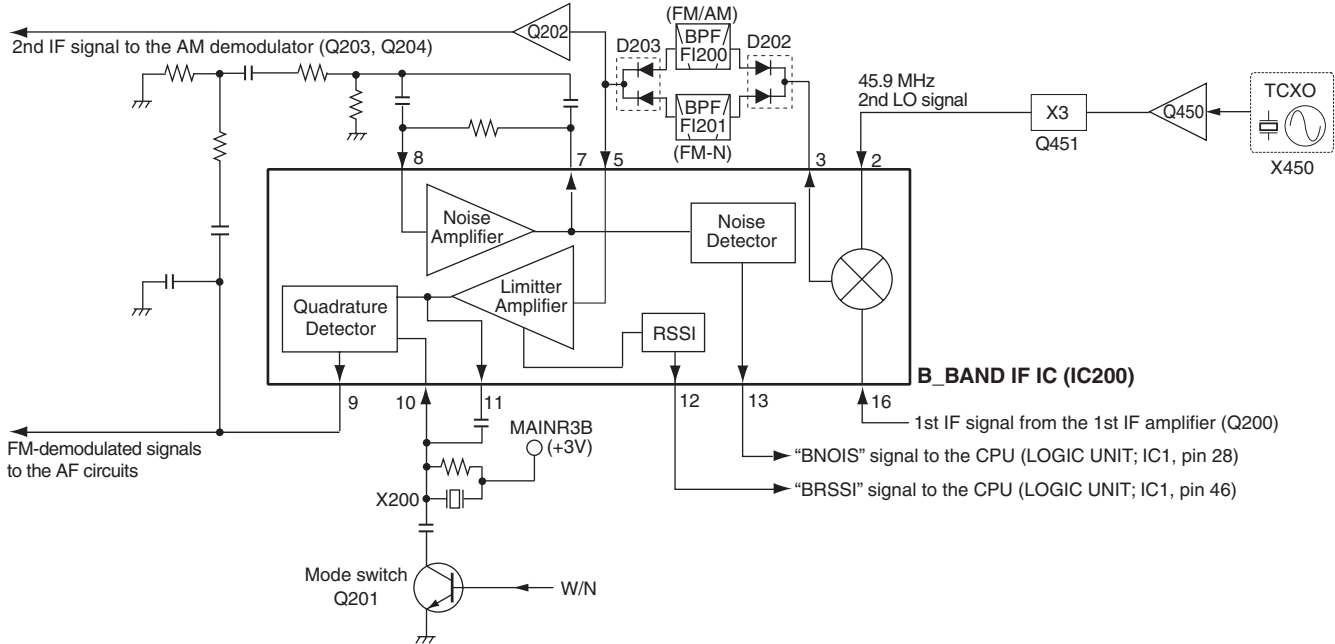
• AM mode

The 2nd IF signal is passed through the F1200 and applied to the AM demodulator circuit (Q203, Q204) via the IF amplifier (Q202). The demodulated AF signals are applied to the AF amplifier circuits.

• A_BAND 2ND IF AND DEMODULATOR CIRCUITS



• **B_BAND 2ND IF AND DEMODULATOR CIRCUITS**



5-1-4 AF AMPLIFIER CIRCUITS (MAIN AND LOGIC UNITS)

The demodulated AF signals from the demodulator circuits are amplified and filtered in AF amplifier circuits.

**- A_BAND -
• FM/WFM mode**

The demodulated AF signals from the A_BAND IF IC (MAIN UNIT; IC100, pin 9) are applied to the mode switch (MAIN UNIT; IC500, pins 1, 2) and then passed through the AF filter (MAIN UNIT; Q550, Q552).

• AM mode

The demodulated AF signals from the AM-demodulator circuit (MAIN UNIT; Q104, Q105) are applied to the AF switch (MAIN UNIT; D105) and then passed through the AF filter (MAIN UNIT; Q550, Q552).

**- B_BAND -
• FM/FM-N mode**

The demodulated AF signals from the B_BAND IF IC (MAIN UNIT; IC200, pin 9) are applied to the mode switches (MAIN UNIT; IC500, pins 10, 11, IC700, pins 11, 10) and then passed through the AF filter circuit (MAIN UNIT; IC501 pins 1, 2; 6, 7).

• AM mode

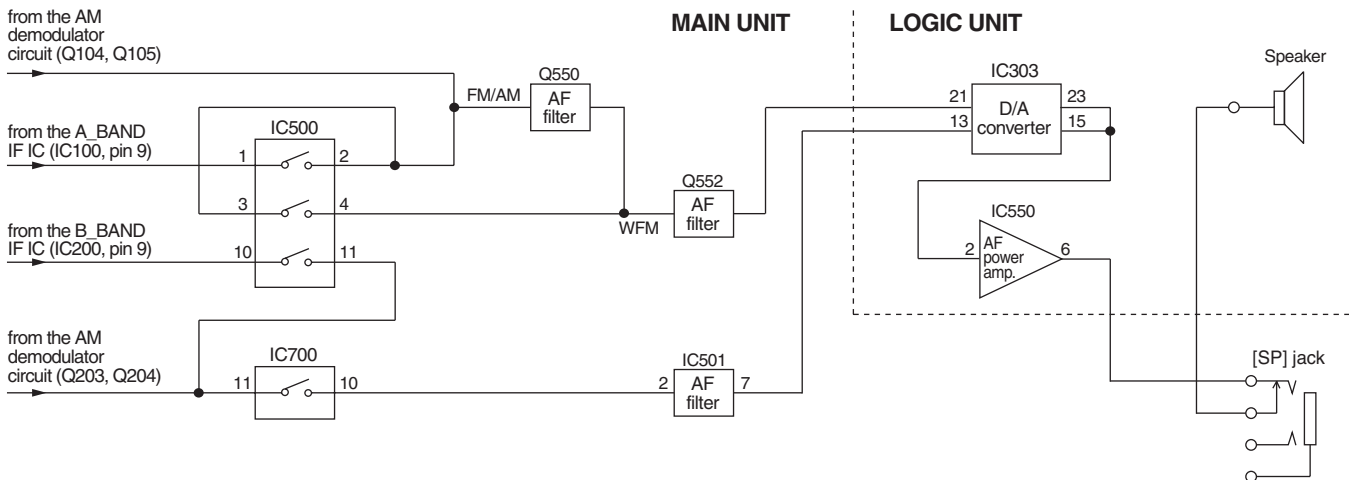
The demodulated AF signals from the AM-demodulator circuit (MAIN UNIT; Q203, Q204) are applied to the AF (MAIN UNIT; D205) and mode switches (MAIN UNIT; IC700, pins 10, 11) and then passed through the AF filter (MAIN UNIT; IC501 pins 1, 2; 6, 7).

The filtered AF signals are applied to the D/A converter (LOGIC UNIT; IC303 for A_BAND; pins 21, 22; 23, 24/for B_BAND; pins 13–16) and level adjusted. The level adjusted AF signals are applied to the AF power amplifier (LOGIC UNIT; IC550, pin 2). The power amplified AF signals are then output from pin 6, and applied to the internal speaker (CHASSIS; SP1) via [SP] connector (MAIN UNIT; J700).

5-1-5 AGC CIRCUIT (MAIN AND RF UNITS)

A portion of the AM-demodulated signals are converted into DC voltage by AGC (Automatic Gain Control) detector (MAIN UNIT; Q104; A_BAND/Q203; B_BAND), and fed back to the IF and RF circuits as the AGC signal.

• AF CIRCUITS



The AGC signal controls the bias of the IF amplifiers (MAIN UNIT; Q100, Q103/Q200, Q202) and RF amplifiers (RF UNIT; Q200, Q250, Q300, Q301, Q401, Q500, Q501, Q601/Q700, Q701, Q450, Q651) according to the received signal strength to prevent audio distortion and stabilize the demodulated AF signal level.

5-1-6 SQUELCH CIRCUITS (MAIN AND LOGIC UNITS)

• NOISE SQUELCH

The noise squelch mutes the AF output signals when no RF signals are received. By detecting noise components in the demodulated AF signals, the squelch circuit toggles the AF power amplifier ON and OFF.

A portion of the FM-demodulated AF signals from the IF IC (MAIN UNIT; IC100/IC200 (A_BAND/B_BAND), pin 9) is passed through the noise filter (MAIN UNIT; IC100/IC200, pins 7, 8, R109–R113, C118–C122/R212–R216, C218–C222). The filtered noise signals are then applied to the noise amplifier in the IF IC to be amplified the noise components only.

The amplified noise components are converted into the pulse-type signal at the noise detector section, and output from pin 13 as the noise signal (A_BAND: “ANOIS”/B_BAND: “BNOIS”). The noise signal is applied to the CPU (LOGIC UNIT; IC1, pin 26/28), then the CPU outputs “AFON” signal from pin 97 according to the noise detection signal level to toggle the AF power amplifier regulator (LOGIC UNIT; Q105, Q106) ON and OFF.

• TONE SQUELCH

The tone squelch detects the tone signal in the demodulated AF signals, and opens the squelch only when matched subaudible tone frequency is detected in the received signal.

While the tone squelch is in use, and the received signal contains no sub-audible tone signal or mismatched tone frequency, the tone squelch mutes the AF signals even if the noise squelch is open.

A portion of the demodulated AF signals from the IF IC (MAIN UNIT; IC100/IC200, pin 9) are passed through the two-staged CTCSS/DTCS filter (MAIN UNIT; IC150/IC250, pins 3, 1 and pins 5, 7) via the mode switch (MAIN UNIT; IC500, pins 1, 2/10, 11) to suppress unwanted voice signals. The filtered CTCSS/DTCS signals (A_BAND: “ATONE”/B_BAND: “BTONE”) are applied to the CPU (LOGIC UNIT; IC1, pin 49/45) via the tone selector (MAIN UNIT; IC703, pins 3, 4/10, 11).

The CPU decodes the CTCSS/DTCS signal, and outputs “AFON” signal from pin 97 according to the set CTCSS/DTCS signal to toggle the AF power regulator (LOGIC UNIT; Q105, Q106) ON and OFF.

5-2 TRANSMITTER CIRCUITS

5-2-1 MICROPHONE AMPLIFIER CIRCUIT (LOGIC UNIT)

The microphone amplifier circuit contains AF amplifier, IDC, splatter filter, etc. The AF signals from the microphone (thereafter, it is called “MIC signals”) are filtered and level-adjusted at this circuit.

MIC signals from the microphone (MC300) are applied to the MIC amplifier (Q302), then the amplified MIC signals are applied to the mode switch (IC301, pin 1). In FM mode, the MIC signals are output from pin 6 and applied to the IDC (Instantaneous Deviation Control; IC302, pin 5).

In DV mode[†], the MIC signals are output from pin 7 and applied to the IDC (IC302, pin 5) via the ALC amplifier (IC300, pins 3, 5).

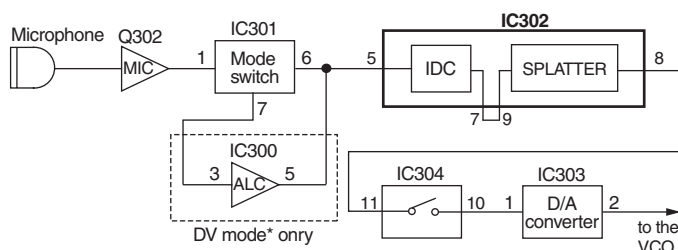
The IDC limits the level of the amplitude of MIC signals to prevent over deviation. The limited MIC signals are output from pin 7 and passed through the splatter filter (IC302, pins 8, 9).

The splatter filter suppresses 3 kHz and higher audio components.

The filtered MIC signals are applied to the D/A converter (IC303, pin 1) via the mode switch (IC304, pins 10, 11). The D/A converter (IC303) adjusts the deviation according to “DATA” signal from the CPU (IC1, pin 30).

The level adjusted MIC signals are output from pin 2 and then applied to the modulation circuit (VCO UNIT; D55 or D100) to modulate the VCO oscillating signal.

• MICROPHONE AMPLIFIER CIRCUIT



5-2-2 MODULATION CIRCUIT (VCO UNIT)

The modulation circuit modulates the VCO oscillating signal with the AF signals from the microphone and the tone signals from the CPU.

• MICROPHONE SIGNALS

The level adjusted MIC signals from the D/A converter (LOGIC UNIT; IC303, pin 2) are applied to the D55 (in transmitting on 144 MHz band) or D100 (in transmitting on 430 MHz band) to modulate the VCO oscillating signal by changing the reactance of D55 or D100. The modulated VCO output signal is buffer-amplified by Q200 and Q201, then applied to the transmit amplifiers via TX/RX switch (RF UNIT; D100) and amplifier (RF UNIT; IC100, pins 1, 4), as a transmit signal.

• TONE SIGNALS

The CTCSS and DTCS signals are generated by the CPU (LOGIC UNIT; IC1) and output from pin 43.

The CTCSS/DTCS signals are passed through the tone filter (Q307, R338–R340, C339, C340). The filtered tone signals are mixed with the MIC signal coming from the IDC amplifier (LOGIC UNIT; IC302, pin 7), then applied to the modulation circuit via the splatter filter, mode switch and D/A converter.

The CTCSS/DTCS signals are passed through the tone filter (Q307, R338–R340, C339, C340). The filtered tone signals are mixed with the MIC signals come from the IDC amplifier (LOGIC UNIT; IC302, pin 7), then applied to the modulation circuit (D55 or D100) via the splatter filter (LOGIC UNIT; IC302, pins 9, 8), mode switch (LOGIC UNIT; IC304, pins 11, 10) and D/A converter (LOGIC UNIT; IC303, pins 4, 3).

5-2-3 TRANSMIT AMPLIFIERS (RF UNIT)

The VCO output signal is amplified to transmit output power level by the transmit amplifiers.

The VCO output signal from the amplifier (IC100) is applied to the pre-driver (Q102), driver (Q101) and power (Q100) amplifiers in sequence to be amplified to the transmit output power level. The power amplified transmit signal is passed through the antenna switching circuit and transmit filters.

[†]Available for IC-91AD or when the optional UT-121 is installed into IC-91A.

5-2-4 APC CIRCUIT (RF UNIT)

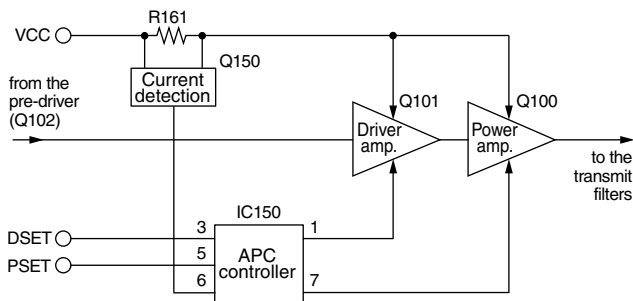
The APC (Automatic Power Control) circuit stabilizes transmit output power and controls it High or Low. The APC of this transceiver is a current monitoring type that detects the transmit signal current of the transmit amplifiers and controls the transmit output power.

While transmitting, the voltage drop in R161 is detected by the current detection circuit (Q150) and applied to the APC controller (IC150, pin 6). Also, "PSET" signal (power setting reference voltage) from the D/A converter (IC950, pin 19) is applied to another input (pin 5).

Then the voltage difference between pins 5 and 6 is output from pin 7, and the transmit output power is controlled by varying of the gate voltage of the power amplifier (Q100).

When the transmit output power is set to Low, the driver gate voltage "DSET" is controlled at the same time as the power setting reference voltage "PSET" and the dynamic range of the power control is ensured. Thus, the APC circuit maintains a constant transmit output power.

• APC CIRCUIT

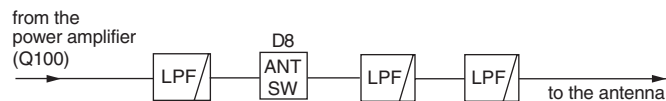


5-2-5 TRANSMIT FILTERS (RF UNIT)

The power amplified transmit signal from the power amplifier is filtered at the transmit filters. The transmit filters prevent unwanted RF signals being emitted to the air.

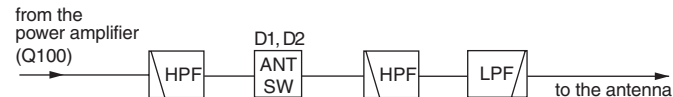
While transmitting in 144 MHz band, the power amplified transmit signal is passed through the LPF (L102, L103, C106, C107), antenna switch (D8) and two LPFs (L5–L7, C10–C14; L1–L3, C1–C6) before being applied to the antenna connector (CHASSIS; J1).

• 144 MHz



While transmitting in 430 MHz band, the power amplified transmit signal is passed through the HPF (L100, L101, C100–C103), antenna switch (D1, D2), HPF (L4, C7–C9) and LPF (L1–L3, C1–C6) before being applied to the antenna connector (CHASSIS; J1).

• 430 MHz



5-3 PLL CIRCUITS

5-3-1 VCO CIRCUITS (VCO UNIT AND MAIN UNIT)

This transceiver has 5 VCOs; BC BAND VCO, VHF BAND VCO, UHF BAND VCO, B_BAND VBVCO and B_BAND UBVCO. The BC BAND VCO, B_BAND VBVCO and B_BAND UBVCO oscillate the 1st LO signals. The VHF BAND VCO and UHF BAND VCO oscillate both transmit output signal and 1st LO signals.

- A_BAND -

• BC BAND VCO

The BC BAND VCO (VCO UNIT; Q1, D1, D3, D5, D6) generates the 1st LO signals for receiving 0.495–76 MHz signals. The output signals are amplified at the buffer amplifiers (VCO UNIT; Q200, Q201), then applied to the 1st mixer (RF UNIT; IC900, pin 3) via TX/RX switch (RF UNIT; D850).

• VHF BAND VCO

The VHF BAND VCO (VCO UNIT; Q51, D51, D54, D55) generates both of transmit output signal for 144 MHz band and 1st LO signals for receiving 76–260 MHz.

While receiving, the VCO oscillates the 1st LO frequency, and the output signals are amplified at the buffer amplifiers (VCO UNIT; Q200, Q201). The buffer-amplified signals are applied to the 1st mixer (RF UNIT; IC900, pin 3) via TX/RX switch (RF UNIT; D850).

While transmitting, the VCO oscillates the transmit frequency, and the output signal is amplified at the buffer amplifiers (VCO UNIT; Q200, Q201). The buffer-amplified signals are applied to the transmit amplifiers via TX/RX switch (RF UNIT; D100).

• UHF BAND VCO

The UHF BAND VCO (Q101, D100, D101, D104) generates both of the transmit output signal for 430 MHz band and 1st LO signals for receiving 260–999 MHz.

While receiving, the VCO oscillates the 1st LO frequency, and the output signals are amplified by the buffer amplifiers (VCO UNIT; Q200, Q201). If the receiving frequency is 600 MHz and below, the buffer-amplified signals are applied to the 1st mixer (RF UNIT; IC900, pin 3) via TX/RX switch (RF UNIT; D850). If the receiving frequency is 600 MHz and higher, the buffer-amplified signals are applied to the doubler circuit (RF UNIT; Q850), then the doubled signals are applied to the 1st mixer (RF UNIT; IC900, pin 3).

While transmitting, the VCO oscillates the transmit frequency, and the output signal is amplified at the buffer amplifiers (VCO UNIT; Q200, Q201), then applied to the transmit amplifiers via TX/RX switch (RF UNIT; D100).

A portion of the VCO output signals generated at BC/VHF/UHF BAND VCO are applied to the PLL IC (RF UNIT; IC800, pin 4) via buffer amplifiers (VCO UNIT; Q200, Q202) for comparison signal.

- B_BAND -

• VBVCO

The VBVCO (MAIN UNIT; Q301, D300, D301) generates the 1st LO signals for receiving 118–174 MHz signals. The output signals are amplified at the buffer amplifier (MAIN UNIT; Q353), then applied to the 1st mixer (MAIN UNIT; IC50, pin 3).

• UBVCO

The UBVCO (MAIN UNIT; Q351, D350, D352) generates the 1st LO signals for receiving 350–470 MHz signals. The output signals are amplified at the buffer amplifier (MAIN UNIT; Q353), then applied to the 1st mixer (MAIN UNIT; IC50, pin 3).

A portion of the VCO output signals generated at VBVCO/UBVCO are applied to the PLL IC (MAIN UNIT; IC400, pin 8) via buffer amplifier (MAIN UNIT; Q353) for comparison signal.

5-3-2 PLL CIRCUITS (RF AND MAIN UNITS)

The PLL circuit provides stable oscillation of the transmit frequency and receive 1st LO frequency. The PLL circuit compares the phase of the divided VCO frequency with the reference frequency. The PLL output frequency is controlled by the divided ratio (N-data) from the CPU.

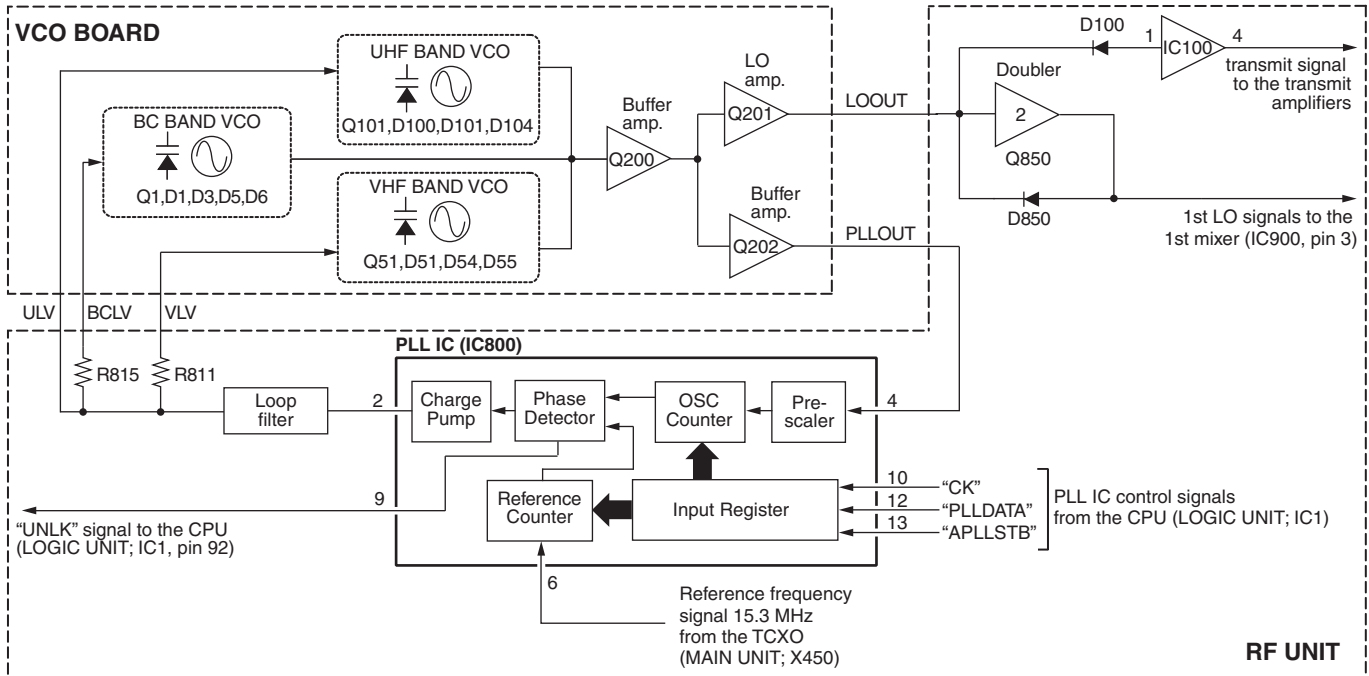
• A_BAND PLL CIRCUIT

The buffer amplified signals from the buffer amplifier (VCO UNIT; Q202) are applied to the PLL IC (RF UNIT; IC800, pin 4). The applied signals are divided at the prescaler and OSC counter according to the "PLLDATA" signal from the CPU (LOGIC UNIT; IC1, pin 94). The divided signal is phase compared with the reference frequency at the phase detector.

The phase difference is output from pin 2 as a pulse type signal after being passed through the internal charge pump. The output signal is applied to the BC/VHF/UHF BAND VCOs (VCO UNIT) after being converted into the DC voltage (lock voltage) at the loop filter (RF UNIT; C820–C823, R806–R808).

The lock voltage for BC BAND VCO ("BCLV") from the loop filter is applied to the VCO (VCO UNIT) via R815 and pin 1 of J800 (RF UNIT). The lock voltage for VHF BAND VCO ("VLV") from the loop filter is applied to the VCO (VCO UNIT) via R811 and pin 6 of J800 (RF UNIT). The lock voltage for UHF BAND VCO ("ULV") from the loop filter is applied to the VCO (VCO UNIT) via pin 16 of J800 (RF UNIT).

• A_BAND PLL AND VCO CIRCUITS



• B_BAND PLL CIRCUIT

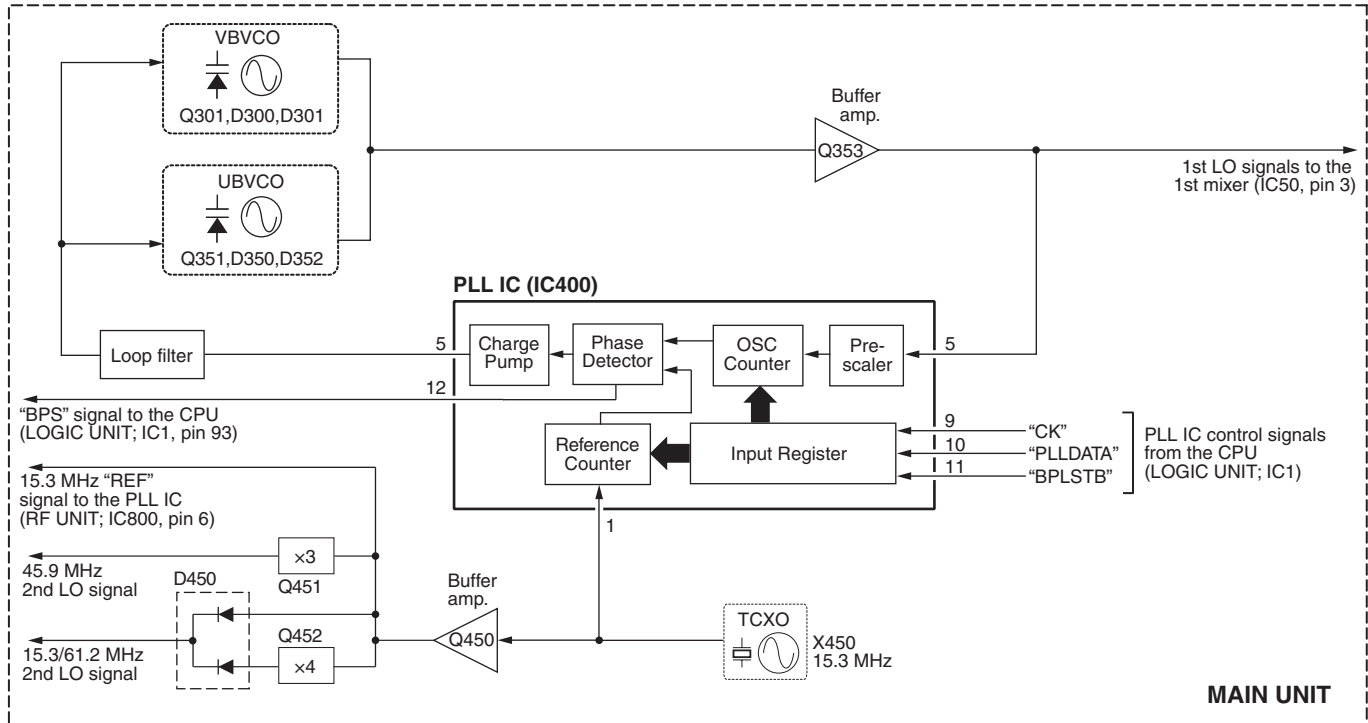
The buffer amplified signals from the buffer amplifier (MAIN UNIT; Q353) are applied to the PLL IC (MAIN UNIT; IC400, pin 8). The applied signals are divided at the prescaler and OSC counter according to the "PLLDATA" signal from the CPU (LOGIC UNIT; IC1, pin 94). The divided signal is phasecompared with the reference frequency at the phase detector.

The phase difference is output from pin 5 as a pulse type signal after being passed through the charge pump. The output signal is

applied to the B_BAND VBVCO/UBVCO (MAIN UNIT) after being converted into the DC voltage (lock voltage) at the loop filter (MAIN UNIT; R404–R406, C410–C412, C414). The lock voltage from the loop filter is applied to each VCO.

If the oscillated signal drifts, its phase changes from that of the reference frequency, causing a lock voltage change to compensate for the drift in the oscillated frequency.

• B_BAND PLL AND VCO CIRCUITS



5-4 DIGITAL CIRCUITS

5-4-1 LINER CODEC

(UT-121; CODEC UNIT for IC-91AD)

IC350 is a liner codec IC which converts transmitting AF signals from the LOGIC unit into digital signals, and outputs them to the audio codec IC (IC101) as 16-bit audio data.

IC350 also converts the 16-bit audio data from the audio codec IC (IC101) into analog signals, and outputs them to the LOGIC unit as the receiving AF signals.

5-4-2 AUDIO CODEC

(UT-121; CODEC UNIT for IC-91AD)

IC101 is an AMBE audio codec IC. While receiving, digital signals from the LOGIC unit are expansion decoded in IC101, and output to the liner codec IC (IC350).

While transmitting, 16-bit audio data from the liner codec IC (IC350) are compression coded in IC101, and output to the LOGIC unit.

5-4-3 MODEM (UT-121; CODEC UNIT for IC-91AD)

IC202 is a GMSK modem IC. While receiving, demodulated AF signals from the MAIN unit are output to the LOGIC unit as digital signals synchronized with clock signal.

While transmitting, digital signals from the LOGIC unit are converted into GMSK base-band signal, and output to the LOGIC unit.

5-5 POWER SUPPLY CIRCUITS (LOGIC UNIT)

Line	Description
HV	The same voltage as the connected external power supply through the external DC IN jack (RF UNIT; J1000).
VCC	The same voltage as the attached battery pack passed through the DC switch (RF UNIT; Q1000, D1000, D1001). The voltage is applied to the drive amplifier (RF UNIT; Q101), power amplifier (RF UNIT; Q100) and LED driver (LOGIC UNIT; Q202, Q203).
+3CPU	Common 3 V converted from VCC line at the CPU3 regulator (LOGIC UNIT; IC100). The converted voltage is applied to the CPU (IC1), EEPROM (IC51), etc.
+3V	Common 3 V converted from +3CPU line at the +3V regulator (Q100, Q101, D100). The converted voltage is applied to the PLL IC (RF UNIT; IC800), D/A converters (MAIN UNIT; IC701, IC702), etc.
RFR3	Receive 3 V controlled by RFR3 regulator (Q602, Q603) using "R3C" signal from the expand IC (MAIN UNIT; IC701, pin 14), controlled by strobe signal "IOSTB3" from the CPU (LOGIC UNIT; IC1, pin 87). The voltage is applied to the RF circuits (RF UNIT).
T5	Transmit 5 V controlled by T5 regulator (Q650–Q652) using "TXC" signal from the CPU (IC1, pin 99). The controlled voltage is applied to the APC amplifier (IC150), pre-driver (Q102), etc.
VCO3V	Common 3 V converted from VCC line at the VCO3V regulator (Q800, Q801, D800, D801). The voltage is applied to the VCO regulators.
BCVCO3	Receive 3 V controlled by BCVCO3 regulator (RF UNIT; Q801) using "BCVCOC" signal from the expander (RF UNIT; IC951, pin 5), controlled by "IOSTB1" signal from the CPU (IC1, pin 29). The controlled voltage is applied to the BC BAND VCO (VCO UNIT; Q1, D1, D3, D5).
VVCO3	Common 3 V controlled by VVCO3 regulator (Q800) using "VAVCOC" signal from the expander (IC951, pin 6), controlled by the "IOSTB1" from the CPU (LOGIC UNIT; IC1, pin 29). The controlled voltage is applied to the VHF BAND VCO (VCO UNIT; Q51, D51, D54, D55).
UVCO3	Common 3 V controlled by VVCO3 regulator (Q800) using "UAVCOC" signal from the expander (IC951, pin 7), controlled by the "IOSTB1" from the CPU (LOGIC UNIT; IC1, pin 29). The controlled voltage is applied to the UHF BAND VCO (VCO UNIT; Q101, D100, D101, D104).
+5.5V	Common 5.5 V boosted at the DC-DC up-converter (IC101, D101). The boosted voltage is applied to the PLL IC (RF UNIT; IC800), PLL IC (MAIN UNIT; IC400), reference oscillator (X450), etc.

5-6 CPU PORT ALLOCATION

Pin No.	Port Name	Description
1	BLED	Outputs TX/RX indicator (DS201) control signal to the TX/RX LED driver (Q201). "High"=While the squelch is open.
2	LIGHT	Outputs backlight control signal to the backlight LEDs driver (Q203). "High"=While backlight is ON.
3–7	KR0–KR4	Input ports for keypad.
8	POWER	Input port for [PWR] key (S1). "Low"=When the key is pushed.
9	BATT	Outputs power supply line select signal to the DC switch (IC50, Q35).
10	ESIO	I/O port for data signal from the EEPROM (IC51).
11	ECK	Outputs clock signal to the EEPROM (IC51).
13–17	KS0–KS3	Input ports for keypad.
22	PTT	Input port for [PTT] key (MAIN UNIT; S701). "High"=While the key is pushed.
23	SQL	Input port for [SQL] key (MAIN UNIT; S700). "Low"=While the key is pushed.
24, 5	DICK, DIUD	Input ports for [DIAL] (MAIN UNIT; S702).
26	ANOIS	Input port for noise signal from the A_BAND IF IC (MAIN UNIT; IC100).
27	DTCS	Outputs tone filter passband control signal to the tone filter (R337–R340, C338–C341) switch (Q307). "High"=During mode operation.
28	BNOIS	Input port for noise signal from the B_BAND IF IC (MAIN UNIT; IC200).
29	IOSTB1	Outputs strobe signal to the D/A converter (RF UNIT; IC951).
30	DATA	Outputs common data signal to the D/A converters (LOGIC UNIT; IC303, MAIN UNIT; IC702, RF UNIT; IC951).
31	AMBETXD†	Outputs AME TX data to the attached UT-121 (CODEC UNIT for IC-91AD).
32	AMBERXD†	Input ports for AME RX data from the attached UT-121 (CODEC UNIT for IC-91AD).
33	CK	Outputs common clock signal to the D/A converters (LOGIC UNIT; IC303, MAIN UNIT; IC702, RF UNIT; IC951).
34	DASTB1	Outputs strobe signal to the D/A converter (RF UNIT; IC950).
35	DASTB2	Outputs strobe signal to the D/A converter (IC303).
36	CHGC	Outputs charge control signal to the charger circuit (Q150, Q153, D150, D152). "High"=While charging.
37	CHGH	Outputs charging current control signal to the charger circuit (Q150, Q153, D150, D152). "High"=When the charging is completed.
38	CPUHV	Inputs HV line detection signal from the HV line voltage detector (Q51, D52). "Low"=While no external DC power supply is connected.
39	PCON	Outputs power control signal to the +3V regulator (Q100, Q101, D100). "Low"=While the transceiver's power is OFF.
41	MUTE	Outputs TX mute signal to the mode switch (IC301). "High"=While the transmitting is muted.
43	CTCOUT	Outputs CTCSS/DTCS signals.

†Available for the IC-91AD or when UT-121 is installed into the IC-91A.

5-6 CPU PORT ALLOCATION (continued)

Pin No.	Port Name	Description
44	DTMF	Outputs DTMF/beep/1750 Hz tone signals.
45	BTONE	Input port for tone signals (B_BAND; CTCSS, DTCS, etc.) from the tone selector (MAIN UNIT; IC703).
46	ATONE	Input port for tone signals (A_BAND; CTCSS, DTCS, etc.) from the tone selector (MAIN UNIT; IC703).
47	S-LV	Input port for RSSI signal from the D/A converter (MAIN UNIT; IC700).
48	VOL	Input port for audio level setting signal from the volume controller (MAIN UNIT; S702).
49	BRSSI	Input port for RSSI signal (B_BAND) from the IF IC (MAIN UNIT; IC200).
50	TEMP	Input port for transceiver's temperature detection signal from the detecting temperature selector (IC50).
51	VIN	Input port for the voltage level from the connected power supply.
59	RESET	Input port for reset signal from the reset IC (IC100).
69	CLSFT	Outputs CPU clock frequency shift signal to the clock frequency oscillator (X1, D103).
70	ACQ [†]	Outputs ACQ signal to the modem IC (UT-121; IC202).
71	DCEL [†]	Outputs DSEL signal to the modem IC (UT-121; IC202).
72	RXCK [†]	Input port for RX clock signal from the modem IC (UT-121; IC202).
73	RXDT [†]	Input port for RX data to the modem IC (UT-121; IC202).
74	TXDT [†]	Outputs TX data to the modem IC (UT-121; IC202).
75	TXCK [†]	Input port for TX clock signal from the modem (UT-121; IC202).
83	TX232	Outputs data signal to the RS-232C driver (IC52).
84	RX232	Input port for data signal from the RS-232C driver (IC52).
85	AMBECLK [†]	Outputs AME clock signal to the DSP IC (UT-121; IC101).
86	AMBERES [†]	Outputs AME reset signal to the DSP IC (UT-121; IC101).
87	IOSTB3	Outputs strobe signal to the D/A converter (MAIN UNIT; IC701).
88	AMBESTB [†]	Outputs AME strobe signal to the DSP IC (UT-121; IC101).
89	AMBEEPR [†]	Input port for AMEEPR signal from the DSP IC (UT-121; IC101).
90	DVC	Outputs +3D line control signal to the 3.3 V regulator (UT-121; Q50, Q51). "Low"=During DV [†] mode operation.
91	D_AS	Outputs mode select signal to the mode switch (IC301). "Low"=During DV [†] mode operation.
92	BPS	Outputs power control signal to the B_BAND PLL IC (MAIN UNIT; IC400). "Low"=During in power save mode.
93	UNLK	Input port for unlock signal from the PLL IC (RF UNIT; IC800). "Low"=While the PLL is unlocked.
94	PLLDATA	Outputs data signal to the PLL IC's (MAIN UNIT; IC400, RF UNIT; IC800).
95	BPLSTB	Outputs strobe signal to the PLL IC (MAIN UNIT; IC400).

Pin No.	Port Name	Description
96	IOSTB2	Outputs strobe signal to the D/A converter (MAIN UNIT; IC702).
97	AFON	Outputs AF power amplifier control signal to the AF6V regulator (Q105, Q106). "High"=While the squelch is open.
98	APLLSTB	Outputs strobe signal to the A_BAND PLL IC (RF UNIT; IC800).
99	TXC	• Outputs transmitter circuit control signal to the LED driver (Q200) and the T5 regulator (MAIN UNIT; Q650–Q652) "High"=While transmitting.
100	MICC	Outputs microphone amplifier (Q302) and ALC circuit (IC301) control signal to the microphone amplifier driver (Q300). "Low"=While transmitting.

[†]Available for the IC-91AD or when UT-121 is installed into the IC-91A.

SECTION 6 ADJUSTMENT PROCEDURES

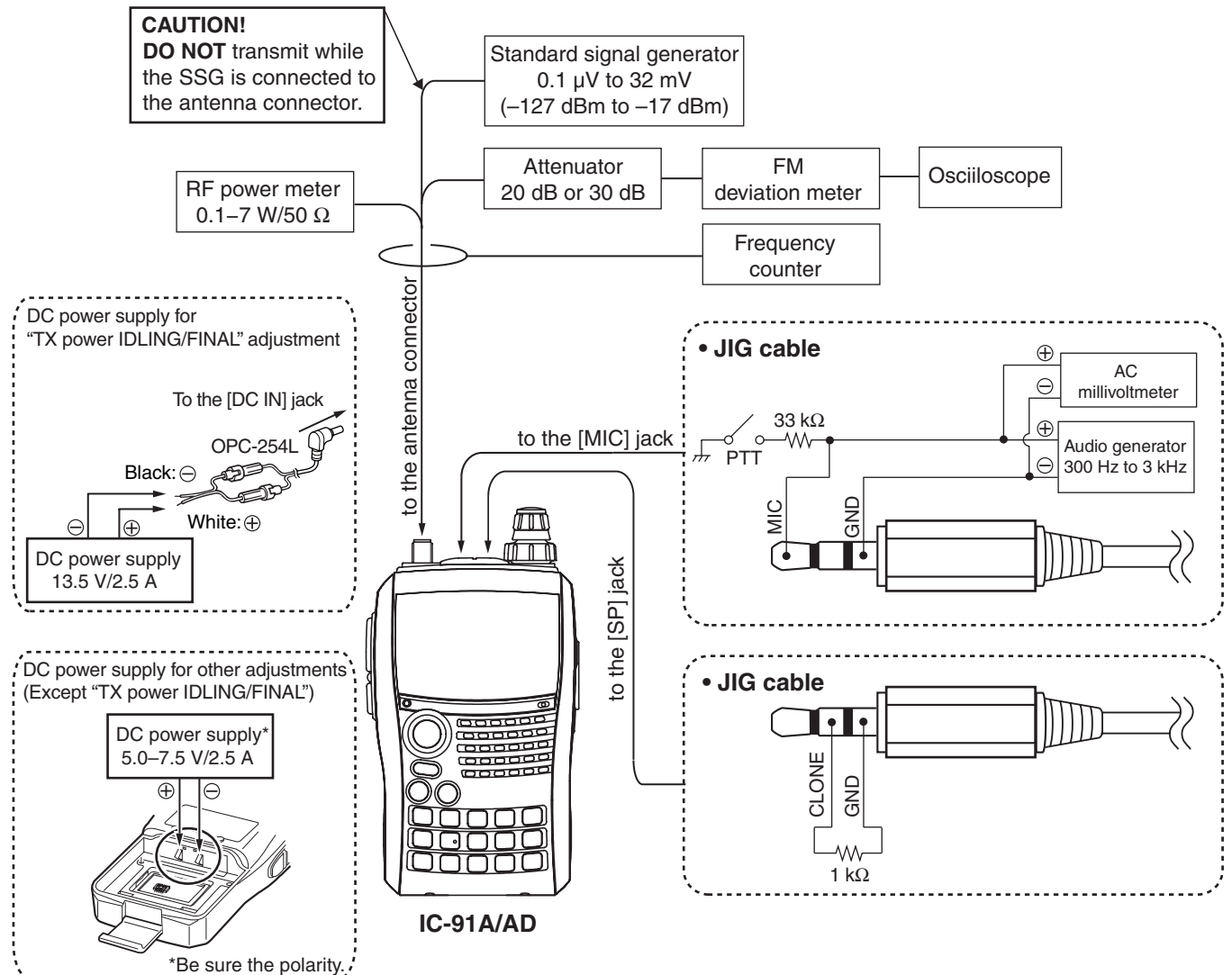
6-1 PREPARATION

When adjusting IC-91A/AD, these test equipments, OPC-254L and the JIG cables (see the illust below) are required.

REQUIRED TEST EQUIPMENTS

EQUIPMENT	GRADE AND RANGE	EQUIPMENT	GRADE AND RANGE
DC power supply	Output voltage : 5.0–13.5 V DC Current capacity : More than 2.5 A	Standard signal generator (SSG)	Frequency range : 0.1–1800 MHz Output level : 0.1 μ V to 32 mV (–127 to –17 dBm)
RF power meter (terminated type)	Measuring range : 0.1–7 W Frequency range : 100–500 MHz Impedance : 50 Ω SWR : Less than 1.2 : 1		
Frequency counter	Frequency range : 0.1–500 MHz Frequency accuracy : ± 1 ppm or better Sensitivity : 100 mV or better	Oscilloscope	Frequency range : DC–20 MHz Measuring range : 0.01–20 V
FM deviation meter	Frequency range : 30–500 MHz Measuring range : 0 to ± 10 kHz	AC millivoltmeter	Measuring range : 10 mV to 10 V
Audio generator	Frequency range : 300–3000 Hz Output level : 1–500 mV (–47 to 7 dBm)	External speaker	Input impedance : 8 Ω Capacity : More than 50 mW
		Attenuator	Power attenuation : 20 or 30 dB Capacity : More than 7 W

CONNECTION

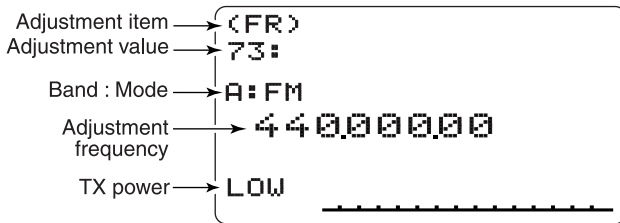


CAUTION!: BACK UP the originally programmed memory data in the transceiver before starting the adjustment.
There is possibility of losing original memory data when the adjustment is finished.

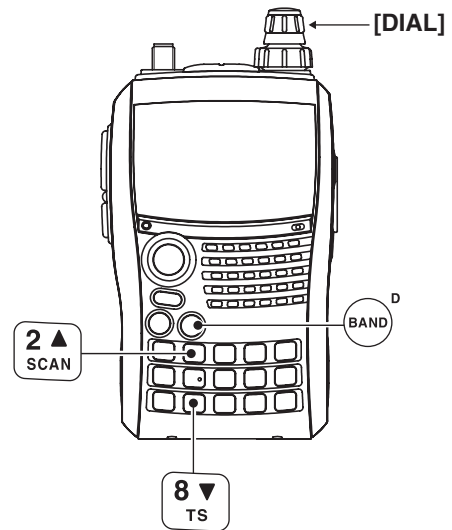
■ ENTERING ADJUSTMENT MODE


- ① Turn the power OFF.
- ② Connect the JIG cables (see page 6-1) to the [SP] and [MIC] jacks.
- ③ While pushing [SQL] and [8] keys, turn the power ON.

During adjustment mode, the function display shows the adjustment item, frequency, etc. as below.



■ KEY ASSIGNMENTS FOR ADJUSTMENT MODE



- **2 ▲ SCAN** : Selects the next adjustment item.
- **8 ▼ TS** : Selects the previous adjustment item.
-  : Adjusts the value for the item manually.
- **BAND^D** : Stores the set value.
Adjusts the value for the item automatically.

NOTE: The set value storing cannot be performed during transmit. Release PTT switch to return to receive first, then push this key to store the set value.

■ QUITTING ADJUSTMENT MODE

- While pushing [VFO], [MR] and [BAND] keys, turn the power OFF.

6-2 TRANSMITTER ADJUSTMENT

- Set the specified value using [DIAL], and push [BAND] to store the set value. Then push [2] to move to the next adjustment item.
- NOTE:** “REFERENCE FREQUENCY” should be adjusted before “FM DEVIATION” and “RXBPF.” Otherwise, these adjustments will not be adjusted properly.

ADJUSTMENT ITEM		ADJUSTMENT CONDITION	VALUE
REFERENCE FREQUENCY [Fr] (FR)	1	<ul style="list-style-type: none"> • Connect an RF power meter to the antenna connector. • Loosely couple a frequency counter to the antenna connector. • Transmitting 	440.000 MHz
TX POWER IDLING 5.0 V (VHF) [id] (id)	1	<ul style="list-style-type: none"> • Power supply voltage : 5.0 V • Adjustment value of (Po): 00 • Connect an RF power meter to the antenna connector. • Transmitting 	10 mW
	(UHF)	2	
FINAL 5.0V (VHF) [Po] (Po)	1	<ul style="list-style-type: none"> • Transmitting 	0.1 W
	(UHF)	2	
TX POWER IDLING 7.4V [Id] (Id) (VHF)	1	<ul style="list-style-type: none"> • Power supply voltage : 7.4 V • Adjustment value of (Po): 00 • Transmitting 	[HIGH]: 5.0 W [LOW]: 40–100 mW
	(UHF)	2	
NOTE: Take notes of this adjustment value. (This value is used as reference for “TX POWER IDLING 13.5V” adjustment)			
FINAL 7.4V [Po] (Po) (VHF)	1	<ul style="list-style-type: none"> • Transmitting 	[HIGH]: 5 W [LOW]: 0.5 W
	(UHF)	2	
TX POWER IDLING 13.5V [Id] (Id) (VHF)	1	<ul style="list-style-type: none"> • Power supply voltage : 13.5 V • Adjustment value of (Po): 00 • Transmitting 	[HIGH]: • Adjustment value: 58 or • TX pwr.: 3.0–4.0 W ⁽¹⁾ [LOW]: 40–100 mW
	(UHF)	2	
FINAL 13.5V [Po] (Po) (VHF)	1	<ul style="list-style-type: none"> • Transmitting 	[HIGH]: 4.6–5.2 W [LOW]: 0.4–0.6 W
	(UHF)	2	

(1); Adjustment value for the case If the output power exceeds 4.0 W.

(2); Set to the value in which 3 is subtracted from the [Id] value set by “TX POWER IDLING 7.4 V (UHF)/[LOW].“

6-2 TRANSMITTER ADJUSTMENT (continued)

• Set the specified value using [DIAL], and push [BAND] to store the set value. Then push [2] to move to the next adjustment item.

ADJUSTMENT ITEM		ADJUSTMENT CONDITION	VALUE
FM DEVIATION [FMV] (FMU) (VHF)	1	<ul style="list-style-type: none"> • Connect an audio generator to the [MIC] jack through the JIG cable (see the page 6-1) and set as; Frequency : 1.0 kHz Level : 90 mVrms • Connect a modulation analyzer to the antenna connector through an attenuator and set as; LPF : 20 kHz HPF : OFF De-emphasis : OFF Detector : (P-P)/2 • Transmitting 	±4.4 kHz
(UHF)	2	• Transmitting	
[FMR] (FMR) (VHF)	3	<ul style="list-style-type: none"> • Set the audio generator to the [MIC] jack through the JIG cable (see the page 5-1) and as; Frequency : 300 Hz Level : 90 mVrms • Transmitting 	
(UHF)	4	• Transmitting	
DIGITAL DEVIATION† [DVV] (DUU) (VHF)	1	<ul style="list-style-type: none"> • Connect a modulation analyzer to the antenna connector through an attenuator and set as; LPF : 20 kHz HPF : OFF De-emphasis : OFF Detector : (P-P)/2 • Transmitting 	±1.2 kHz
(UHF)	2	• Transmitting	
[DVR] (DUR) (VHF)	3	• Transmitting	minimum deviation
(UHF)	4	• Transmitting	
DTMF, CTCSS/DTCS DEVIATION [DTMF] (DTMF) (DTMF, VHF)	1	<ul style="list-style-type: none"> • Connect a modulation analyzer to the antenna connector through an attenuator and set as; LPF : 20 kHz HPF : OFF De-emphasis : OFF Detector : (P-P)/2 • No audio signals are applied to [MIC] jack. • Transmitting 	±3.5 kHz
(DTMF, UHF)	2	• Transmitting	
[CT] (CT) (CTCSS, VHF)	3	• Transmitting	±0.75 kHz
(CTCSS, UHF)	4	• Transmitting	
[DT] (DT) (DTCS, VHF)	5	• Transmitting	
(DTCS, UHF)	6	• Transmitting	

*Displayed on the function display.

†The output level of the standard signal generator (SSG) is indicated as the SSG's open circuit.

‡Necessary for the IC-91AD or when UT-121 is installed into the IC-91A.

6-3 RECEIVER ADJUSTMENT

- Set the specified value using [DIAL], and push [BAND] to store the set value. Then push [2] to move to the next adjustment item.

ADJUSTMENT ITEM		ADJUSTMENT CONDITION	VALUE
RXBPF (A_BAND) [TrA] (TrA)		• Connect an SSG to the ANT connector and set as; Dev. : 3.5 kHz AF : 1 kHz	Push [BAND] (Automatic adjustment)
30–118 MHz bands	1	• Set the SSG as; Frequency : Specified frequencies* Level : 0 dBμ† (–107 dBm) • Receiving	
118–174 MHz bands	2	• Set the SSG as; Frequency : Specified frequencies* Level : –3 dBμ† (–110 dBm) • Receiving	
174–350 MHz bands	3	• Set the SSG as; Frequency : Specified frequencies* Level : 0 dBμ† (–107 dBm) • Receiving	
350–470 MHz bands	4	• Set the SSG as; Frequency : Specified frequencies* Level : –3 dBμ† (–110dBm) • Receiving	
470–999.9 MHz bands	5	• Set the SSG as; Frequency : Specified frequencies* Level : 0 dBμ† (–107 dBm) • Receiving	
RXBPF (B_BAND) [TrB] (TrB)		• Connect an SSG to the ANT connector and set as; Dev. : 3.5 kHz AF : 1 kHz	Push [BAND] (Automatic adjustment)
118–470 MHz bands	1	• Set the SSG as; Frequency : Specified frequencies* Level : –3 dBμ† (–110 dBm) • Receiving	
FM S-METER (A_BAND) [S3A] (S3A)		• Connect an SSG to the ANT connector and set as; Dev. : 3.5 kHz AF : 1 kHz	
1.0–145 MHz bands	1	• Set the SSG as; Frequency : Specified frequencies* Level : –6 dBμ† (–113 dBm) • Receiving	Push [BAND] (Automatic adjustment)
220–305.1 MHz bands	2	• Set the SSG as; Frequency : Specified frequencies* Level : –4 dBμ† (–111 dBm) • Receiving	
430 MHz band	3	• Set the SSG as; Frequency : Specified frequency* Level : –6 dBμ† (–113 dBm) • Receiving	
530–800 MHz bands	4	• Set the SSG as; Frequency : Specified frequencies* Level : –2 dBμ† (–109 dBm) • Receiving	
FM S-METER (B_BAND) [S3B] (S3B) (VHF)	1	• Set the SSG as; Frequency : Specified frequency* Level : –6 dBμ† (–113 dBm) • Receiving	
(UHF)	2	• Set the SSG as; Frequency : Specified frequency* Level : –6 dBμ† (–113 dBm) • Receiving	

*Displayed on the function display.

†The output level of the standard signal generator (SSG) is indicated as the SSG's open circuit.

6-4 RECEIVER ADJUSTMENT (continued)

ADJUSTMENT ITEM		ADJUSTMENT CONDITION	VALUE
WFM S-METER [S3A] (S3A)		<ul style="list-style-type: none"> • Connect an SSG to the ANT connector and set as; Dev. : 3.5 kHz AF : 1 kHz 	Push [BAND] (Automatic adjustment)
1.0–145 MHz bands	1	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequencies* Level : 0 dBμ[†] (–107 dBm) • Receiving 	
15.1–40.1 MHz bands	2	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequencies* Level : –3 dBμ[†] (–110 dBm) • Receiving 	
60.1 MHz band	3	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : –4 dBμ[†] (–111 dBm) • Receiving 	
87.5 MHz band	4	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : 2 dBμ[†] (–105 dBm) • Receiving 	
107.9 MHz band	5	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : –1 dBμ[†] (–108 dBm) • Receiving 	
145.1 MHz band	6	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : 0 dBμ[†] (–107 dBm) • Receiving 	
220 MHz band	7	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : –2 dBμ[†] (–109 dBm) • Receiving 	
305 MHz band	8	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : 5 dBμ[†] (–102 dBm) • Receiving 	
435 MHz band	9	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : –4 dBμ[†] (–111 dBm) • Receiving 	
530 MHz band	10	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : 10 dBμ[†] (–97 dBm) • Receiving 	
800 MHz band	11	<ul style="list-style-type: none"> • Set the SSG as; Frequency : Specified frequency* Level : 6 dBμ[†] (–101 dBm) • Receiving 	

*Displayed on the function display.

†The output level of the standard signal generator (SSG) is indicated as the SSG's open circuit.

• **ADJUSTMENT FREQUENCY LIST**

NOTE: Adjustment frequency may differ depending on the transceiver.

ADJUSTMENT ITEM		FREQUENCY (MHz)	
REFERENCE FREQUENCY [FR]		440.0	
IDLING [Id] 5.0 V	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
FINAL [Po] 5.0 V	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
IDLING [Id] 7.4 V	HIGH	VHF	(146.0)*
		UHF	(445.0)*
	LOW	VHF	(146.0)*
		UHF	(445.0)*
FINAL [Po] 7.4 V	HIGH	VHF	(146.0)*
		UHF	(445.0)*
	LOW	VHF	(146.0)*
		UHF	(445.0)*
IDLING [Id] 13.5 V	HIGH	VHF	(146.0)*
		UHF	(445.0)*
	LOW	VHF	(146.0)*
		UHF	(445.0)*
FINAL [Po] 13.5 V	HIGH	VHF	(146.0)*
		UHF	(445.0)*
	LOW	VHF	(146.0)*
		UHF	(445.0)*
FMDEVIATION [FMV]	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
[FMR]	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
DIGITAL DEVIATION [DVV]†	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
[DVR]†	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
TONE DEVIATION [DTMF]	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
[CT]	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*
[DTMF]	VHF	145.0	(146.0)*
	UHF	435.0	(445.0)*

*[USA] version.

†Necessary for the IC-91AD or when UT-121 is installed into the IC-91A.

ADJUSTMENT ITEM	FREQUENCY (MHz)	
RXBPF (A_BAND) [TrA]	30.1	
	49.9	
	50.1	
	75.9	
	76.1	
	90.1	
	117.9	
	118.1	
	146.1	
	173.9	
	174.1	
	222.1	
	159.9	
	260.1	
	305.1	
	149.1	
	350.1	
	349.1	
	350.1	
	RXBPF (B_BAND) [TrB]	440.1
469.9		
470.1		
535.1		
599.9		
600.1		
800.1		
999.9		
118.1		
146.1		
FM/WFM S-METER (A_BAND) [S3A]	173.9	
	350.1	
	440.1	
	469.9	
	1.01	
	15.1	
FM S-METER (B_BAND) [S3B]	40.1	
	60.1	
	87.5	
	145.1	
	220.1	
	305.1	
	435.1	
	535.1	
	800.1	
	FM S-METER (B_BAND) [S3B]	VHF
UHF		435.1

[LOGIC UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Rows include items like R323, R324, R325, etc., up to R605 and C1 through C69.

[LOGIC UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Rows include items like C70, C100, C101, etc., up to C345.

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side) S.=Surface mount

[LOGIC UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION	
C346	4550007730	S.TAN TEESVJ 0J 106M8R	B	18.7/46.7	
C347	4030017460	S.CER ECJ0EB1E102K	B	21.2/39.8	
C348	4030018860	S.CER ECJ0EB0J105K	B	17.6/46.2	
C349	4030017460	S.CER ECJ0EB1E102K	B	21.5/44.3	
C350	4030017460	S.CER ECJ0EB1E102K	B	20.6/44.3	
C351	4550007730	S.TAN TEESVJ 0J 106M8R	B	46.9/51	
C352	4030016960	S.CER ECJ0EB1C183K	B	24.4/17.9	
C353	4030018860	S.CER ECJ0EB0J105K	T	42.2/51	
C400	4030017460	S.CER ECJ0EB1E102K	B	3.7/79.1	
C505	4550007720	S.TAN TEESVP 0G 476M8R	B	8.6/40.4	
C506	4030018860	S.CER ECJ0EB0J105K	B	7.5/35.7	
C509	4030018860	S.CER ECJ0EB0J105K	B	7.5/37.4	
C510	4030016930	S.CER ECJ0EB1A104K	B	39.8/58.4	
C550	4030017780	S.CER ECJ0EB1E472K	B	17.9/49.5	
C551	4030016930	S.CER ECJ0EB1A104K	B	18.8/49.5	
C553	4550002960	S.TAN TEESVA 1C 155M8R	B	19.7/51.4	
C554	4510008500	S.ELE EEEE1CA101WP	B	13.6/49.6	
C555	4030016950	S.CER ECJ0EB1A473K	B	15.8/60.2	
C556	4030017460	S.CER ECJ0EB1E102K	B	10.9/45.6	
C655	4510005590	S.ELE ECEV0JA331P [USA], [USA-1], [TPE], [EXP-2]	B	5.6/47.3	
	4510009020	S.ELE EEEE0JA331P Except [USA], [USA-1], [TPE], [EXP-2]	B	5.6/47.3	
J50	6510024580	S.CNR HSJ1621-019011	B	53.3/70.5	
J200	6510023390	S.CNR 27FLZ-SM1-TB [USA], [USA-1], [TPE], [EXP-2]	B	27/62.1	
	6510023391	S.CNR 27FLZ-SM2-TB (LF) (SN) Except [USA], [USA-1], [TPE], [EXP-2]	B		
27/62.1				B	
J400	6510025540	S.CNR AXK724127G	B	33.9/18.9	
J401	6510025080	S.CNR 04-6240-040-001-800+	B	7.5/67.8	
DS1	5030002880	LCD HLM7972-010100			
DS201	5040002670	S.LED CL-165HR/YG	T	5.1/55.6	
DS202	5040002930	S.LED SML-512MW T86	T	11.1/38	
DS203	5040002930	S.LED SML-512MW T86	T	13.1/23	
DS204	5040002930	S.LED SML-512MW T86	T	22.7/14.6	
DS205	5040002930	S.LED SML-512MW T86	T	39.9/14.8	
DS206	5040002930	S.LED SML-512MW T86	T	14.1/7.8	
DS207	5040002930	S.LED SML-512MW T86	T	31.3/7.3	
DS208	5040002930	S.LED SML-512MW T86	T	48.5/8.3	
DS209	5040002930	S.LED SML-512MW T86	T	5.1/49.5	
DS210	5040002960	S.LED SML-A12MT T86	T	18/87	
DS211	5040002960	S.LED SML-A12MT T86	T	36/87	
MC300	7700002310	MIC EM-140			
SP1	2510000840	SP CS028014-12			
W1	8900010520	CBL OPC-1033			
W2	7120000470	JMP ERDS2T0			
W3	7120000470	JMP ERDS2T0			
EP50	6910015370	S.BEA ACZ1005Y-102-T	B	48.3/81.4	
EP51	6910015370	S.BEA ACZ1005Y-102-T	B	44.4/76.7	
EP52	6910015370	S.BEA ACZ1005Y-102-T	T	51.2/68.6	
EP53	6910015370	S.BEA ACZ1005Y-102-T	T	52.1/66.9	
EP100	6910015370	S.BEA ACZ1005Y-102-T	B	43.4/6.4	
EP101	6910015370	S.BEA ACZ1005Y-102-T	B	32.6/4	

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
IC50	1110005230	S.IC μPC2757TB-E3	T	24.3/17.8
IC100	1110003201	S.IC TA31136FNG (EL)	B	47.3/27.4
IC150	1110005330	S.IC NJM12904V-TE1	B	18.3/29.3
IC200	1110003201	S.IC TA31136FNG (EL)	B	41.7/27.4
IC250	1110005330	S.IC NJM12904V-TE1	B	13.4/39.9
IC400	1130011671	S.IC MB15E03SLPFV1-G-BND-E1	T	31.9/6.9
IC500	1130011770	S.IC CD4066BPWR	B	18.4/40.5
IC501	1110005330	S.IC NJM12904V-TE1	B	18.3/20.3
IC700	1130011770	S.IC CD4066BPWR	T	8.9/40
IC701	1130011760	S.IC CD4094BPWR	B	12.5/9
IC702	1130011760	S.IC CD4094BPWR	T	12.5/9.1
IC703	1130011770	S.IC CD4066BPWR	T	8.9/45.5
Q1	1530002280	S.TR 2SC4081 T106 S	T	23.6/20.8
Q2	1530003260	S.TR 2SC5006-T1	T	26.6/27.5
Q100	1530002601	S.TR 2SC4215-O (TE85R)	B	47.2/37.9
Q101	1590003390	S.TR UNR9215J-(TX)	T	33.9/39.7
Q102	1590002380	S.TR XP1115 (TX)	T	36.5/40.3
Q103	1590001190	S.TR XP6501-(TX) AB	B	27.8/34.1
Q104	1590001190	S.TR XP6501-(TX) AB	B	25.5/38.9
Q105	1590003250	S.TR UNR9115J-(TX)	T	24.7/38.4
Q150	1590003250	S.TR UNR9115J-(TX)	T	20.9/38.9
Q200	1530002601	S.TR 2SC4215-O (TE85R)	B	39.4/33.4
Q201	1530002280	S.TR 2SC4081 T106 S	T	33.1/32.2
Q202	1590001190	S.TR XP6501-(TX) AB	T	16.2/18.5
Q203	1590001190	S.TR XP6501-(TX) AB	T	17.3/23.6
Q204	1590003250	S.TR UNR9115J-(TX)	T	17.5/15.9
Q205	1590003250	S.TR UNR9115J-(TX)	T	31.7/35.6
Q206	1590003390	S.TR UNR9215J-(TX)	T	35.7/32.2
Q250	1590003250	S.TR UNR9115J-(TX)	T	17.5/13.5
Q300	1590003250	S.TR UNR9115J-(TX)	T	31.4/27.4
Q301	1530003560	S.TR 2SC5195-T1	T	30.7/22.1
Q350	1590003250	S.TR UNR9115J-(TX)	T	33.4/26.4
Q351	1530003560	S.TR 2SC5195-T1	T	34.3/18.1
Q353	1530003260	S.TR 2SC5006-T1	T	30.1/16.7
Q450	1530002601	S.TR 2SC4215-O (TE85R)	B	36.6/7
Q451	1530002601	S.TR 2SC4215-O (TE85R)	B	41.7/9.2
Q452	1530002601	S.TR 2SC4215-O (TE85R)	B	45.7/7.2
Q453	1590001980	S.TR XP4315 (TX)	T	36.4/42.8
Q500	1590003390	S.TR UNR9215J-(TX)	B	14.3/17.6
Q550	1590001190	S.TR XP6501-(TX) AB	T	18.2/32.2
Q552	1530002280	S.TR 2SC4081 T106 S	T	15.1/36.3
Q600	1520000460	S.TR 2SB1132 T100 R	B	4/37.2
Q601	1590001170	S.TR XP1501-(TX) AB	B	6.1/41.1
Q602	1520000460	S.TR 2SB1132 T100 R	B	4.5/6.1
Q603	1590001170	S.TR XP1501-(TX) AB	T	5.3/6.9
Q604	1590002380	S.TR XP1115 (TX)	B	11.2/25.8
Q650	1590003280	S.TR UNR9211J-(TX)	T	19.7/5
Q651	1590001170	S.TR XP1501-(TX) AB	T	22.5/6.8
Q652	1520000460	S.TR 2SB1132 T100 R	T	26.6/8.4
D10	1750001210	S.DIO HSB88ASTR-E	T	25.3/23.4
D100	1790001240	S.DIO MA2S728-(TX)	T	26.3/35.3
D104	1790001240	S.DIO MA2S728-(TX)	B	24.6/35.9
D105	1790001250	S.DIO MA2S111-(TX)	B	22.1/36.3
D200	1790001240	S.DIO MA2S728-(TX)	B	35.5/28.8
D204	1790001240	S.DIO MA2S728-(TX)	T	19.5/20.4
D205	1790001250	S.DIO MA2S111-(TX)	T	17.2/27.3
D300	1750001270	S.VCP 1SV325 (TPH3)	T	28.2/24.9
D301	1750000771	S.VCP HVC376BTRF-E	T	28.7/23.7
D350	1750000721	S.VCP HVC375BTRF-E	T	32.3/21.1
D351	1790000850	S.DIO MA132WK (TX)	T	28.1/19.8
D352	1750000721	S.VCP HVC375BTRF-E	T	34/21.9
D451	1790001240	S.DIO MA2S728-(TX)	T	48.2/5.7
FI1	2030000450	S.MLH FL-394 (61.65 MHz)	B	23.6/27.6
FI2	2020001800	S.CER SFECV13M3DA0001-B0	B	33.1/32.5
FI50	2030000150	S.MLH FL-335 (46.350 MHz)	B	30.7/24
FI100	2020001270	CER CFWLB450KE2A-B0		
FI200	2020001270	CER CFWLB450KE2A-B0		
FI201	2020002180	S.CER CFWKA450KHFA-R0	B	33.2/14.5
X100	6070000190	S.DCR CDBC450KCA24-R0	T	47.5/35.8
X200	6070000190	S.DCR CDBC450KCA24-R0	T	41/35.8
X450	6050012380	S.XTL CR-826 (15.3 MHz)	T	46.4/8.3
L1	6200009101	S.COL ELJRE R18GFA	T	25.2/25.4
L10	6200011031	S.COL ELJRF R10JFB	B	22.3/18.6
L30	6200010380	S.COL ELJRE R15J-F3 [USA-1], [EXP-2]	B	23.8/17
	6200010381	S.COL ELJRE R15JFA Except [USA-1], [EXP-2]	B	23.8/17
L50	6200011021	S.COL ELJRF 82NJFB	T	26.9/16.2
L100	6200003540	S.COL MLF1608D R22K-T	B	48.7/35.1
L101	6200003131	S.COL NLV32T-120J	T	48.6/40.8
L102	6200003540	S.COL MLF1608D R22K-T	B	50.6/34.2
L103	6200003540	S.COL MLF1608D R22K-T	B	51.2/23.9

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains parts list for [MAIN UNIT] with items L200 through R219.

[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains parts list for [MAIN UNIT] with items R220 through R602.

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains 400 rows of equipment specifications.

[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains 200 rows of equipment specifications, including various antenna models and components.

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side) S.=Surface mount

• UT-121 (Optional product; [CODEC UNIT] for IC-91AD)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
IC1	1180002371	S.REG R1111N321B-TR-F	B	30.6/13.6
IC101	1130010920	S.IC AMBE-2020	T	25.4/10.7
IC102	1130011930	S.IC SN74LVC1G04DCKR	B	29.9/17.3
IC201	1130012511	S.IC SN74AHC2G74HDCT3	B	18.4/10.5
IC202	1110005430	S.IC CMX589AD5	T	6.3/13.4
IC203	1130011801	S.IC SN74AHC1G66HDBV3	B	6.1/11.7
IC300	1110005290	S.IC NJM2115V-TE1	B	17.5/3.5
IC301	1110005290	S.IC NJM2115V-TE1	B	5.6/16.6
IC350	1130011631	S.IC AD73311ARSZ	T	5.9/4.7
Q1	1510000581	S.TR 2SA1362-GR (TE85R F)	T	15.2/2.7
Q2	1590003280	S.TR UNR9211J-(TX)	T	18/2.2
Q50	1510000581	S.TR 2SA1362-GR (TE85R F)	B	26.1/7.9
Q51	1590001170	S.TR XP1501-(TX) AB	B	28/3.7
Q100	1530002280	S.TR 2SC4081 T106 S	B	26.3/17.3
Q200	1590003390	S.TR UNR9215J-(TX)	B	11.3/13.7
Q201	1590003390	S.TR UNR9215J-(TX)	B	9.3/13.7
Q202	1530002280	S.TR 2SC4081 T106 S	B	19.5/13.7
D100	1790001240	S.DIO MA2S728-(TX)	T	26.9/1.6
X100	6050012290	S.XTL CR-820 (16.384 MHz)	B	25.3/13.9
X200	6050012300	S.XTL CR-821 (9.8304 MHz)	B	15.7/16.6
R1	7030000180	S.RES MCR10EZHZ 22 (220)	B	26.1/11.2
R2	7030005240	S.RES ERJ2GEJ 473 X (47 k)	B	20.7/1.8
R3	7030005050	S.RES ERJ2GEJ 103 X (10 k)	T	17/3.6
R50	7030005040	S.RES ERJ2GEJ 472 X (4.7 k)	B	26.2/3.8
R100	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.3/2.4
R104	7030005120	S.RES ERJ2GEJ 102 X (1 k)	B	31.9/16
R105	7030007280	S.RES ERJ2GEJ 331 X (330)	T	12.8/5.8
R106	7030004980	S.RES ERJ2GEJ 101 X (100)	B	32.7/18.5
R107	7030005120	S.RES ERJ2GEJ 102 X (1 k)	B	28/18.5
R108	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	28.2/16.8
R109	7030004990	S.RES ERJ2GEJ 221 X (220)	B	24.1/17.5
R202	7030010040	S.RES ERJ2GEJ-JPW	B	15.2/10.1
R203	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.7/10.6
R204	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.7/11.5
R205	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.7/12.4
R206	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.7/13.3
R207	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.7/14.2
R208	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	23/4.1
R209	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	24.7/2.3
R210	7030005220	S.RES ERJ2GEJ 223 X (22 k)	T	2.5/18.6
R211	7030006610	S.RES ERJ2GEJ 394 X (390 k)	B	3.3/11.1
R212	7030005240	S.RES ERJ2GEJ 473 X (47 k)	B	9.7/11.7
R213	7030004980	S.RES ERJ2GEJ 101 X (100)	B	15.9/13
R214	7030005120	S.RES ERJ2GEJ 102 X (1 k)	B	17.5/13
R215	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	17.7/14.2
R216	7030004990	S.RES ERJ2GEJ 221 X (220)	B	22.4/13.8
R217	7030005050	S.RES ERJ2GEJ 103 X (10 k)	B	11.4/11.3
R300	7030005240	S.RES ERJ2GEJ 473 X (47 k)	B	21.8/5.7
R301	7030005310	S.RES ERJ2GEJ 124 X (120 k)	B	21.8/4
R302	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	17.9/6.2
R303	7030005240	S.RES ERJ2GEJ 473 X (47 k)	B	10.1/3.3
R304	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	11.7/2.3
R305	7310004610	S.TRI EVM-2WSX80 B15 (104)	T	13.8/18
R306	7030005060	S.RES ERJ2GEJ 333 X (33 k)	B	10.4/16.9
R307	7030005090	S.RES ERJ2GEJ 104 X (100 k)	B	8.8/18.4
R310	7030005530	S.RES ERJ2GEJ 100 X (10)	T	14.3/5
R350	7030005010	S.RES ERJ2GEJ 681 X (680)	B	1.7/3.7
R351	7030005120	S.RES ERJ2GEJ 102 X (1 k)	B	2.1/8.3
R352	7030005240	S.RES ERJ2GEJ 473 X (47 k)	B	6.9/3.8
R353	7030004980	S.RES ERJ2GEJ 101 X (100)	B	9.2/3.3
R354	7030005240	S.RES ERJ2GEJ 473 X (47 k)	T	11.3/7.1
C1	4550007680	S.TAN TEESVP 0J 226M8R	B	28.7/10
C2	4550007600	S.TAN F920J106MPABMA	B	30.6/10
C3	4030016930	S.CER ECJ0EB1A104K	B	33.2/13.3
C50	4030017460	S.CER ECJ0EB1E102K	B	26.3/2.2
C51	4030017460	S.CER ECJ0EB1E102K	B	25.3/4.7
C52	4550006930	S.TAN TEESVP 0J 225M8R	B	29.6/5.9
C53	4030017460	S.CER ECJ0EB1E102K	B	30.3/3.5
C54	4030017460	S.CER ECJ0EB1E102K	B	24.2/5.9
C100	4030016930	S.CER ECJ0EB1A104K	T	18.7/18.7
C101	4030016930	S.CER ECJ0EB1A104K	B	21.7/15.8
C102	4030016930	S.CER ECJ0EB1A104K	B	33.2/10.1
C103	4030016930	S.CER ECJ0EB1A104K	T	32.5/19.2
C104	4030016930	S.CER ECJ0EB1A104K	T	33.2/3.6
C106	4030016930	S.CER ECJ0EB1A104K	T	16.3/10.9
C107	4030016930	S.CER ECJ0EB1A104K	T	16/7.6
C108	4030016930	S.CER ECJ0EB1A104K	B	20.2/8.3
C109	4030016930	S.CER ECJ0EB1A104K	T	22.8/1.5

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C110	4030017420	S.CER ECJ0EC1H470J	B	29.1/7.3
C111	4030017460	S.CER ECJ0EB1E102K	B	22.9/16.2
C112	4030016930	S.CER ECJ0EB1A104K	B	26.4/19
C113	4030016930	S.CER ECJ0EB1A104K	B	24.1/18.4
C114	4030016930	S.CER ECJ0EB1A104K	B	27.8/14.2
C116	4030016930	S.CER ECJ0EB1A104K	B	31.8/18.5
C117	4030017460	S.CER ECJ0EB1E102K	B	24.1/16.6
C200	4030017460	S.CER ECJ0EB1E102K	B	18.2/15.9
C201	4030016930	S.CER ECJ0EB1A104K	B	16.8/14.2
C202	4030016930	S.CER ECJ0EB1A104K	B	21.2/13.5
C203	4030016930	S.CER ECJ0EB1A104K	B	13/17.5
C204	4030017460	S.CER ECJ0EB1E102K	B	17.8/8.3
C205	4030016930	S.CER ECJ0EB1A104K	T	12.1/15.7
C206	4030017030	S.CER ECJ0EB1A273K	B	2.3/18.4
C207	4030017030	S.CER ECJ0EB1A273K	T	2.5/17.7
C208	4030016930	S.CER ECJ0EB1A104K	B	2.9/12.3
C209	4030017400	S.CER ECJ0EC1H220J	B	24.7/3.2
C210	4030018860	S.CER ECJ0EB0J105K	T	11.2/15.7
C211	4030017460	S.CER ECJ0EB1E102K	B	11.3/15.2
C212	4030017760	S.CER ECJ0EB1H222K	T	11.1/18.8
C213	4030016930	S.CER ECJ0EB1A104K	B	5.2/13.9
C214	4030017460	S.CER ECJ0EB1E102K	B	7.5/13.9
C216	4030017460	S.CER ECJ0EB1E102K	B	19.1/15.9
C300	4030016930	S.CER ECJ0EB1A104K	B	23/2.3
C301	4030018860	S.CER ECJ0EB0J105K	B	18.9/7.1
C302	4030016930	S.CER ECJ0EB1A104K	B	10/4.9
C303	4030016930	S.CER ECJ0EB1A104K	B	9.9/15.7
C304	4030017490	S.CER C1608 JB 1A 105K-T	B	2.2/14
C310	4550007730	S.TAN TEESVP 0J 106M8R	B	3/5.9
C350	4030016930	S.CER ECJ0EB1A104K	B	2.1/9.2
C351	4030016970	S.CER ECJ0EB1C223K	B	1.7/4.6
C352	4030016950	S.CER ECJ0EB1A473K	B	8.7/4.9
C353	4030016930	S.CER ECJ0EB1A104K	B	2.8/2.8
C354	4030016930	S.CER ECJ0EB1A104K	B	2.8/1.9
C355	4030017420	S.CER ECJ0EC1H470J	T	13/2.7
C356	4550006930	S.TAN TEESVP 0J 225M8R	B	7.2/2.4
C357	4030016930	S.CER ECJ0EB1A104K	B	9.6/1.8
C358	4030016930	S.CER ECJ0EB1A104K	B	4.7/4.2
C360	4030017460	S.CER ECJ0EB1E102K	B	5/1.4
J1	6510025550	S.CNR AXK824125WG	B	10.8/8.2
EP1	6910015370	S.BEA ACZ1005Y-102-T	B	33.2/11.7
EP100	6910015370	S.BEA ACZ1005Y-102-T	B	33.2/15.8
EP200	6910015370	S.BEA ACZ1005Y-102-T	B	13/15.9
EP201	6910015370	S.BEA ACZ1005Y-102-T	B	15/12
EP202	6910015370	S.BEA ACZ1005Y-102-T	T	13.7/15.4
EP203	6910015370	S.BEA ACZ1005Y-102-T	B	8.8/11.7
EP300	6910015370	S.BEA ACZ1005Y-102-T	B	23/3.2
EP350	6910015370	S.BEA ACZ1005Y-102-T	B	4/6.3
EP351	6910015370	S.BEA ACZ1005Y-102-T	B	6.2/5.6
EP352	6910015370	S.BEA ACZ1005Y-102-T	T	11.3/3.4

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

SECTION 8 MECHANICAL PARTS AND DISASSEMBLY

[CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6510022671	Connector SMA-R235-1	1
S702	2250000540	Encoder TP76D96E20-21.5F-B103-2888	1
MP1	8210022430	2888 rear panel	1
MP2	8610012870	Knob N342	1
MP3	8610012890	Knob N-342 cover	1
MP4	8610012880	Knob N343	1
MP5	8930068280	2888 D-cap	1
MP6	8930068290	2888 jack cap	1
MP7	8930068300	2888 PTT rubber	1
MP8	8930068350	2888 PTT plate	1
MP9	8930069020	O-ring (BG)	1
MP10	8930068360	2888 BP plate	1
MP11	8930056310	2507 terminal holder	1
MP12	8930054371	2372 terminal spring-1	2
MP13	8310050391	2372 lock plate-1	1
MP14	8930054490	2372 shaft	1
MP15	8830000710	VR nut (G)	1
MP16	8830000880	VR nut (I)	1
MP17	8930053590	O ring (AG)	1
MP18	8930039850	Sealing washer (J)	3
MP19	8930057022	Thermally sheet (AE)-2	1
MP20	8610007510	Knob spring No.7800	1
MP21	8810008971	Screw FH BT M2 × 3.5 NI-ZC3	4
MP22	8810009511	Screw PH BT M2 × 4 NI-ZC3	2
MP23	8810008621	Screw PH BT M2 × 20 NI-ZK3	1
MP24	8810009561	Screw PH BT M2 × 6 ZK3	4
MP25	8810010091	Screw PH BT M2 × 12 NI-ZK3	2
MP26	8810000101	Screw PH M2 × 4 ZK3	1
MP27	8930069240	Insulation sheet (LI)	1
MP28	8860001410	2888 ANT rug	1
MP30	8930070220	Thermally sheet (BG)	1
MP31	8510017870	2888 PA cover	1

[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J700	6450002250	Connector HSJ1456-010320	1
J701	6450000131	Connector HSJ1102-018540	1
S700	2260002840	Switch SKHLLFA010	1
S701	2260002840	Switch SKHLLFA010	1
MP300	8510016470	2775 VCO case	1
MP301	8510016460	2775 VCO cover	1

[RF UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1000	6450000870	Connector HEC2711-01-020	1
MP100*	8410002610	2888 PA heat sink	1
MP101*	8510017610	Plate OG-542925	1
MP200*	8510017610	Plate OG-542925	1

[VCO UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MP1	8510017470	2888 VCO case	1
MP2	8930069680	Ferrite sheet (P)	1

[LOGIC UNIT]

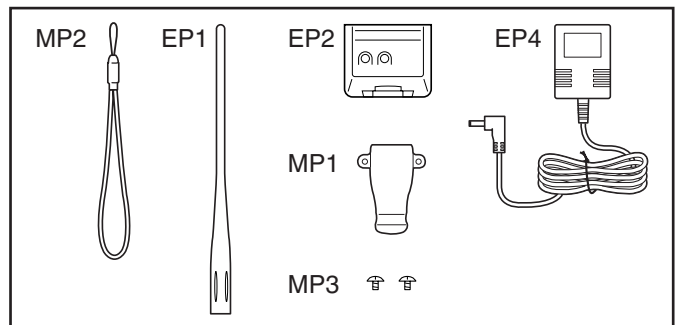
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
W1	8900010520	Cable OPC-1033	1
DS1	5030002880	LCD HLM7972-010100	1
MC300	7700002310	Microphone EM-140	1
SP1	2510000840	Speaker CS028014-12	1
MP1	8210022570	2888 front panel assembly [IC-91AD]	1
		(incl. MP7)	
	8210022960	2888 front panel (A) assembly [IC-91A]	1
		(incl. MP7)	
MP2	8310066690	2888 window plate (A) [IC-91A]	1
	8310066710	2888 window plate (C) [IC-91AD]	1
MP3	8310065440	2888 plate	1
MP4	8210022450	2888 reflector	1
MP5	8930068310	2888 key	1
MP6	8930068320	2888 main seal	1
MP8	8930048840	2135 mic sponge	1
MP9	8810009510	Screw PH BT M2 × 4 NI-ZU	6
MP10	8930068870	2888 LCD sponge	1
MP11	8930068880	2888 window sheet	1
MP12	8930054221	2372 lens-1	1
MP13	8930046020	1123 sheet (A)-1	1
MP14	8930069550	2888 rubber sheet	2
MP20*	8510017840	Plate OG-321610G	1
MP21	8930070200	2888 codec plate	1
MP22	8930070170	AL sheet (AO)	1
MP23	8930070340	2888 codec sponge	1

[CODEC UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
-	Optional product	UT-121 [IC-91AD] only	1

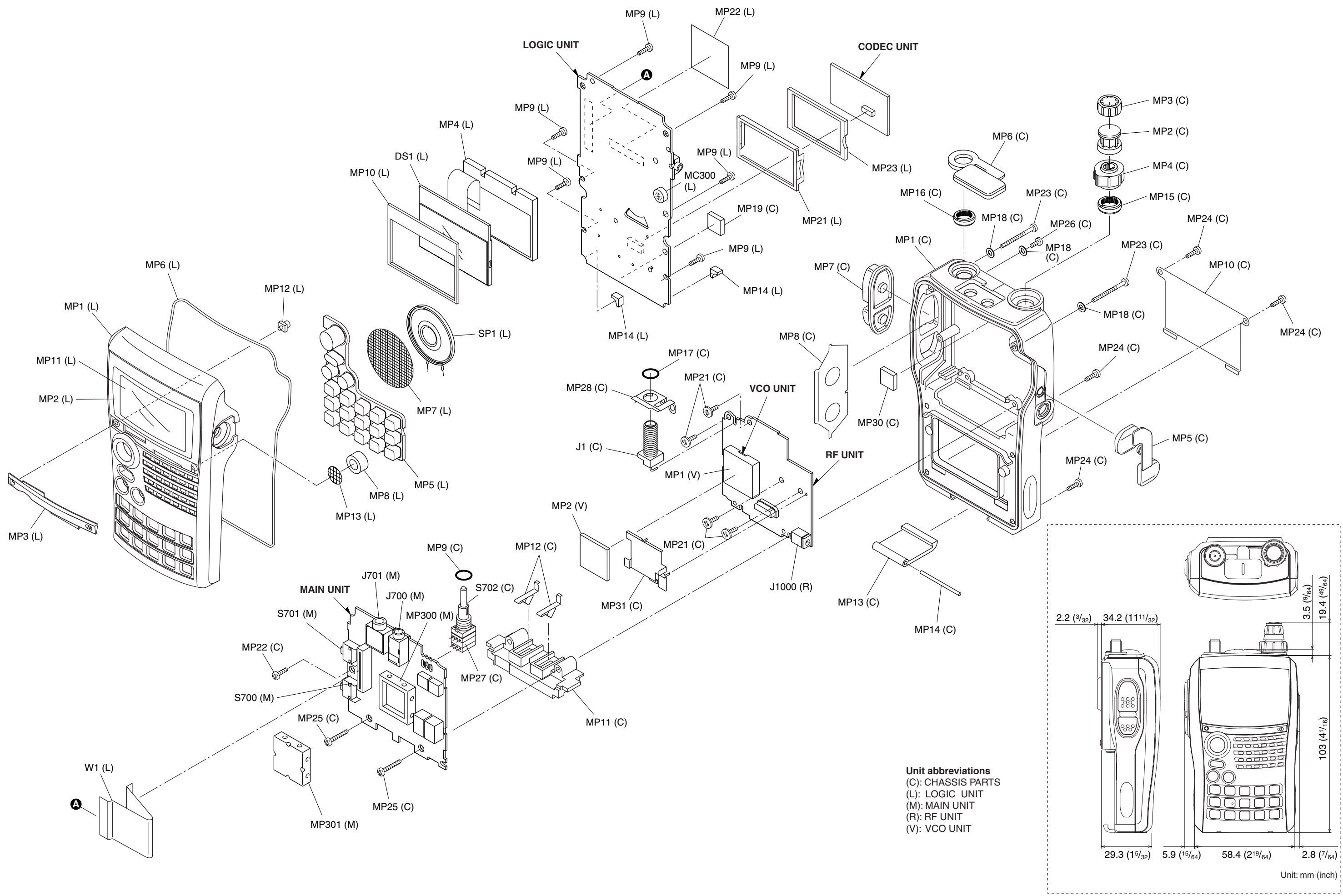
[ACCESSORIES]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
EP1	Optional product	Antenna FA-S270C	1
EP2	Optional product	Battery BP-217	1
EP4	Optional product	Charger BC-167D [KOR], [AUS], [EXP-2]	1
	Optional product	Charger BC-167A [USA], [EXP], [USA-1], [TPE], [EXP-1]	1
MP1	8930068840	2933 belt clip	1
MP2	8010018080	Strap belt HK-009	1
MP3	8810010470	Screw trass M3 × 4 SUS SSBC	2



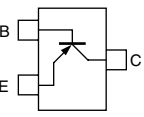
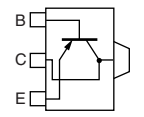
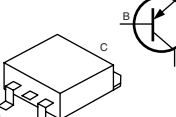
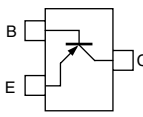
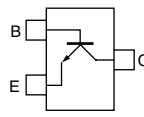
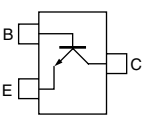
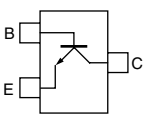
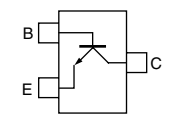
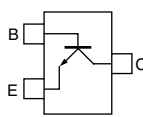
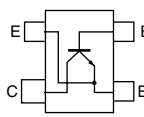
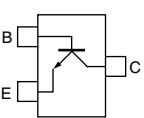
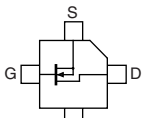
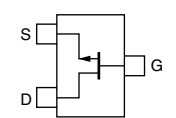
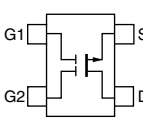
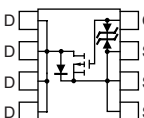
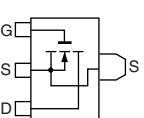
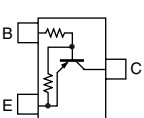
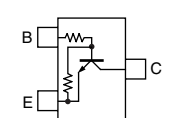
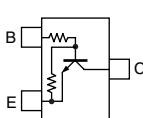
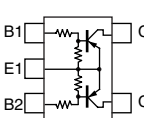
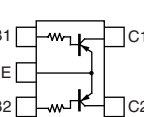
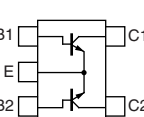
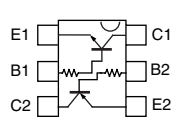
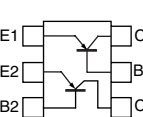
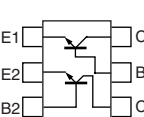
*: Refer to SECTION 10 BOARD LAYOUTS.

Screw abbreviations BT: Self-tapping FH: Flat head
 PH: Pan head ZK, NI-ZK3: Black
 NI-ZU, NI-ZC3: Nickel-Zinc
 SUS: Stainless

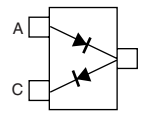
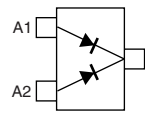
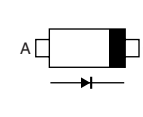
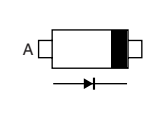
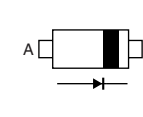
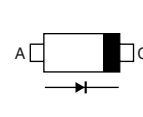
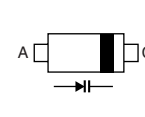
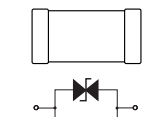
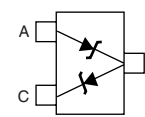
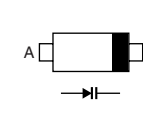
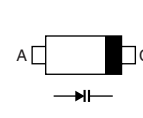
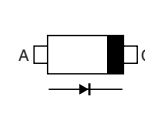
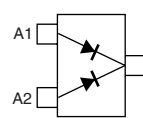
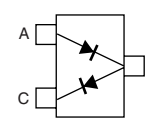
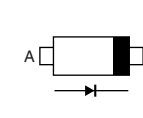
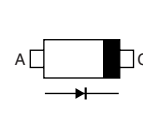
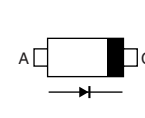
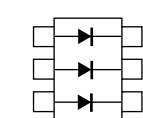
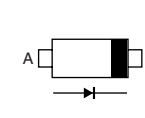
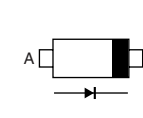
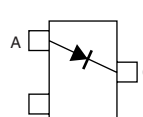


SECTION 9 SEMICONDUCTOR INFORMATION

• TRANSISTORS AND FET'S

2SA1362 GR (Symbol: AEG) 	2SB1132 T100 R (Symbol: BAR) 	2SB1201 S (Symbol: B1201) 	2SB1462 R (Symbol: AR) 	2SC4081 T106 S (Symbol: BS) 
2SC4215 O (Symbol: QO) 	2SC4226 T1 R25 (Symbol: R25) 	2SC5006 T1 (Symbol: 24) 	2SC5195 (Symbol: 88) 	2SC5624 (Symbol: VH-) 
2SD2216J (Symbol: Y) 	2SK3476 (Symbol: UC F) 	2SK880 Y (Symbol: XY) 	3SK318YB (Symbol: YB-) 	HAT1026R (Symbol: 1026) 
RD01MUS1 (Symbol: K2) 	UNR9115J (Symbol: 6E) 	UNR9211J (Symbol: 8A) 	UNR9215J (Symbol: 8E) 	XP1114 (Symbol: 7Q) 
XP1115 (Symbol: 9L) 	XP1501 AB (Symbol: 5R) 	XP4315 (Symbol: CB) 	XP6401 (Symbol: 5O) 	XP6501 AB (Symbol: 5N) 

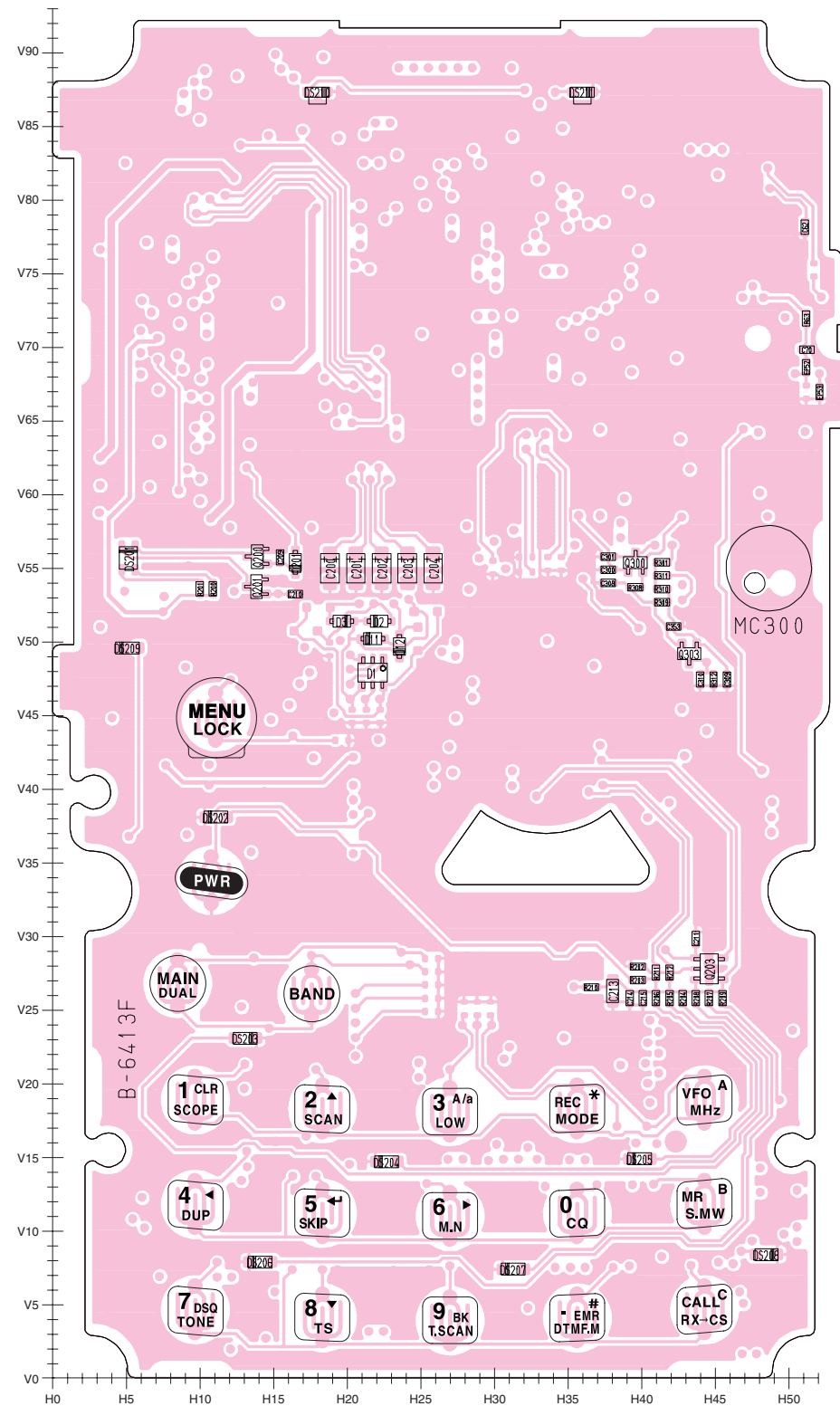
• DIODES

1SS362 (Symbol: C3) 	1SS364 (Symbol: BF) 	1SV271 (Symbol: TG) 	1SV286 (Symbol: T7) 	1SV307 (Symbol: TX) 
1SV308 (Symbol: TX) 	1SV325 (Symbol: V8) 	AVR-M1005C080MTABB (Symbol: N/Available) 	HSB88AS (Symbol: C1) 	HVC375B (Symbol: B8) 
HVC376B (Symbol: B9) 	1SS400 (Symbol: A) 	MA132WK (Symbol: MU) 	MA133 (Symbol: MP) 	MA2S077 (Symbol: S) 
MA2S111 (Symbol: A) 	MA2S728 (Symbol: B) 	MA6S121 (Symbol: M2D) 	RB060L 40 (Symbol: 36) 	RB551V-30 (Symbol: D3G) 
SB07-03C (Symbol: J) 				

SECTION 10 BOARD LAYOUTS

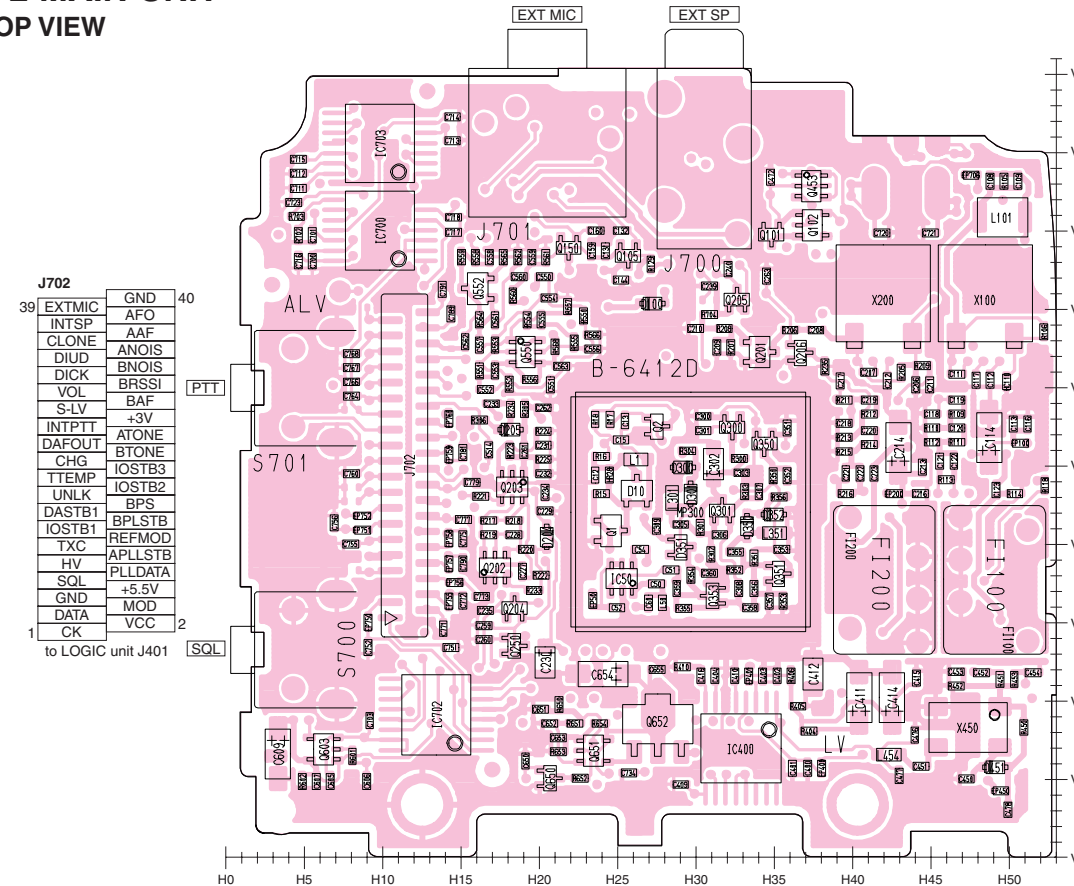
10-1 LOGIC UNIT

• TOP VIEW



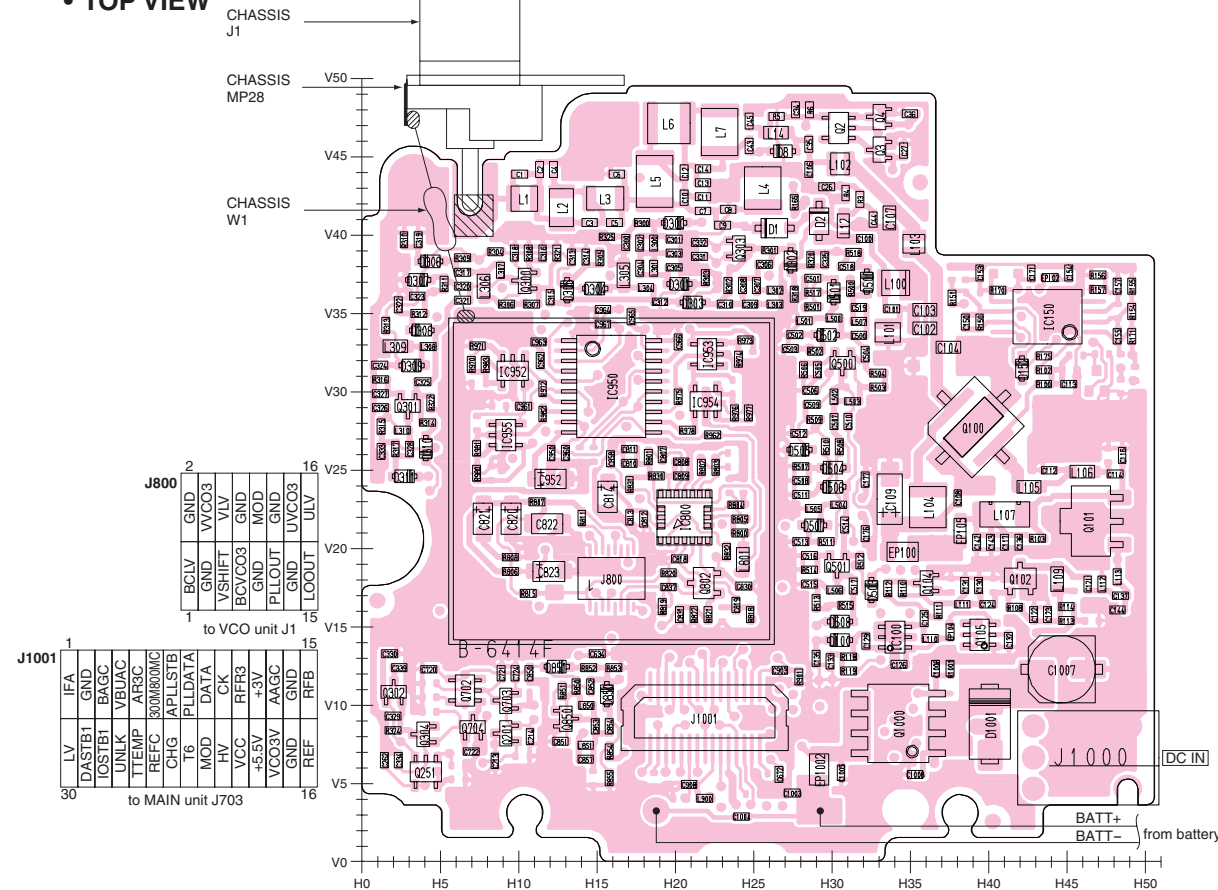
10-2 MAIN UNIT

• TOP VIEW



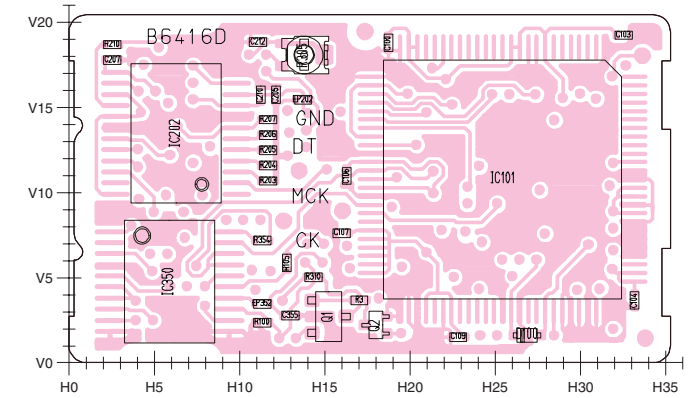
10-3 RF UNIT

• TOP VIEW



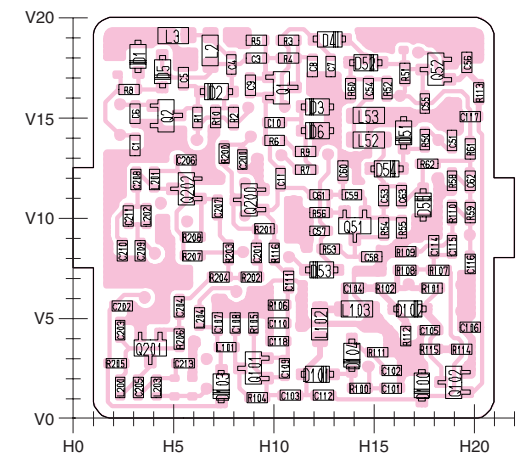
10-4 UT-121 (Optional Product ; CODEC UNIT for IC-91AD)

• TOP VIEW

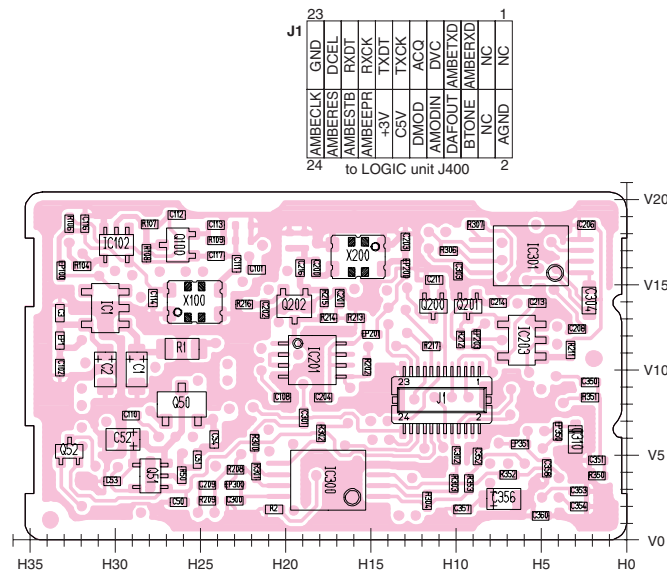


10-5 VCO UNIT

• TOP VIEW



• **BOTTOM VIEW**
(UT-21 (Optional Product ; CODEC UNIT for IC-91AD))



23	1
AMBECLK	GND
AMBERES	DCEL
AMBESTB	FXDT
AMBEPR	FXCK
+3V	TXDT
C5V	TXCK
DMOD	ACQ
AMODIN	DVC
DAFOUT	AMBERTXD
BTONE	AMBERXD
NC	NC
NC	NC
24	2
	AGND

to LOGIC unit J400

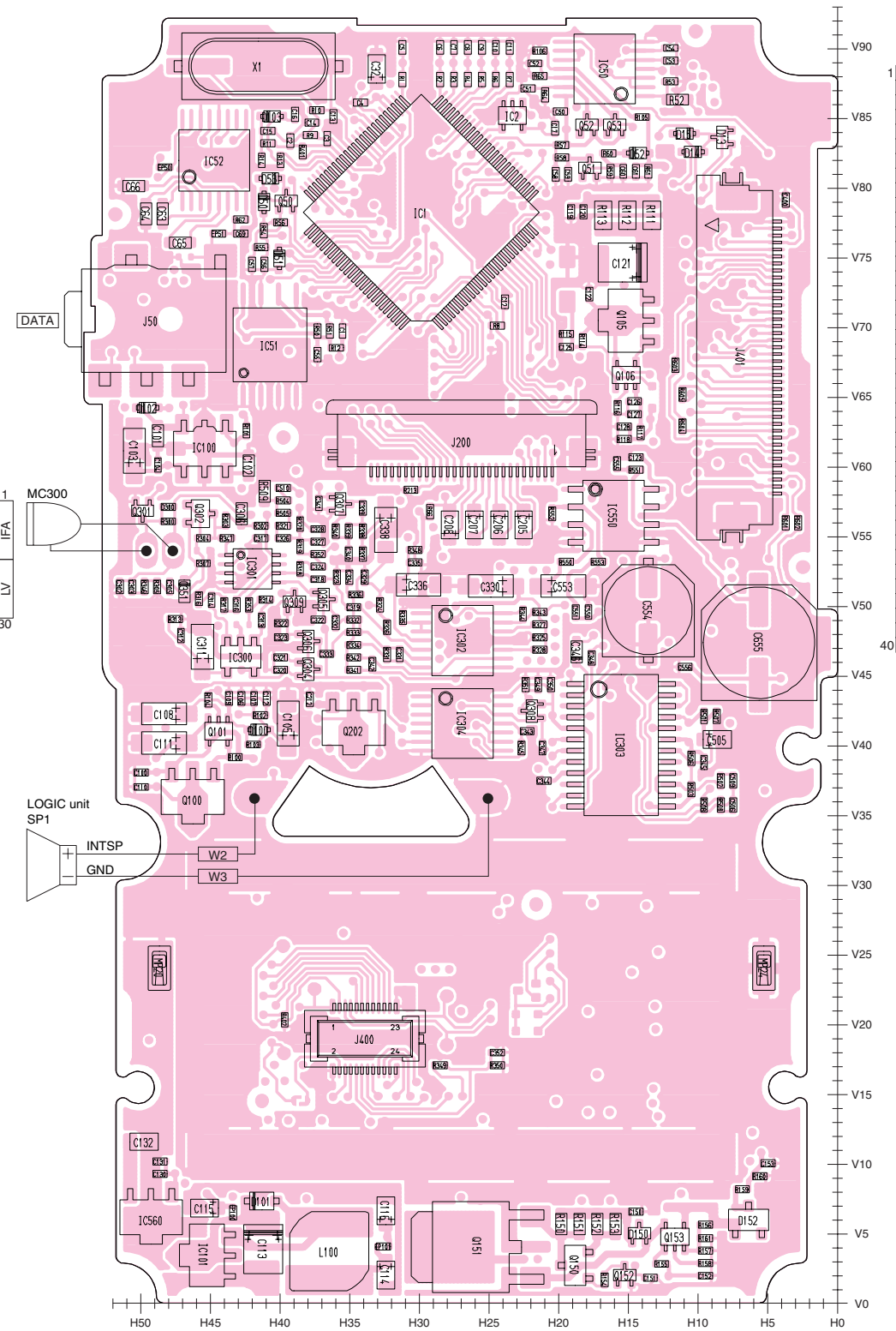
• **BOTTOM VIEW (MAIN UNIT)**



15	1
REF	RFB
GND	GND
VCO3V	AAGC
+5.5V	+3V
VCC	RFR3
HV	OK
MOD	DATA
T6	PLLDATA
CHG	APLLSTB
REFC	800M800MC
TTEMP	AR3C
UNLK	VBUAC
LOSTB1	BAGC
DASTB1	GND
LV	IFA
16	30

to RF unit J1001

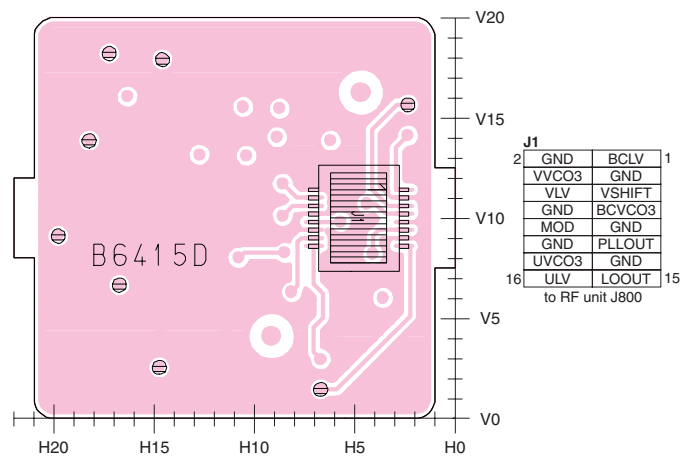
• **BOTTOM VIEW (LOGIC UNIT)**



1	1
GND	GND
EXTMIC	AFO
INTSP	INTSP
AAF	CLONE
ANOIS	ANOIS
DIUD	DIUD
BNOIS	DICK
BRSSI	BRSI
VOL	VOL
BAF	S-LV
S-LV	+3V
+3V	INTPTT
INTPTT	ATONE
ATONE	DAFOUT
DAFOUT	BTONE
BTONE	CHG
CHG	IOSTB3
IOSTB3	TTEMP
TTEMP	IOSTB2
IOSTB2	UNLK
UNLK	BPS
BPS	DASTB1
DASTB1	BPLSTB
BPLSTB	IOSTB1
IOSTB1	REFMOD
REFMOD	TXC
TXC	APLLSTB
APLLSTB	HV
HV	PLLDATA
PLLDATA	SOL
SOL	+5.5V
+5.5V	GND
GND	MOD
MOD	DATA
DATA	VCC
VCC	CK
40	2

to MAIN unit J702

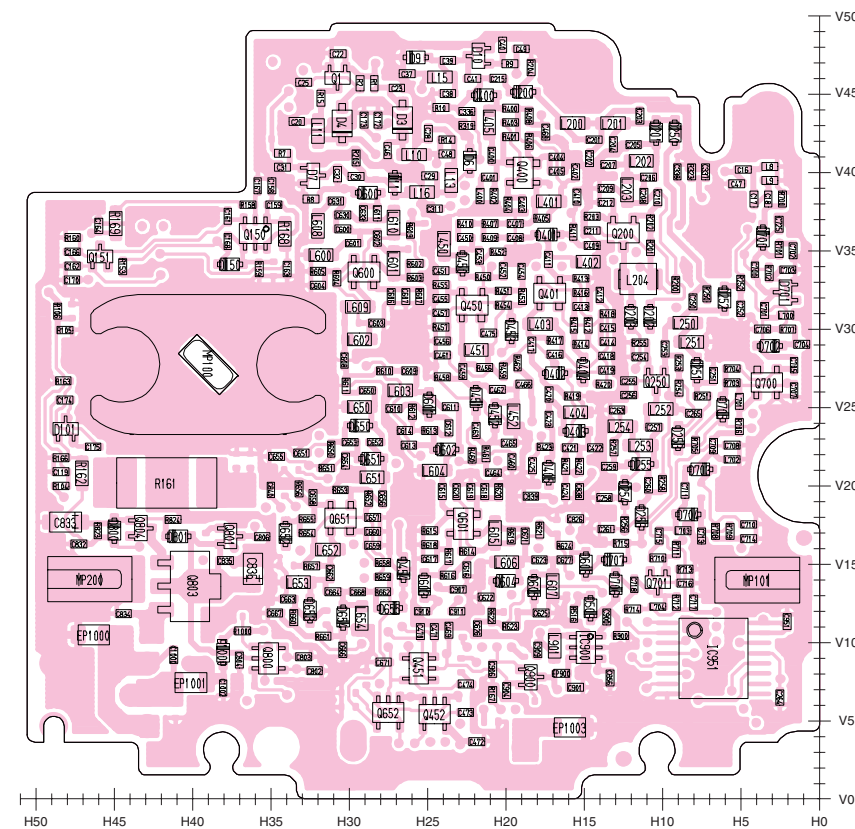
• **BOTTOM VIEW (VCO UNIT)**



2	1
GND	BCLV
VVCO3	GND
VLV	VSHIFT
GND	BCVCO3
MOD	GND
GND	PLLOUT
UVCO3	GND
ULV	LOOUT
16	15

to RF unit J800

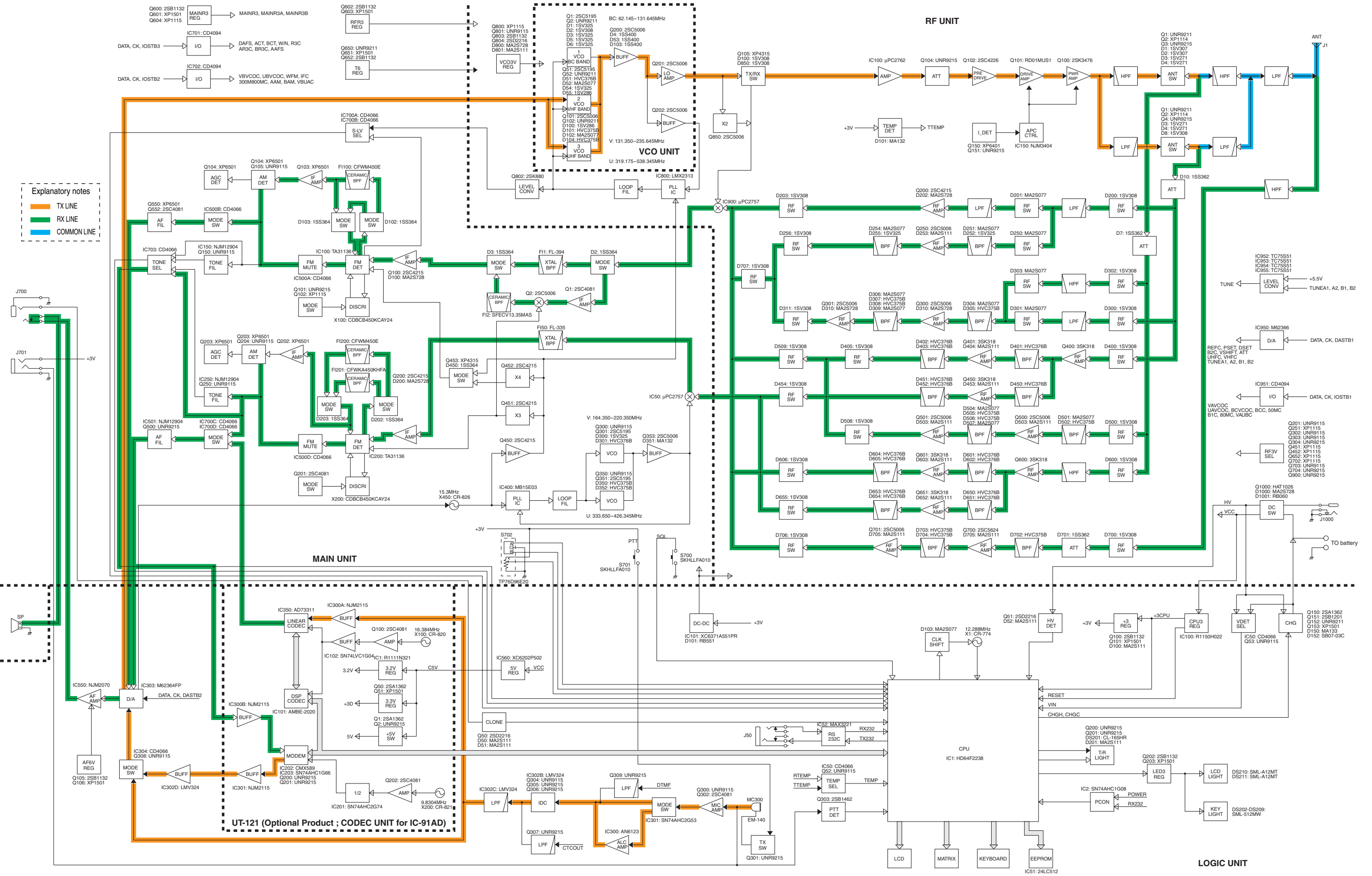
• **BOTTOM VIEW (RF UNIT)**



1	23
GND	RESET
FLASH	LDZ
BTONE	AMBERXD
DAFOUT	AMBERTXD
AMODIN	DVC
DMOD	ACQ
C5V	TXCK
+3V	TXDT
AMBEPR	FXCK
AMBERES	DCEL
AMBECLK	GND
24	24

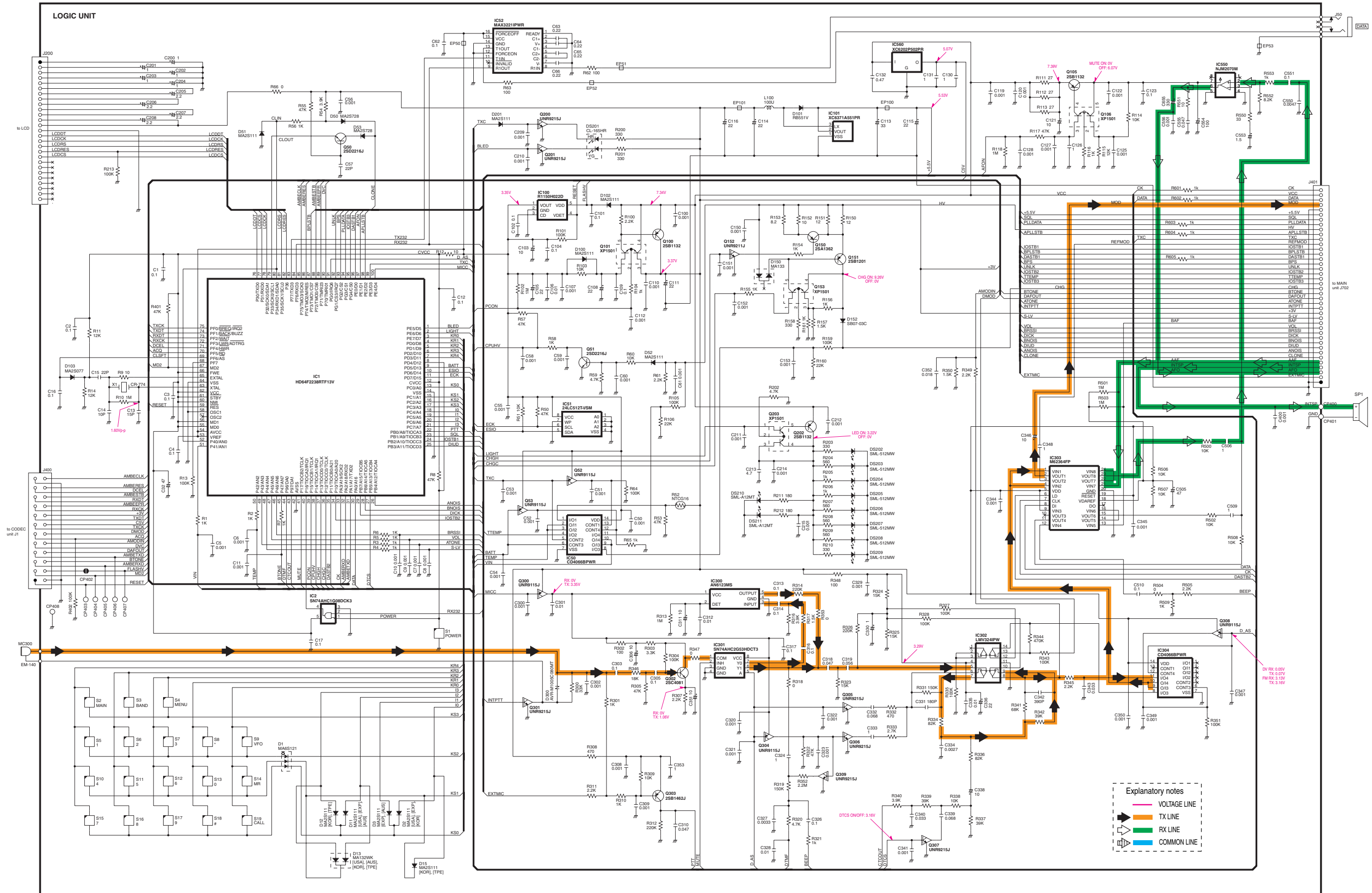
to CODEC unit J1

SECTION 11 BLOCK DIAGRAM

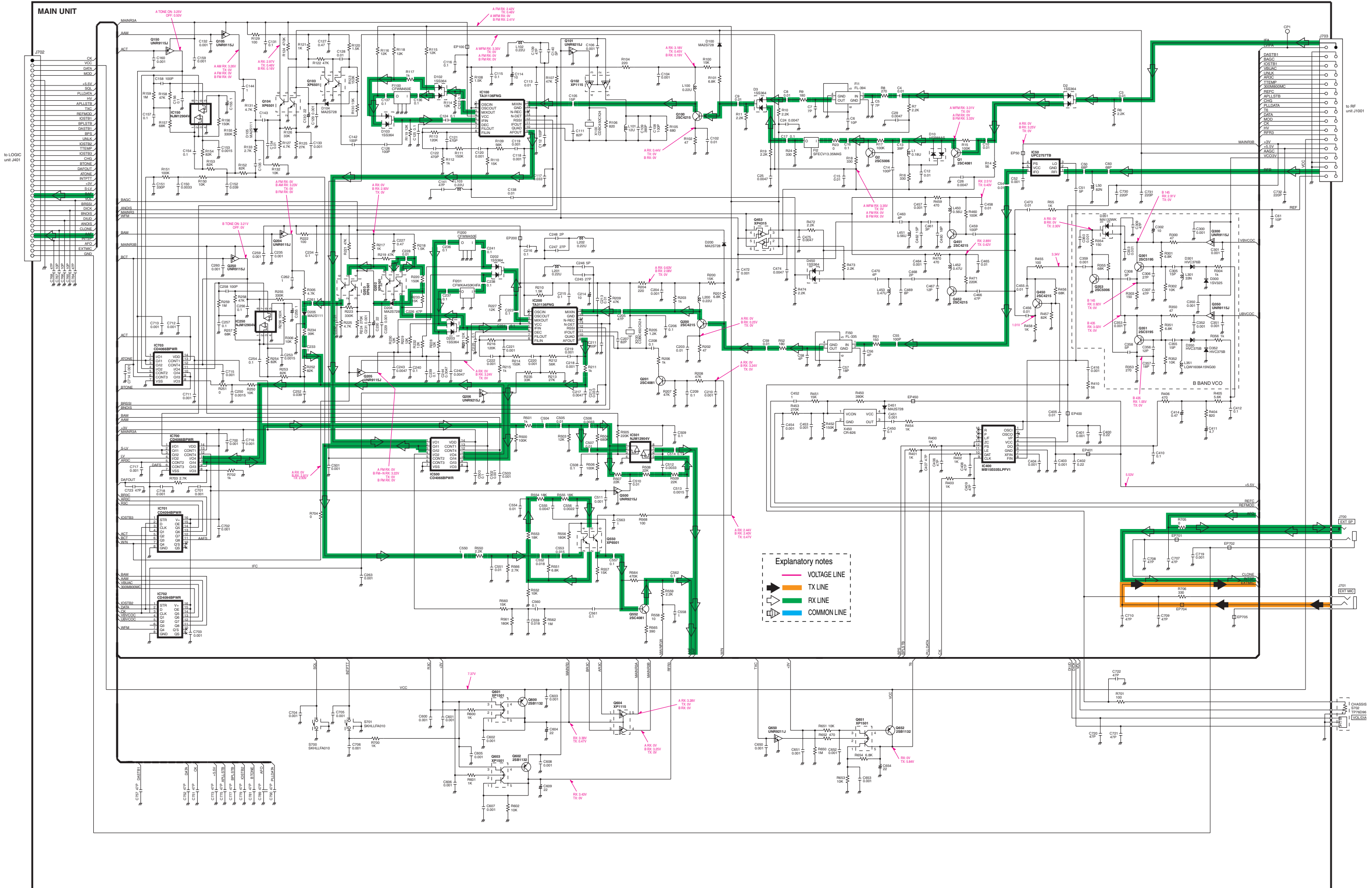


SECTION 12 VOLTAGE DIAGRAMS

12-1 LOGIC UNIT



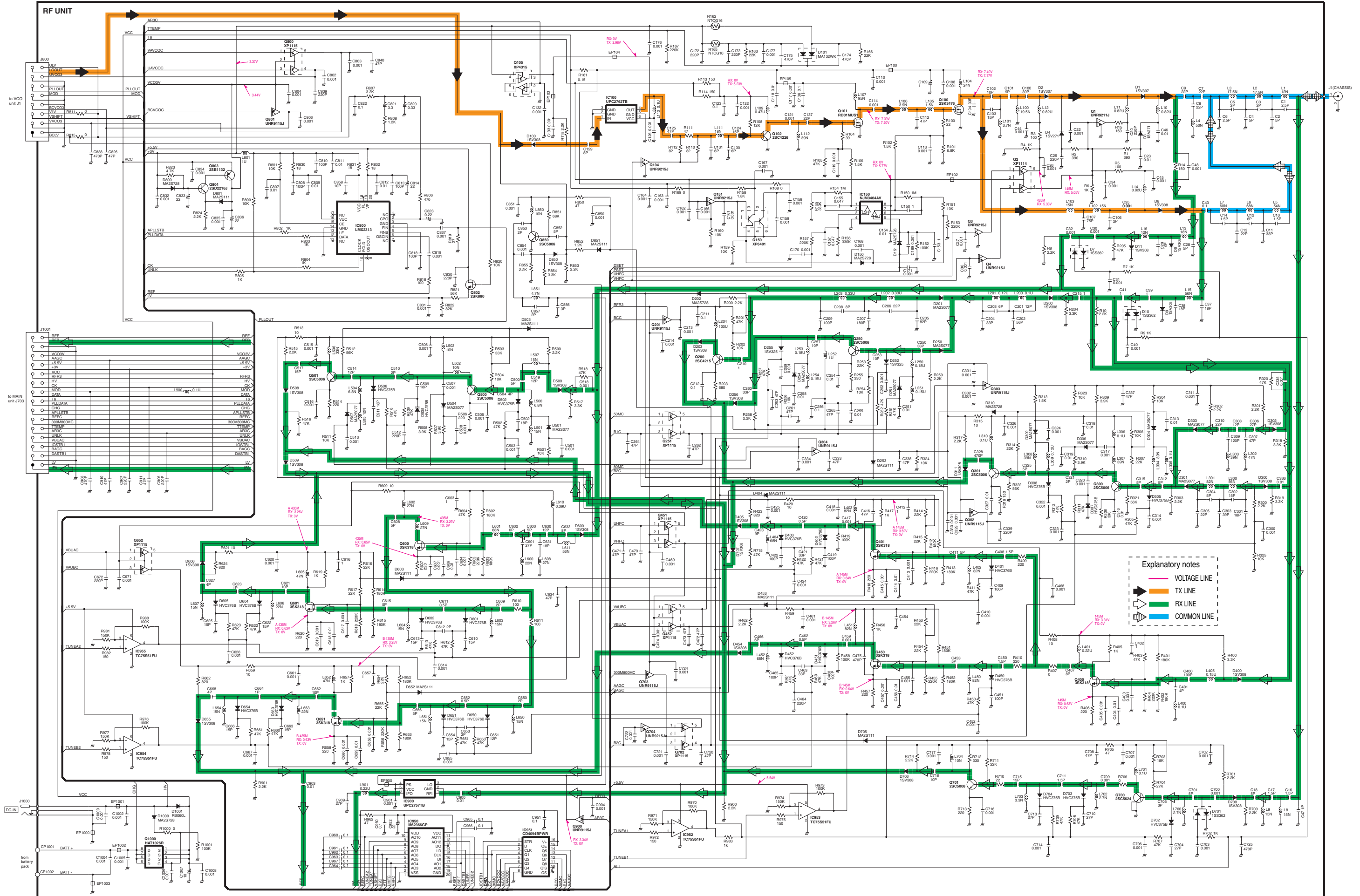
12-2 MAIN UNIT



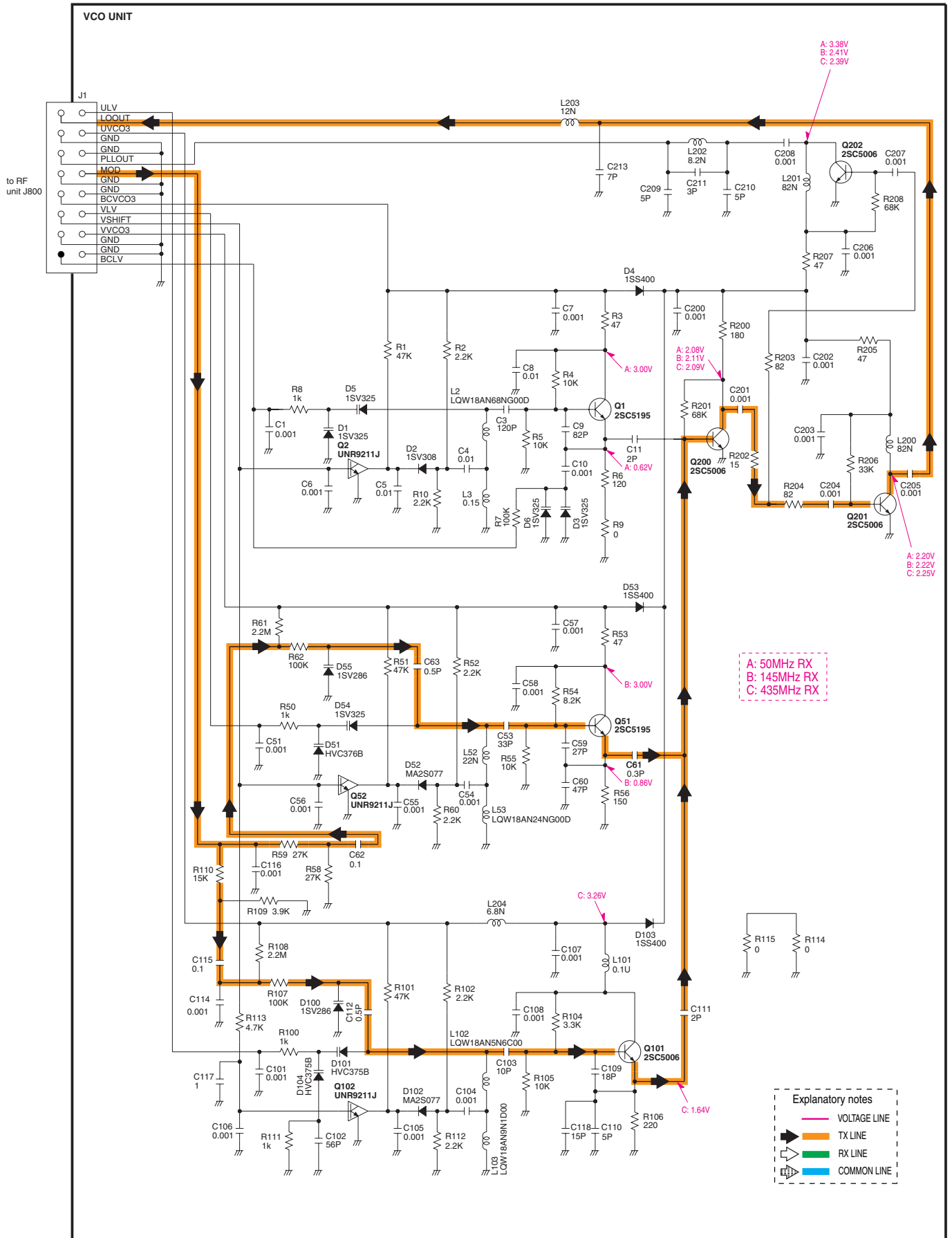
Explanatory notes

- VOLTAGE LINE
- TX LINE
- RX LINE
- COMMON LINE

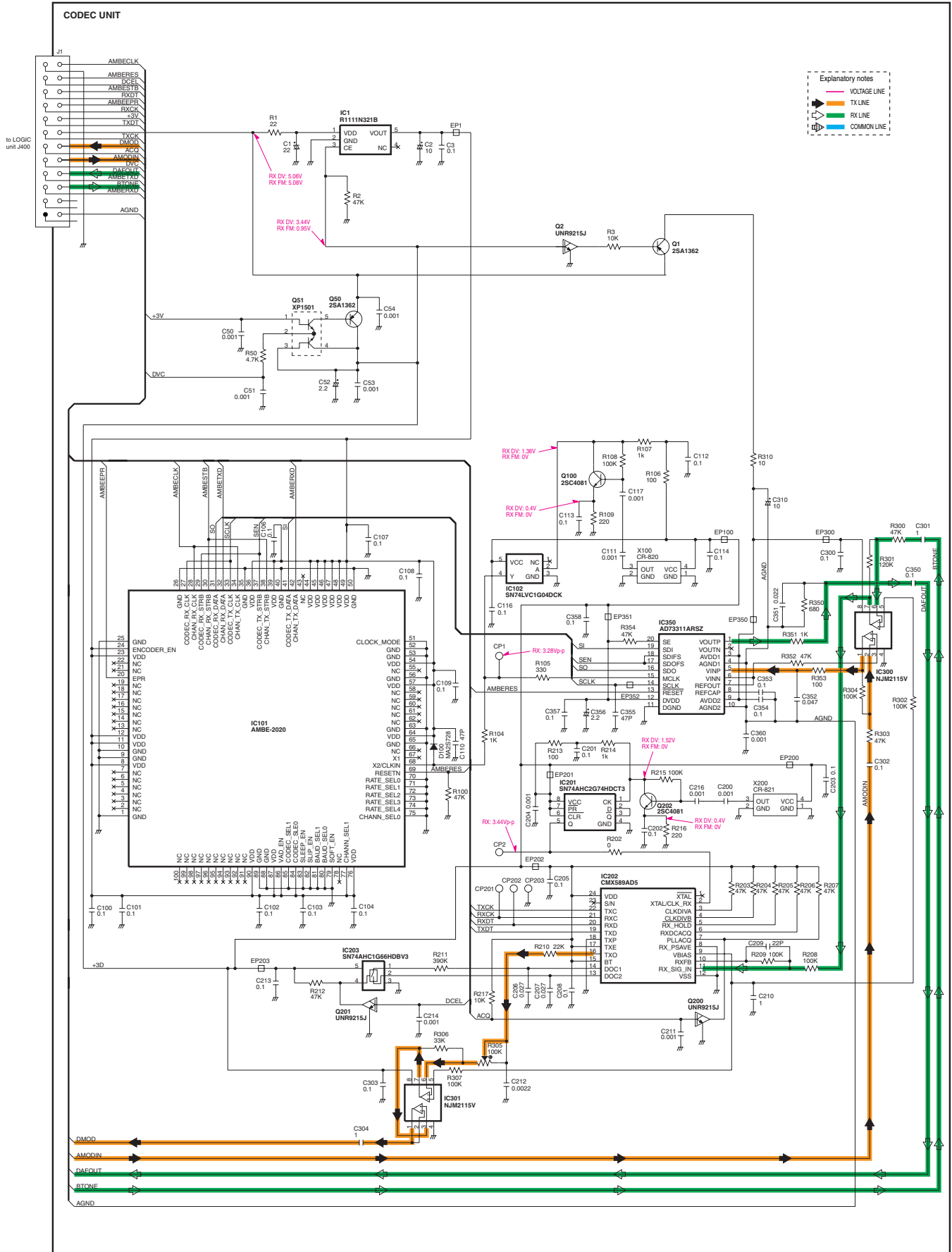
12-3 RF UNIT



12-4 VCO UNIT



12-5 UT-121 (Optional Product ; CODEC UNIT for IC-91AD)



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