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#### T375 Receiver

#### VHF FM 138-225MHz

(TM-375)

Issue A

### TECHNICAL INFORMATION

For further information about this Manual or the equipment it describes, contact the Product Distribution Group, Tait Electronics Ltd, at the above address.

#### UPDATING EQUIPMENT & SERVICE MANUALS

In the interests of improving performance, reliability or servicing, Tait Electronics Ltd reserve the right to update their equipment and/or Service Manuals without prior notice.

#### SCOPE OF MANUAL

This Manual covers General, Technical and Servicing Information on the Tait T375 VHF FM Receiver.

## Ordering Service Manuals

When ordering the T375 Service Manual quote the Tait IPN and the version required, e.g. IPN TM-375, Version T375/02, T375/05 etc.

## Date Of Issue

IPN TM-375

T375 Service Manual

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A2C606

A2C605

A2C279 (Fold-out)

C1C594 (Fold-out)

#### SECTION 1 GENERAL INFORMATION

#### 1.1 INTRODUCTION

The T375 is a high performance FM Base Station Receiver designed for single or four channel operation in the 138 to 225MHz frequency range. The receiver has two basic versions which cover either 138-174MHz or 174-225MHz.

The RF section of the receiver is a dual conversion superhet with intermediate frequencies of 21.4MHz and 455kHz. The IF line-up includes two 21.4MHz crystal filters which determine the band-pass characteristics. Amplitude limiting, detection and an audio pre-amplifier are contained in a single integrated circuit.

The IF section also drives carrier and noise level detectors for signal strength indication and gating of audio outputs.

The audio section delivers up to +10dBm to a 600 ohm balanced output and 1 watt to a local monitor speaker. Provision is also made to monitor the 600 ohm balanced output.

Front panel controls include gate sensitivity, line level, monitor volume, local oscillator frequency monitor and mute disable switch. This switch disables the mute (squelch) signal to the monitor amplifier as an aid to servicing. A local oscillator frequency trim adjustment is also provided when the standard reference crystal is fitted.

The components are mounted on two printed circuit boards which are secured in a metal frame. The frame fits into a robust metal sleeve which gives mechanical and environmental protection.

The T375 may be housed together with up to six similar modules in a Tait rack shelf which in turn may be mounted in a standard 484mm (19 inch) rack frame or a lockable cabinet to provide an attractive and convenient installation.

#### 1.2 **SPECIFICATIONS**

#### 1.2.1 INTRODUCTION

The performance figures given below are typical figures, unless otherwise indicated, for equipment operating under standard test conditions (13.8V DC supply and ambient temperature 22°C to 28°C); unless otherwise indicated, the figures apply to all versions.

Where applicable, the test methods used to obtain the following performance figures are those described in the New Zealand Post Office Specification RTA25.

Details of test methods and the conditions which apply for Type Approvals can be obtained from Tait Electronics Ltd.

#### 1.2.2 GENERAL

Type

.. dual conversion superheterodyne

Frequency Range

.. 138 to 225MHz

Frequency Increments

.. 5kHz or 6.25kHz

Number of Channels

.. one (four channel option available)

Switching Range

.. 2.5MHz

Supply Voltage:

Operating Range

Standard Test Voltage

Polarity

.. 10.8 to 16 volts DC

.. 13.8 volts DC

.. negative earth only

Supply Current:

Gate Off, Monitor On

Gate On, Monitor On, full audio

.. 220mA

.. 600mA

Antenna Impedance

.. 50 ohms (nominal)

Operating Temperature Range

(refer to Section 1.2.5)

.. -30°C to +60°C

Dimensions:

Height

191mm

Length

300mm

Width

60mm

Weight:

With Sleeve

.. 1.2kg

With Sleeve & Guide

.. 1.6kg

1.2.3 RF & IF SECTIONS

12dB Sinad Sensitivity

.. -118dBm

21.4MHz and 455kHz

IF Amplifiers:

Frequencies

7.5kHz

Bandwidth: Narrow Band .

Wide Band

.. 15kHz

Signal+Noise To Noise Ratio:

RF Level -107dBm .. 20dB RF Level -47dBm .. 45dB

Selectivity (Adjacent Channel):

Narrow Band .. 80dB
Wide Band .. 85dB

Spurious Response Attenuation .. 85dB

Intermodulation Response Attenuation .. 75dB

Gating Sensitivity:

Threshold .. -121dBm (0.2μV pd)/6dB Sinad Hard Setting .. -110dBm (0.7μV pd)/22dB Sinad

Oscillator Fine Frequency Trim Range .. ±500Hz

1.2.4 AUDIO OUTPUTS

Outputs Available .. line and monitor

Load Impedances:

Line Terminals .. 600 ohms Monitor Speaker .. 3 ohms

Output Power:

Line Terminals .. +10dBm Monitor Speaker .. 1W maximum

Distortion .. 3% (line terminals)

Audio Frequency Characteristics:

Types Available .. flat or de-emphasised

Flat Response:
67 to 3400Hz .. within +1, -2dB of output level at 1kHz

De-emphasised Response:

67 to 300Hz .. within +1, -2dB of output level at 100Hz 300 to 3400Hz .. within +1, -3dB of a 6dB/octave deemphasis characteristic (ref. 1kHz)

1.2.5 FREQUENCY REFERENCE

Oscillator Frequency:

5kHz Reference Frequency .. 10.24MHz 6.25kHz Reference Frequency .. 12.8MHz

Crystal Type .. TE/26

Standard Versions:

Temperature Range .. -10°C to +60°C

Stability .. ±4ppm

Low Temperature Version:

Temperature Range

Stability

Crystal Frequency

TCXO Type

.. -30°c to +60°C

.. +2.5ppm

12.8MHz

.. TCO-909A

#### 1.3 VERSIONS

The following versions of the T375 receiver are covered by this Manual:

T375/02

FM Receiver 138-174MHz

Frequency increment 6.25kHz minimum

IF bandwidth 15kHz

Front panel frequency trim B3535/02 audio processor

T375/05

FM Receiver 138-174MHz

Frequency increment 6.25kHz minimum

IF bandwidth 7.5kHz

Front panel frequency trim B3535/02 audio processor

T375/15

FM Receiver 174-225MHz

Frequency increment 6.25kHz minimum

IF bandwidth 7.5kHz

Front panel frequency trim B3535/02 audio processor

T375/22

FM Receiver 138-174MHz

Frequency increment 6.25kHz minimum

IF bandwidth 15kHz 4 channels available

Front panel frequency trim

B3535/02 audio processor

T375/25

FM Receiver 138-174MHz

Frequency increment 6.25kHz minimum

IF bandwidth 7.5kHz 4 channels available

Front panel frequency trim B3535/02 audio processor

T375/42

FM Receiver 174-225MHz

Frequency increment 6.25kHz minimum

IF bandwidth 7.5kHz 4 channels available

Front panel frequency trim B3535/02 audio processor

T375/51

FM Receiver 138-174MHz

Frequency increment 5kHz minimum

IF bandwidth 15kHz

Front panel frequency trim B3535/02 audio processor

T375/55

FM Receiver 138-174MHz

Frequency increment 5kHz minimum 🗸

IF bandwidth 15kHz 4 channels available

Front panel frequency trim B3535/02 audio processor

## T375 Circuit Operation

## SECTION 2 CIRCUIT OPERATION

Refer to the Block and Circuit Diagrams at the rear of this Manual.

## 2.1 FREQUENCY SYNTHESIZER

The T375 employs the dual modulus system of frequency synthesis. The voltage controlled oscillator (VCO, Q22 and buffer Q23) runs at the actual local oscillator operating frequency; ie, there are no multiplier stages. The loop control voltage on varicap D16 determines the VCO frequency over its switching range.

A crystal provides a stable reference frequency of 12.8MHz (10.24MHz) which is divided down to 6.25kHz (5kHz) and fed to one input of a phase comparator within IC3. Fine tuning trim consists of a varicap diode and a front panel multiturn preset control. A second varicap (D19), together with an NTC resistor (R89), temperature compensates the oscillator. For applications which require better frequency stability over the temperature range, a temperature compensated crystal oscillator (TCO-909) may be fitted, however this cannot be adjusted from the front panel.

The VCO frequency is divided by the 40/41 prescaler, IC4, and then further divided within IC3 to provide the other input to the phase comparator. The division ratio in IC3, and hence the channel frequency, is determined either by the onboard DIP switches for single channel applications, or by a diode matrix board for multi-channel applications.

The phase comparator output (pins 7 & 8 of IC3) is fed to the VCO tuning varicap via an active loop filter (IC2a). The loop bandwidth is opened up, using pin diode switching, when the lock detect line on IC3 indicates an out of lock condition.

#### 2.2 RF & IF SECTIONS

The signal enters the receiver via the N-type female connector mounted on the rear chassis panel, and feeds down via a 50 ohm coaxial cable to the PCB.

It passes through a two-section, capacatively-coupled band-pass filter to the base of the common-emitter RF amplifier (Q8) mounted on the underside of the board. From the collector, the signal is further filtered by a 3-section, capacitively-coupled band-pass filter before being presented to the sources of the balanced mixer transistors (Q11 & Q12).

The output from the synthesizer is phase split and presented to the bases of the mixer transistors (Q11 & Q12). The IF signal appears in anti-phase at the drains of the mixer transistors and is combined in the tuned circuit before being presented to the first IF crystal filter (XF1) which provides the required high selectivity. The filter passes only the 21.4MHz component of the mixer output to the input of the first IF amplifier (Q14 & Q15). Further IF filtering then takes place in a two pole crystal filter (XF2) before the 21.4MHz signal is mixed with the output of a 20.945MHz crystal oscillator (X2 & IC1a) to give the second IF signal at 455kHz. This signal is then fed into a limiting amplifier before demodulation, using a frequency discriminator (IC1). The audio output is then fed to the audio processor PCB.

The squelch operates at frequencies outside the audio bandwidth. The audio signal is band-pass filtered (IC1c), amplified, rectified and then smoothed. The resultant DC level is compared with a preset DC level, adjustable from the front panel, and the mute control line fed to the audio PCB.

#### T375 Circuit Operation

Hysteresis is introduced on the comparator (IC5a) to prevent the mute opening and closing in rapid succession. The attack and decay time constants of the squelch are controlled by the components on the output of IC5a.

A signal strength indicator is provided to meet the requirments of a trunking system. The 455kHz IF signal is buffered (Q28), amplified (Q29), rectified (D24) and smoothed. Feedback is provided to achieve a linear characteristic between received signal strength (dBm) and output voltage. The resultant signal is fed through a temperature compensated amplifier (IC5d) and output to the rear D range connector.

#### 2.3 9 VOLT REGULATOR

A 5V line is generated directly (Q1) from the 13.8V (nominal) supply. A 9V supply is generated using a DC differential amplifier configuration (Q2 - Q5). The DC gain is determined by the ratio of R4 and R5.

## 2.4 AUDIO PROCESSOR (B3535)

The mute output from the RF/IF section is fed to a buffer inverter (Q1) whose collector current flows through the 'gate' LED on the front panel to the bases of Q2 and Q5.

Q1A would provide a receiver 'disable' function by clamping the base of Q1 to its emitter. An 'enable' delay may be provided by RV1a, R1b, R2b and C1b when the receiver is to be used in a semi-duplex, two frequency linking system to prevent interaction. This block of circuitry is unused in the standard T375.

When the base of Q2 is driven, its collector clamps the bases of both gating transistors (Q3 and Q4) to their emitters, thus allowing audio to pass to the line and monitor outputs. Q4 maybe held in the unmuted condition by closing the front panel 'Monitor Mute' toggle switch (S1) as a servicing aid.

Applying drive to the base of Q5 also causes its collector to clamp to the 0V line, providing an open-collector output which may be used to key a transmitter. If a relay is fitted, the pull-in coil is also driven by Q5 (Q5 may be a medium or high power device depending upon its application).

The audio signal is applied to the op. amps (IC1a and IC1b) which, depending upon the associated components, provide either a CTCSS-compensated function or a totally 'flat' function, both having unity gain at 1kHz. A bridged T network (C7, C8, C9, R13 & R15) connected in a feedback loop gives a broad resonance at about 280Hz. When combined with the de-emphasis network (R19, C10), the circuit complies with the requirements for a flat response at CTCSS frequencies, but retains the de-emphasized response at audio frequencies.

When a totally flat response is required (e.g. in linking systems), the de-emphasis components are effectively eliminated by the changes set out in Section 4.5.

The signal is presented to a low pass filter (R21, C12 to C15, L1 & R22) which limits the upper audio response, then passes to the individual line and monitor level controls (RV39 & RV40). The signal is taken from the sliders of the potentiometers past the muting transistors (Q3 & Q4) to IC2 which contains two power amplifier sections. One section provides an output to drive a speaker, the other provides the line output via transformer T1. The output impedance is set at 600 ohms by two feedback loops around the line amplifier – one proportional to the output current, the other proportional to the output voltage.

A monitor output is also available from the 'hot' end of the line transformer primary, via R41, which may be taken to a monitor module.

### SECTION 4 INSTALLATION

### 4.1 GENERAL

Tait Fixed Equipment transmitters and receivers may be assembled into a wide variety of fixed equipment systems, from a simple 'land mobile base' to a complex 'linking system' operating in the 'hot standby mode'.

#### 4.2 TALK THROUGH REPEATER

In this configuration the receiver directly keys the transmitter when the signal is received. The demodulated audio is fed via 600 ohm lines to the transmitter to modulate the carrier. The receiver and transmitter operate simultaneously and must therefore be on different frequencies. The minimum frequency separation depends on the duplexer used.

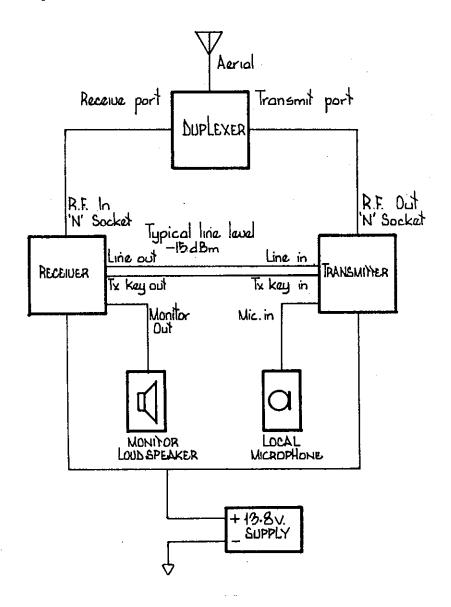


Figure 1 Talk Through Repeater

## 4.3 LINE CONTROLLED BASE STATION (WITHOUT TALK THROUGH)

This installation contains a transmitter and receiver which may or may not be on the same frequency, thus simultaneous transmission and reception is not possible. In this case the transmitter is keyed from the Remote Control Unit (RCU). When the transmitter is keyed, the coaxial relay is also energised. When the relay is in its rest position, signals from the aerial are passed to the receiver and the demodulated output is fed via 600 ohm lines to the RCU.

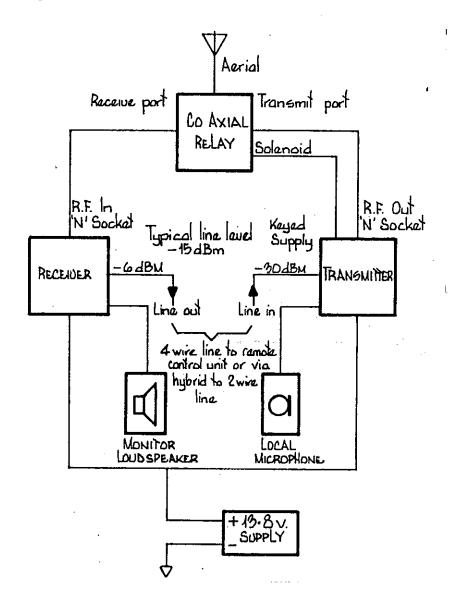


Figure 2 Line Controlled Base Station

## 4.4 4-WIRE TO 2-WIRE CONVERTER (HYBRID)

One way in which a base station may be line controlled by a two-wire line is by providing a 4-wire to 2-wire converter (hybrid). The line transformers may be interconnected to form such a hybrid by the following method.

1. Interconnect the windings of the line transformers as shown in Figure 3.

Note: Although the turns ratios of the transformers are not optimum for correct impedance match, the configuration performs adequately for most applications.

#### T375 Installation

- 2. RV1 (which controls the hybrid balance) may be adjusted for maximum isolation or for a specific talk through level as required. When maximum isolation is required, the slope of the average audio response between the two 4-wire ports may be improved by slight adjustment of RV1, sacrificing 1 or 2dB of total isolation between the 4-wire ports. This adjustment can be carried out audibly by listening to the retransmitted white noise output of an open receiver with no signal input and observing the relative response slope while making the adjustment.
- 3. The hybrid balance may be improved by fitting a capacitor (C1) in series with RV1. This will depend on the line characteristics and whether a capacitor is fitted in the line path. Some experimentation may be necessary to find the value of C1 (usually 1 or  $2\mu F$ ).

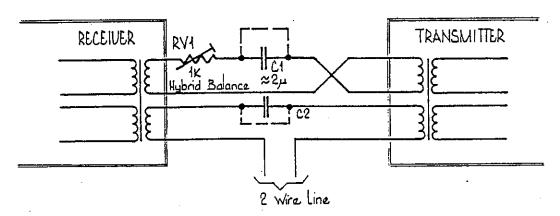


Figure 3 4-Wire to 2-Wire Converter

Note 1: It is important that the two windings of each transformer are phased as shown.

Note 2: C2 may be fitted to allow DC signalling on the line.

## 4.5 AUDIO PROCESSOR FLAT RESPONSE

Refer to Circuit Diagram A2C 279.

The B3535 audio processor links are set to give a CTCSS de-emphasis response. If a flat response is required (e.g. in linking systems), the following circuit changes should be carried out:

- (a) Fit an insulated wire link across C8 on the underside of the PCB.
- (b) Remove the link which connects the junction of 'C11, R20' and the junction of 'C10, R19 to IC1b pin 8'.
- (c) Fit an insulated wire link between the junction of 'C11, R20' and point 'B' which is connected to pin 14 of IC1a.

Note: No components are removed, so the de-emphasis can be restored by removing the links (a) and (c) and refitting the link (b) as given above.

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#### SECTION 5 SERVICING

#### 5.1 GENERAL

#### **5.1.1 NOTES**

If further information is required about the T375 or this Manual, it may be obtained from Tait Electronics Ltd or accredited agents. When requesting this information, please quote either the equipment serial number or works order number (found on a label at the back of the set). In the case of the Service Manual quote the Tait Internal Part Number (IPN) and Issue, and for Circuit Diagrams quote the 'Title' and 'Issue'.

#### CAUTION: CMOS DEVICES

This equipment contains CMOS Devices which are susceptible to damage from static charges. Care when handling these devices is essential. For correct handling procedures refer to manufacturers' data books covering CMOS devices, e.g. Philips Data Handbook Covering CMOS Devices; Motorola CMOS Data Book Section 5 (Handling Procedures), etc.

## 5.1.2 TECHNICAL INSTRUCTIONS (TI's)

From time to time TI's are issued by Tait Electronics Engineering Division. These TI's may be used to update equipment or information, or to meet specific operational requirements.

#### 5.2 MECHANICAL

## 5.2.1 POSIDRIV RECESS HEAD SCREWS

Posidriv recess head screws are the preferred standard on all Tait manufactured equipment. The very real advantages of this type of screw will not be realised unless the correct screwdrivers are used by servicing personnel.

Note: Philips cross-head screws are used in some locations which require very small screws. A Philips cross-head driver must be used on these screws.

#### 5.3 REPAIR

## 5.3.1 COMPONENT CHECKS

If a transistor is suspected of faulty operation, an indication of its performance can be assessed by measuring the forward and reverse resistance of the junctions. First make sure that the transistor is not shunted by some circuit resistance (unless the device is completely unsoldered). A 20k ohm/V or better multimeter should be used for taking the measurements, using only the medium or low resistance ranges.

The collector current drawn by multi-junction transistors is a further guide to their performance.

If an IC is suspect, the most reliable check is to measure the DC operating voltages. Due to the catastrophic nature of most IC failures, the pin voltages will usually be markedly different from the recommended values in the presence of a fault. The recommended values can be obtained from either the Circuit Diagram or the component data catalogue.

#### 5.3.2 COMPONENT REPLACEMENT

Whenever components are removed from, or fitted to the printed circuit track, care must be taken to avoid damage to the track. If it is necessary to remove a component from the track, the following procedure is recommended:

Remove the solder from the component leads using a solder wick or solder sucker.

Loosen the individual leads from the printed track.

Withdraw the component from the top of the PCB.

Do not remove the component from the PCB while the solder is still molten.

Keep all soldering operations, and the heat and solder applied, to a minimum. A thermally controlled, fine tip soldering iron should be used. Ensure that the iron is earthed back to the frame of the set.

#### 5.3.3 INTER-BOARD WIRING

To assist circuit tracing all plugs and connections are shown on the outer edge of the Circuit Diagrams.

#### 5.4 SETTING UP

#### 5.4.1 TEST EQUIPMENT REQUIRED

- 1. Oscilloscope good quality, 0-10MHz (e.g. Trio 1566A, Telequipment D61A)
- 2. VHF FM generator capable of providing 138-225MHz at 0dBm to -127dBm frequency modulated to ±25kHz deviation at 1kHz. (e.g. HP8654)
- 3. VHF frequency counter (accuracy better than 1ppm)
- 4. Sinad meter (e.g. Helper Instruments Sinadder)
- 5. Audio oscillator
- 6. Multimeter or DMM (e.g. AVO model 8 or Fluke 8012A)
- 7. AC millivoltmeter (e.g. Trio VT106)
- 8. DC millivoltmeter (e.g. Trio VT120, Tech.Inst. TE65)
- 9. RF diode probe (e.g. Greenpar GE 88202)
- 10. External speaker 3 ohm voice coil
- 11. DC power supply capable of delivering 1 amp at 13.8V
- 12. 'N' to 'BNC' adaptors (2)

13. Trimming tools (available from Tait Electronics Ltd)

WT 9 Tait IPN 9360110 WT 10 9360111 WT 11 9360112

14. Bench service lead

TA-086 Tait Electronics Ltd.

15. Audio band-pass (300-3000Hz) or high-pass (300Hz) filter required for signal to noise ratio measurements only.

#### 5.4.2 TUNING HINTS

- 1. When using an RF probe, the ground return lead should be kept as short as possible and connected as close as possible to the point at which the measurement is being made. This is to minimize stray pick-up which may affect readings.
- 2. When tuning coils on the Receiver PCB, all adjustments must be made from the top (component side) of the PCB. A non-metallic tuning tool should be used.

## 5.4.3 FREQUENCY RANGE SELECTION

#### **5.4.3.1** General

The T375 will cover the frequency ranges 138-174MHz or 174-225MHz according to the version specified (refer to Section 1.3). Each of these frequency bands is further divided into two by a simple arrangement of solder bridge links.

The 138-174MHz version is band-split at 156MHz and the 174-225MHz version is band-split at 200MHz. When manufactured the T375 is initially set for high band operation.

# 5.4.3.2 To Set To Low Band Operation (138-156MHz or 174-200MHz)

Under each of the five front-end coils (L4, L5, L7, L8 & L9) are two octagonal pads, A and B. Link pad A to ground and isolate pad B from ground by the addition or removal of a solder bridge link.

Under L13 connect link A and open circuit link B by the addition or removal of a solder bridge (refer to Figure 4).

Bring C96 into the VCO circuit by short circuiting link 6 with a solder bridge. This link is positioned under the rear VCO cover and uses the terminal pads of C95 and C96.

Ensure that the connection between link 4 and link 5 is open circuit.

## 5.4.3.3 To Restore To High Band Operation (156-174MHz or 200-225MHz)

Under each of the five front-end coils (L4, L5, L7, L8 & L9) are two octagonal pads, A and B. Link pad B to ground and isolate pad A from ground by the addition or removal of a solder bridge link.

Under L13 connect link B and open circuit link A by the addition or removal of a solder bridge (refer to Figure 4).

Isolate C96 from the VCO circuit by setting link D open circuit by removing the solder bridge. This link is positioned under the rear VCO cover and uses the terminal pads of C95 and C96.

When operating in the 200-225MHz band, connect link 4 and link 5 with a solder bridge.

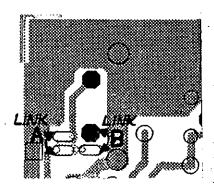


Figure 4 Link Arrangement Under L13

#### 5.5 RECEIVER ADJUSTMENTS

### 5.5.1 PRELIMINARY CHECKS

Check that the correct components are fitted for operation in the required frequency band.

Check for short circuits between the positive rail and earth ('D' range pins 9 and 15), and between the +9V regulated rail and earth.

Set up the test equipment as shown in Diagram 1.

Connect the bench test plug to the receiver while monitoring the supply current, This should be between 100 and 200mA.

Set the gating sensitivity control (RV38) fully anticlockwise (this should open the gate).

Set the line level control (RV39) and monitor level control (RV40) at levels suitable for driving a Sinadder, speaker, etc.

#### 5.5.2 CHANNEL PROGRAMMING

#### 5.5.2.1 Reference Frequency Selection

For 12.5kHz or 25kHz channel spacing, use a 6.25kHz reference (12.8MHz crystal).

For 30kHz channel spacing, use a 5kHz reference (10.24MHz crystal).

#### 5.5.2.2 Programming

For single channel applications, the synthesizer may be programmed using the two 8-bit switches positioned centrally on the receiver PCB.

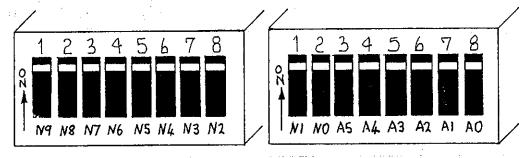


Figure 5 8-Bit Switches

16 bits (numbered A0-N9) are required to programme a frequency, which will be a multiple of the reference frequency. Note that a particular bit will add its frequency increment to the programmed frequency only when its corresponding switch is in the off position.

Table 1 shows the frequency increments represented by each of the 16 bits, A0-N9. Note the discontinuity between A5 and N0.

			Table 1		27.4
174	5.0kHz -102.4MHz -51.2 -25.6 -12.8 -6.4 -3.2 -1.6 -0.8 -0.4	700	6.25kHz 128.0MHz 64.0) 32.0- 16.0 8.0 4.0 2.0 1.0- 0.5	Bit No.  N9  N8  N7  N6  N5  N4  N3  N2  N1	152.6 -188 -188 -16 146 8. 6.6
	0.2 0.16 0.08 0.04 0.02 0.01 0.005		0.25 0.2 0.1 0.05 0.025 0.0125 0.00625	- N0 A5 A4 A3 - A2 A1 A0	12304 2 9 210 8

The following example shows a simple method of calculating the correct programme code.

Example: Rx frequency = 154.0MHz.

VCO frequency = 154.0 - 21.4 = 132.6 MHz.

VCO frequency: Subtract	132.6 $128.0$	switch 'off' N9
Subtract	$\frac{4.6}{0.6}$	switch 'off' N4
Subtract	$\frac{0.5}{0.1}$	switch 'off' N1
Subtract	$\frac{0.1}{0.0}$	switch 'off' A4

In each case subtract the largest value from Table 1 which yields a positive result. Continue the process until zero is reached.

1740

To check: The sum of the extracted values should equal the required VCO frequency.

N9 + N4 + N1 + A4 = VCO

128 + 4 + 0.5 + 0.1 = 132.6

The DIP switches would therefore be arranged as shown in Figure 6.

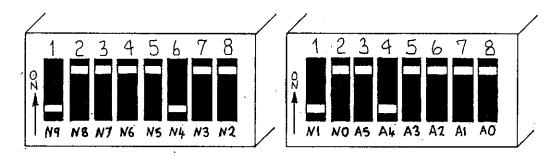


Figure 6 DIP Switch Positions

#### 5.5.3 MULTI-CHANNEL PROGRAMMING

#### 5.5.3.1 General

A plug-in diode matrix board can be used to programme the receiver for up to 4 channels. Channel selection is achieved by pulling low one of four lines via a second 'D' range connector (refer to the Wiring Diagram for pin-out details).

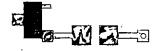
Note: Only one line may be pulled low at any one time, and the PCB mounted DIP switches must all be in the off position.

#### 5.5.3.2 TA-500/M2 PCB

Note: The supplied diode programming PCB will have several low value chip resistors fitted as standard for production testing. These must be removed (from both channels) before any programming is attempted.

The programming of each of the four channels is accomplished by soldering between the required pads on each row of surface mount diodes (see Figure 7).





A connected pad pulls IC1 input low and deletes the frequency increment.

An unconnected pad allows IC1 input to go high and adds the frequency increment.

#### Figure 7

Table 2 shows the frequency increments represented by each of the 16 bits, A0-N9. Note the discontinuity between A5 and N0.

Table 2

			*********
5.0kHz	6.25kHz	Bit No.	
102.4MHz	128.0MHz	N9	
51.2	64.0	N8	
25.6	32.0	N7	~~ *~ V \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
12.8	16.0	N6	**************************************
6.4	8.0	N5	
3.2	4.0	N4	
1.6	2.0	N3	
0.8	1.0	N2	**************************************
0.4	0.5	N1	
0.2	0.25	NO	
0.16	0.2	A5	
0.08	0.1	A4	
0.04	0.05	A3	
0.02	0.025	A 2	
0.01	0.0125	A1	
0.005	0.00625	A0	
0.003	0.00020	ΛU	
			الملم المحتما
			on bod d d d d d

The four programmable channels on the diode matrix board correspond to channels 1-4 in the T375 as follows:

TA-500/M2	<u>T375</u>
T1	Channel 1
T2.	Channel 2
R1	Channel 3
R2	Channel 4

When a pad is solder bridged, its corresponding N or A value is subtracted from the maximum frequency count.

When a pad is left open, the corresponding value is incremented from zero.

The following example shows a simple method of calculating the correct diode programme.

Example: Rx frequency = 154.0MHz.

VCO frequency = 154.0 - 21.4 = 132.6 MHz.

VCO frequency:	132.6	
Subtract	128.0	pad N9 unconnected
	4.6	
Subtract	4.0	pad N4 unconnected
	$\overline{0.6}$	
Subtract	0.5	pad N1 unconnected
	$\overline{0.1}$	
Subtract	0.1	pad A4 unconnected
	$\overline{0.0}$	

In each case subtract the largest value from Table 2 which yields a positive result. Continue the process until zero is reached.

To check: The sum of the extracted values should equal the required VCO frequency.

N9 + N4 + N1 + A4 = VCO

128 + 4 + 0.5 + 0.1 = 132.6

Note: All these N values have pads left open. The remainder, i.e. N8, N7, N1, A5, A4, A2, A1 & A0, are all solder shorted.

#### 5.5.4 VCO ALIGNMENT

Ensure that the synthesizer is correctly programmed.

Monitor the loop voltage (centre pin of P3) with a high impedance volt meter (0-15V range).

## 5.5.4.1 Single Channel Operation

Adjust CV98 for 5V at P3.

Check the frequency using a frequency counter connected to the front panel BNC socket.

#### 5.5.4.2 Multi-Channel Operation

Adjust CV98 so that, when switching between channels, the loop voltages are symmetrically placed about 5 volts, but within the limits of 2 and 8 volts. The channel frequencies should not span more than 2.5MHz.

Check each frequency using a frequency counter connected to the front panel BNC socket.

## 5.5.4.3 Reference Frequency Adjustment (standard option only)

Any error in the reference frequency will be seen as a frequency error in the VCO frequency.

Adjust the front panel mounted 'Freq Trim' pot for the correct VCO frequency ( $\pm 100 \mathrm{Hz}$ ), monitoring the frequency on the front panel BNC socket.

Note: CV108 provides coarse frequency adjustment to compensate for variances in crystal characteristics and is set as follows:

Set the voltage on the wiper of the crystal trim pot. to approximately 5 volts and adjust CV108 to the nominal frequency.

Fine trim as above.

#### 5.5.5 BALANCED MIXER ALIGNMENT

This adjustment can be made without any receive signal input and should be carried out before any IF or RF alignment is made.

Ensure that L13 has the correct taps for the frequency used (refer to Section 5.4.3).

Connect a DC voltmeter across R18.

Adjust L13 for maximum DC voltage across R18.

A maximum voltage corresponds to maximum power transfer from the local oscillator into the mixer. This DC voltage should increase about 0.5V for optimum injection. A typical adjustment would vary this voltage from approximately 2 to 2.5V or from 3 to 3.5V.

#### 5.5.6 RF ALIGNMENT

- Note 1: The following RF alignment procedure assumes that the IF alignment is correct and should not be touched unless there is clear evidence of malfunction. If IF alignment is required see Section 5.5.7.
- Note 2: In this and following Sections, measurements and settings are given which differ for wide band and narrow band sets. In these cases the figures for wide band sets are given first followed by the figures for narrow band sets in brackets.

Set up the test equipment as in Diagram 1.

Set the mute disable switch on the front panel to off.

For multi-channel operation, select a frequency mid-way between the highest and lowest channel frequencies.

Set the RF signal generator to give an accurate 'on channel' signal modulated to ±3kHz (±1.5kHz) deviation at 1kHz and the output level to give a 12dB Sinad.

Check that coils L4, L5, L7, L8 and L9 have the correct tap settings for the frequency used (refer to Section 5.4.3).

Tune L4, L5, L7, L8 and L9 for best Sinad while progressively reducing the signal generator output level to keep the Sinad reading less than 12dB to ensure that limiting does not occur.

Retune L4, L5, L7, L8 and L9 for best Sinad while progressively reducing the signal generator output as before.

The signal generator output level should not be greater than -118dBm for a 12dB Sinad.

#### 5.5.7 IF ALIGNMENT

#### 5.5.7.1 General

Note: The IF sections have been correctly aligned during manufacture and should not require readjustment unless repairs have been carried out, or there is clear evidence of misalignment or malfunction.

In this Section two methods of IF alignment are described. Section 5.5.7.2 describes Sinad alignment and Section 5.5.7.3 Sweep alignment. The Sinad method is recommended for field alignments. The 'sweep' method requires the use of an oscilloscope and a signal generator with DC FM facilities.

## 5.5.7.2 Sinad Alignment

Ensure that the VCO and RF sections are correctly aligned as in Sections 5.5.4, 5.5.5 and 5.5.6.

Connect a Sinad meter to the audio output terminals.

Set the RF signal generator to give an accurate 'on channel' signal modulated to  $\pm 3 \text{kHz}$  ( $\pm 1.5 \text{kHz}$ ) deviation at an output level to give a 12dB Sinad.

Tune L14, L15, L19, L20 and L21 for best Sinad, reducing the signal generator output level to maintain a Sinad of about 12dB.

Repeat the previous step.

To further improve response, measure the 'Receiver Signal Strength Indicator' (RSSI) output voltage ('D' range pin 5) with a DC voltmeter.

Adjust L19 for maximum voltage and L20 for minimum voltage at this point.

The signal generator output level should not be greater than -118dBm for a 12dB Sinad.

## 5.5.7.3 Sweep Alignment

Ensure that the VCO and RF sections are correctly aligned as in Sections 5.5.4, 5.5.5 and 5.5.6.

Set the RF signal generator to give an on channel signal level of approximately -105dBm.

Externally modulate the signal generator with between 3 and 5Hz and set it to give approximately  $\pm 30 \mathrm{kHz}$  deviation.

Connect the oscilloscope 'Y' amp. to the RSSI output (pin 5 of the D range).

Connect the source of the modulating signal to the oscilloscope 'X' amp. (both X and Y amps. should preferably be DC coupled).

Set the oscilloscope to the 'X-Y' display.

Tune L14, L15 and L19 for maximum oscilloscope waveform amplitude.

Tune L20 for a minimum oscilloscope waveform amplitude.

Retune L19 and L15 alternately for minimum ripple consistent with maximum oscilloscope waveform amplitude.

Repeat the tuning of L14, L15, L19 and L20 to obtain a symmetrical response with minimum ripple.

Disconnect the external modulation and the oscilloscope.

Set the signal generator modulation to ±3kHz (±1.5kHz) deviation at 1kHz.

Connect a Sinad meter across the speaker terminals and set the signal generator output level to give a 12dB Sinad.

Tune L21 for best Sinad.

The signal generator output level should not be greater than -118dBm for a 12dB Sinad.

## 5.5.8 CARRIER LEVEL DETECTOR ADJUSTMENT

Connect a DC voltmeter (10V range) to the RSSI output.

Set the signal generator to -100dBm and switch the modulation off.

Adjust R113 for a signal strength voltage of 2.2V.

Vary the signal generator output level in 5dB steps from -115dBm to -95dBm and check that the RSSI voltage increases at a rate of approximately 0.5V/5dB.

#### 5.6 FAULT FINDING

### 5.6.1 GENERAL

During servicing it may be necessary to measure specific performance parameters as a means of verifying the presence of a fault condition. The following performance tests provide a means of checking these parameters.

#### 5.6.2 SYNTHESIZER FAULT FINDING

#### 5.6.2.1 To Check The VCO Control Range

Plug a frequency counter onto the VCO front panel socket.

Short the middle pin on P3 alternately to each of the outer pins of P3.

The frequency shift should be more than 5MHz.

#### 5.6.2.2 If The VCO Gives No Output

Remove the VCO box and check for shorts inside.

Check the drain, gate and source voltages (as per the Circuit Diagram) of the VCO and buffer amplifier transistors, Q22 and Q23.

#### 5.6.2.3 If The Synthesizer Does Not Lock Up

Check the VCO control range following the instructions in Section 5.6.2.1.

If the control range is less than 5MHz, check the circuit for faults between P3 and the varicap. The voltage on the varicap must be the same as the loop voltage (P3).

Tune the VCO until its programmed frequency is within the switching range.

If the loop voltage is still either less than 0.6V or more than 7.5V, check pin 7 and pin 8 of the synthesizer (IC3):

(Under normal operating conditions the loop voltage is between 1.75 and 6.5V and both pin 7 and pin 8 are high, except for very narrow pulses [100ns] at the same rate as the reference frequency.)

- (a) If pin 7 pulses low and the loop voltage is low, or if pin 8 pulses low and the loop voltage is high, check the circuitry between IC3 and P3.
- (b) If both stay high and the loop voltage is high, check the crystal oscillator.

Measure the VCO frequency.

Measure the prescaler output frequency (pin 3).

Check that fprescaler = fVCO/40.

Note: The prescaler should not be loaded with 50 ohms - a 1M ohm input counter must be used.

Check that the input voltage of the synthesizer (pin 1) is more than 500mV pp.

Once locked, the lock detect output (IC3, pin 28) should be high (9V).

## 5.6.2.4 To Check The VCO Output Frequency Stability

If the synthesizer locks up but does not reach a stable VCO output frequency, or if the VCO output frequency is a few channels off frequency, check:

- (a) that the input power to the prescaler from the VCO is not too low; (Check the VCO output power and the circuitry between the VCO and the prescaler.)
- (b) that the modulus control pulse (pin 1 of the prescaler) is more than 4.0V.

## 5.6.2.5 Microphonics

If the set shows a high level of microphonics:

- (a) Check that all components inside the VCO box are flush mounted to the PCB, paying special attention to the trimmer capacitor and the inductor. (Resoldering may be attempted, but a solvent cleaner must never be used inside the VCO box.)
- (b) Remove any excess solder where the VCO box touches the PCB.
- (c) Ensure that all screws are securely tightened.

## 5.6.3 RF SENSITIVITY

Connect a Sinad meter across the speaker terminals.

Connect an 'on channel' RF signal generator to the aerial input terminal.

Set the signal generator accurately on receive frequency.

Set the signal generator deviation to ±3kHz (±1.5kHz) at 1kHz.

Turn the mute monitor off.

Set the signal generator output level to -127dBm.

Increase the signal generator output level until a Sinad of 12dB is reached.

The signal generator output should not be greater than -118dBm (0.28 $\mu V$  pd) and should be typically -120dBm.

## 5.6.4 GATING

## 5.6.4.1 To Check The Gate Operation

Connect an on channel RF signal generator modulated to <u>+</u>3kHz (<u>+</u>1.5kHz) with a 1kHz tone to the receiver input, and a Sinad meter across the speaker terminals.

For several input signal strengths between -123dBm and -105dBm turn the gating sensitivity control on the front panel in a clockwise direction until the audio is muted; turn anticlockwise until the audio just unmutes.

Decrease the RF level in 0.5dB steps until the audio is muted.

Increase the RF level until the audio is just unmuted, noting the increase in RF level and the Sinad level at which the audio unmutes.

The increase in RF level required to unmute the audio is the mute hysteresis.

The mute should operate for Sinads between 6 and 20dB. The hysteresis, which will vary with gate setting, should be between 1.5 and 8dB.

#### 5.6.4.2 To Check The Squelch Ratio

Set the signal generator output level to -107dBm (1 $\mu$ V), modulated to  $\pm 3kHz$  (+1.5kHz) deviation at 1kHz.

Replace the Sinad meter with an AC voltmeter across the speaker terminals.

Turn the squelch control fully anticlockwise.

Adjust the volume control to give a reading of 3 volts on the mV/meter.

Reduce the signal generator output level to -127dBm.

The fall in output is the 'squelch ratio' is should be at least 70dB.

## 5.6.5 RECEIVED SIGNAL STRENGTH (RSSI) OPERATION

Connect the RF signal generator, modulated to ±3kHz (±1.5kHz) with a 1kHz tone, to the receiver input.

Connect the DC voltmeter (set to the 10V range) to pin 5 of the D range connector.

Vary the input signal level over the range -95dBm to -115dBm, recording the output voltage.

Note that the signal strength output voltage should be approximately 2.2V for -95dBm, varying by approximately +0.5V for each 5dB increase in signal level.

#### 5.6.6 LINE AMPLIFIER OUTPUT LEVEL

Set up the test equipment as in Diagram 1(a).

Apply an RF input signal at a level of -50dBm, modulated to  $\pm 3$ kHz ( $\pm 1.5$ kHz) with a 1kHz tone.

Set the receiver line level control to give an indicated output of 0dBm.

While monitoring the waveform on the oscilloscope, increase the line level output to ensure that at least +10dBm is available before clipping occurs.

Set the line level control to give an output of 0dBm.

#### 5.6.7 MONITOR AMPLIFIER OUTPUT LEVEL

Set up the test equipment as in Diagram 1(b), including a 3 ohm speaker (capable of handling 1 watt) connected between pins 8 and 15 of the 'D' range connector.

Connect the audio EVM in parallel with the speaker and apply an RF input signal at a level of -60dBm, modulated to  $\pm 3 \text{kHz}$  ( $\pm 1.5 \text{kHz}$ ) with a 1kHz tone.

Check that at least 1.73 volts RMS is available into the speaker before clipping occurs.

#### 5.6.8 SIGNAL+NOISE TO NOISE RATIO

Set up the test equipment as in the line amplifier test Diagram 1(a).

Set the RF signal generator output to -50dBm, modulated to  $\pm$ 5kHz ( $\pm$ 2.5kHz) with a 1kHz tone. Adjust the line level to 0dBm.

Switch the modulation off.

Check that the residual noise is less than -45dBm at the line output.

Note: It may be necessary to increase the meter sensitivity to get a satisfactory reading.

#### T375 Parts List

### SECTION 6 PARTS LIST

## 6.1 GENERAL

The 10 digit numbers (000-00000-00) in this parts list are 'Internal Parts Numbers' (IPN's).

Your spare parts orders can be handled more efficiently if you quote equipment type, circuit reference, IPN and give a brief description of the part.

The parts list printed below is for all versions of the T375. Different versions have different sub-groups. Check the model of your equipment (printed on a label on the back of the equipment). To find the correct part refer to the sub-groups listed for your equipment. The same circuit reference may be listed in more than one sub-group, but it will only be correct in the sub-group listed for your version.

This section contains a list of the various T375 versions and their sub-groups.

	VERSION	SUB-GROUPS	
1.	<u>T375/02</u> :	B375 B375/AI B375/M B375/W C375/XO C375/X6 C375/O2 C375/B C375/M B3535 C3535/O2	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 6.25kHz Increments Add To B375 For 138-174MHz T375 15kHz IF Bandwidth Parts Add To B375/M For Single D Plug Fitting B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts
2.	<u>T375/05</u> :	B375 B375/AI B375/M B375/W C375/XO C375/X6 C375/02 C375/C C375/M B3535 C3535/02	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 6.25kHz Increments Add To B375 For 138-174MHz T375 7.5kHz IF Bandwidth Parts Add To B375/M For Single D Plug Fitting B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts
3.	<u>T375/15</u> :	B375 B375/AI B375/M B375/W C375/XO C375/X6 C375/12 C375/C C375/M B3535 C3535/02	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 6.25kHz Increments Add To B375 For 174-225MHz T375 7.5kHz IF Bandwidth Parts Add To B375/M For Single D Plug Fitting B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts

# T375 Parts List

4.	T375/22:	B375 B375/AI B375/M B375/W C375/XO C375/X6 C375/02 C375/B C375/M2 B/TA-500/M2 B3535 C3535/02	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 6.25kHz Increments Add To B375 For 138-174MHz T375 15kHz IF Bandwidth Parts Add To B375/M For Double D Plug Fitting SMD Diode Memory PCB Parts B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts
5.	<u>T375/25</u> :	B375 B375/AI B375/M B375/W C375/XO C375/X6 C375/O2 C375/C C375/M2 B/TA-500/M2 B3535 C3535/O2	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 6.25kHz Increments Add To B375 For 138-174MHz T375 7.5kHz IF Bandwidth Parts Add To B375/M For Double D Plug Fitting SMD Diode Memory PCB Parts B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts
6.	T375/42:	B375 B375/AI B375/M B375/W C375/XO C375/X6 C375/12 C375/C C375/M2 B/TA-500/M2 B3535 C3535/02	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 6.25kHz Increments Add To B375 For 174-225MHz T375 7.5kHz IF Bandwidth Parts Add To B375/M For Double D Plug Fitting SMD Diode Memory PCB Parts B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts
7.	<u>T375/55</u> :	B375 B375/AI B375/M B375/W C375/XO C375/X5 C375/02 C375/B C375/M2 C375/55 B/TA-500/M2 B3535 C3535/02	T375 Common Parts T375 Common Auto-Insert Parts T375 Mechanical Parts T375 Wire List T375 Standard Crystal Oscillator Parts Add To C375/XO For 5kHz Increments Add To B375 For 138-174MHz T375 15kHz IF Bandwidth Parts Add To B375/M For Double D Plug Fitting Add to T375 For /55 Version SMD Diode Memory PCB Parts B3535 Audio Processor Basic Parts B3535 3.4kHz Filter Parts

# 6.2 B375

## T375 PCB COMMON PARTS

## 6.2.1 TRANSISTORS

INT	ERI	IAL		AR	Ť į	ŲΟ.	_			QTY/ <sub>SET</sub>		DESCRIPTION		REFERENCE	CH/N
-				. :				_		·					
0	0	0	 5	 ව	0	2	0	1	1	2	BF494-	<u> </u>	ĤΓ	Ø14, Ø30	
0	0	0	0	o	0	<u>٠</u>	1	7	0	1	9273G	4		05	
0	0.	Q	0	0	0	2	0	3	5	1	BF324	4		012	
0	0	0	0	0	0	2	2	5	5	1	2N5484	•		φ28	
_0	0	0	0	0	0	3	1	Z.	5		35K67K	•		φ23	
ō	0	O	0	0	0	3	1	ક	6	1	мрфног.	h		Φ <u>2</u> 9	
0	0	0	0	٥	0	3	2	1	5	1	BFR91			φε	
0	0	Q	٥	0	0	3	3	1	0	-3	T310	TRANSISTOR		φ11, φ12, φ22	

## 6.2.2 DIODES

٥	0	1	o	0	0	i	0	2.	6	4-	BA482 PIODE	D10, D11, D13, D14
0	0	1	0	0	0	1	1	6	0		\$R2607	791.
٥	0	1	Q.	0	 _0	1	2	0	1	0.001	1N4148' DIODES	-D24 (VERT)
6	0	1	0	0	0	1	2	5	3	1	BB405 VARICAR	DI6
_	0		0	0	O	1	5	1	4	1	B2X79/c5V1 ZENER	D12
LΩ	0	1	0.	0	O.	. 1_	2	1	1	1	BZX79/25V1 ZENEK	Die

## 6.2.3 INTEGRATED CIRCUITS

0	2	0	0	0	1	2	4-	9	1	MLM 358 INT CCT	Te2
0	2	0	0	0	1	4	4	0	1_1	MLM 324 THE CET	ICS
0	2	0	0	0	1	4	5	ઉ	. 1	78LØS INT CET	91
.0	2	0	٥	O	1	4	7	o	1	3357 INT CET	IC1
0	2	0	0	0	1	7	5	0		MC12016 INT CET	IC4 98/S-13 <sub>4</sub>
0	2	0	0	0	1	7	6	o	1	145152 INT CCT	Ic3
	0 0 0	02	0 2 0	0 2 0 0		0 2 0 0 0 1	0 2 0 0 0 1 4 0 2 0 0 0 1 4 0 2 0 0 0 1 4	0     2     0     0     0     1     4     4       0     2     0     0     0     1     4     5       0     2     0     0     0     1     4     7       0     2     0     0     0     1     7     5	0     2     0     0     0     1     4     4     0       0     2     0     0     0     1     4     5     8       0     2     0     0     0     1     4     7     0       0     2     0     0     0     1     7     5     0	0 2 0 0 0 1 4 4 0 1 0 2 0 0 0 1 4 5 \$ 1 0 2 0 0 0 1 4 7 0 1	0 2 0 0 0 1 4 4 0 1 MLM 324 INT CET  0 2 0 0 0 1 4 5 & 1 78L\$ INT CET  0 2 0 0 0 1 4 7 0 1 3357 INT CET  0 2 0 0 0 1 7 5 0 1 M(12016 INT CET

### 6.2.4 CAPACITORS

٥	١	1	5	1	1	0	0	0		1	100 CAP PIOD SO/631 CERAMIC	C42A	38/01-632
0	1	1	5	1	4	2.	0	0	1		102 CAP -P100 to 25P 50/63V CER	.c105	
0	1	1	5	L	2	7	0	0	1	2_	2P7 CAP NPO 105P 50/30 CER	<u>C42</u> , <u>C99</u>	
0	1	\$	5	1	-3	9	0	٥	1	i	399 GAP NPO ±0.5P 50/63V COR	<i>c</i> 20	
0	.1.	1	5	-3	4	2	0	0	1	2	120P CAP NIST 5% 50/63 CER	C43,C45	
٥	1	1	5	4	1	0	0	Э	4	1	1m cap T/c B 10/ 63V cer	c7	
		Ť				Ŀ	Ŀ						
0	2	٥	٥	7	1	0	0	0	2	1	in the sov electro syliming	C61,	87/12-473

## T375 Parts List

0	2	0	D.	7	2	2	0	0	1	1	2M2 CAP SOU ELECTRO SXIIMM	CII9		
0	2	ပ	٥	7	4	7	0	0	2		447 CAP 50V ELECTRO 5:11	<75°		
_										ļ			$\perp$	
의	2	0	0	જ	1_	0	0	0	3	4	LOM CAP SON ELECTRO 5 + 11mm	C4, C81, C89, C90		
$\vdash$				_								_ <del> </del>		
0	2.	O.	Q.	8	4-	7	0	0	2.	1	474 CEP LOV ELECTRO 6 Y LLMM	<u> 41</u>	-	
-							_		_	<del> </del>				
1	2	0	0	9	-1	Q	0	0	3	1.	loop cap 160 Electro 8x11mm	c73		
	2		-	9	4	7	>	_	2		Allow and the state of the stat		╅╾┪	
Ť			_ <u></u>		_	-	_				4704 CAP 16V ELECTRO 10+20mm			
0	2.	2	Jo.	4	4	Ð	0	0	1		IN 5 CAP 10% SOV MYLAR	C117		
	<u>L</u> .													
0	2	2	5	4	_4	7	0	٥	Ţ	4	4~7 CAP 10% GOV MYLAR	C77, C84, C94, C128		
$\vdash$	_									<b>!</b>	·		_	
0	2	5	5	5	1	5	0	_1	0	2	150 CAP 10% GOV MYLAR POTTED	c70, c118		
-							_	$\vdash$					$\dashv$	
٥	2	5	(f)	6	1	0	0	0	1	3	toon cap 10/ 500 MYLAR	C60, C80, C85	-	{
		_	_					_	_	<del> </del>			+	
	.2:	3	٥	7	1	<u>o</u>	-	ပ	1	F	IN CAP 35V TANT BEAD	C120		
0	2	5	-	6	4	6			╎	1	day and 1/1/ most dam	<b>C</b> 3	+	
Ť			-×-	$\dashv$		Ψ.	_				100 CAP 16V TANT BEAD	<u> </u>	$\dashv \dashv$	
												<b></b>		

## 6.2.5 RESISTORS

0	3	0	۷	2	4	7	0	2	0	1	47E RESISTOR 5% FILM 4.1.600 R80	-	87/11-140
٥	3	0	5	3	1	2.	0	2	0	11	120E RESISTOR 5% FILM 4x1.6mm PI 755	<del> </del>	
Q	-3	0	5	-35	4.	7	0	2	0	2	470G	-	
0	3	0	وا	3	5	6	0	2	0	Т	560E - 7853	#	
0	3	0	5	3	당	2	0	2	0	1	820E	-	
	3	0	5	4	Ļ	&	Q	2	0	Ĺ	1K8		
	3										10K RESISTOR 5% FILM 4+1.6mm R64 (0.4 LEAD SPACE)	$\perp$	
0	3	Ċ	5	5	1	5	0	2	o	2.	15K " " " " " " " " " " " " " " " " " " "	-	
0	3	0	5	6	3.	9	0	2	0	1	39k ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '	-	81/12-475
Ŀ	-3	0	5	.5	2	2	0	2	0	1	22K - " 7861 (0.4 LEAD SPACE)	- -	
0	73	0	5	151	3	3	٥	2	0	-3	33K " TRIOO, RI14, R122	#	
0	3	0	5	5	5	6	0	2,	0	<u> </u>	56K		
0	3	υ	15	5	6	g.	0	2	0	3	48°E		
6	3	0	5	6	4	7	0	2	0	_11	470K		87/12-475
F													
0	4	2	0	5	4	7	0	ō	9	1	SOK PRESET RES. FLAT, CERMET RVIIZ	+-	
0	4	5	0	4	4	7	0	0	ı	4	4K7 NTC RES. SHIM DISC 72128		
ا ا	اا	L1	I. "	l	L	l	L.,	l.,	1 1	II		- 1	]

## 6.2.6 COILS

٥	ป	0	0	0	0	1	6	2	9	2	COIL TAIT NO 629	414,415		]
L														
0	ы	0	O	0	0	4.	G	J.	1	3	COIL TAIT No 631	L19 L20 L21		
o	ď	٥	O	0	Ĉ	4	6	В	3	4	COIL THIT No 633	427	$\Box$	
0	Ω	0	S	0	0	1.	6	4	2	6	COIL TAIT No 642	64,65,67,68,69,613	[	
										<b>]</b>				

#### 6.2.7 MISCELLANEOUS

٥	6	2	0	0	٥	_1	0	1	_	6	CAN 14mm <sup>2</sup> v.19mm	FOR 647 COILS		
0	5	6	0	.0	0	2.	1.	0	1	5	JUSH EXD IND	16, 112, 124, 125, 126		
0	5	6	0	o	0	2	T	0	2	2_	logul Fro IND	11, 128		
0	5	<u>6</u>	0	0	0	2	ત	0	2	<u>i</u>	68mH Fro IND. SHIELDED	L31_	H	
							-						H	
			0							1	PRINTEP CCT BORRD		$\Box$	
2	3	0	0	0	0	1	0	1	9	2	SWITCH X 8' SPST DIP			
2	4	0	0	0	٥	2.	O	5	7		HEFDER IOWRY 1 ROW		<del>                                     </del>	
2	4	0	0	0	0	2	0	5	9		HEADER BURY I ROW			
2	4	0	0	4-	0	2	0	5	7	1	SKT JOWAY I BOW PEB MIG TOP EMTRY		$\coprod$	
					F	<u> </u>	<u> </u>	F	ļ.,	ļ				
2	.7	4	0	0	0	1	0	0	2	1	CRYSTAL 20.945 MIZ TE 15		+	

# 6.3 B375/AI T375 PCB COMMON AUTO-INSERT PARTS

### 6.3.1 TRANSISTORS

[	0	0	5	0	0	1	1	1	o	6	BC5478 TRANSISTOR	P.T.	φ2. φ3, φ19, φ21, φ25, φ26	
9	0	0	5	0	0	1	1	-3	0	1	BC 5578	<u>.</u>	<b>04</b>	

#### 6.3.2 DIODES

												<u> </u>							- Т		- 1
Į		0	4	5	0	6	4.	2	0	4	0.003	1 Tike	114148	DIODE	5	Œ	11	D6, D9, D12,		<u> </u>	2
- 1	1	_	_		_							1								<u>.</u>	_]

### 6.3.3 CAPACITORS

									·		
0 1	. 1	5	1	4	7	0	0	í		487 CAP MPD 10-5P 50/63V CER AT. C101	
0 1	1	5'	2	1	0	0	O	1	2	10P CAP NPD 5% 50/63V CER AT C59, C63	
0 1	1	5	2	2	7	0	0	i	J	27P CAP NISO 5/ 50/63V CER AT C57	
0 1	1	5	2	3	9	o	0	<u>)</u> .	1	39P CAP NISO 5 5 50/3V CER AT CSE	
0 1	. <u>l</u>	5	3	1	0	0	0	1	11	100P CAP NISO 5/ 50/GV CER AT C74	
0 1	1	5	4	ı	0	0	0	1	4	1m CAP T/C B 10% 63V CER AT C92, C100, C103, C129	\$7/10-388
0 3	L	5	4	4	7	0	1	3	0.016	17Hau 4-17 CAP T/c B, 10/ FDN CER 7152 AI C2, C6, C19, C38, C44, C49, C50  C52, C53, C56, C65, C71, C78, C91  C104, C107	
0 2	- 2	5	5	1	0	0	7	0	6	100 CAP 10% 434 MYLAR BITTED C66, C79, C82, C123, C125  C127	
0 2	2 2	5	5	2	2	0	.±	o	-3	22m CAP 10 (3V MYLAR BITED C76, C83, C93	
0 2	2	5	5	4	7	o	1.	o	-6	47m CAP 10 63V MYLAR BITED C62, C64, C72, C102, C124	
	$\perp$	I									L

## 6.3.4 RESISTORS

	1_	0	5	2	4	7	0	2	0	<u>.</u>			-9/ -		_		0.00	38/3 134 87/11-409
	3.	Š	13	5	-	-		-	Ü	5	47E R	ESISTOIL	5% Fit	-M 4v1	Gmm	RI_	R22, R28, R30, R77, R83	87/11-409
0	3	0	5	3	1	0	0	2	0	-3	100E			-	•		R12, R21, R70	
$\vdash$	<u> </u>	ļ.,	<b> </b>	ļ			<u> </u>	<u> </u>		<b></b>								
10	3	0	5	3	2.	2	0	2	0	2	220E	p			-	•	TR75, TR116	
<u> -</u>	3		<del> -</del>			<u> </u>	H	2	<u> </u>	<del></del>	330E							
ľ	1	Ŭ	3		٦	3	V		0	1	3306	<del></del>	•	<u> </u>	- <del></del>	-	J318-	<del>                                     </del>
0	3	0	5	4	1	0	٥	2	0	5	114	•			-		-R1, R51, R79, R115, R119	
_			<u> </u> _	ļ				L										
0	3	٥	5	4	٤	5	0	2.	Q	1	1×5	<u> </u>		"		<u> </u>	TR112	
ᄂ	3		_	4	2	0	0	2	0	4	2K2							33/3-114 87/10-368
Ĕ			٦		6-	_		<u> </u>	٧		ZKZ						R38, R52, R97, R110	<u>87/30-368</u>
0	3	0	5	4	3	9	0	2	0	2	3K9.				-		B24, R27	
$\vdash$	_	_	_															
۴	3	0	5	4	4	Z	0	2	0	5	4K7	~		<u>-</u> .			R10, R46, R73, R120, R133	_
<u> </u>	3	_	-		_	4	_			2					•			
٣	3		-	4	5			۲.	U		5べん	<del></del>					R29, R47	+
0	3	٥	5	4	છ	છ	0	2	0	3	6K8	-				-	R3, R26, R54	+
$\vdash$			<u> </u>															
10	3	0	5	4	ક	2	0	2.	0	1	8K2				••		TR49	
-	-3	0	_	_	4			2		7	407 =		-9-				R9,R56, ,R96,R101,R104	
		_		Ĭ		)	Ť	-	Ť		107	SESIE IOR	5 <u>5% F</u>	LM1 4 93	· Control	HI	R106, R127	17/10-38W
																	15129, 15167	
0	3	O	5	5	1	2	٥	2	0		12K		-			-	73.4	
-		$\dashv$	-				_		H	<del> </del>		<del></del>						_
0	3	0	. 5	5	2	2		2	0	7	22K		<del></del>			•	R37, R39, R57, R59, R65,	4
$\vdash$			-				_			}		<del></del>					776,705	
6	3.	0	5	5		9	_	2	0	2	39K						TR25, TR\$29	
																	1	
0	3	0	9	5	4	7	٥	2	Q	ន	47K	•	_	-			R2 . R41 . R48 , R50 , R58 ,	
$\vdash$	}	ı		Ш						ļ							R63, R74, R99	
	<del>i</del>								, ,									
	3				8				o	_1_	82K	1	•				R102	88/2.065
	3							·2 2		7	82K 100K	N	-				R102 R60, 7262, 7271, 7272, 7296,	
										7			-	,			R102	88/2.065 33/02-045
0	3	0	5	6	<u>.t</u>	0	0	2	0	7	100K		-	,	•		R102 R60, R62, R71, R72, R98, R123, R124,	
0		0	5	6	<u>.t</u>	0	0	2	0	7		14	-	,			R102 R60, 7262, 7271, 7272, 7296,	
0 0	3	0	n   n	6	1	0	0	2	0	7	100K	14	-	,	•		RIO2 R60, R62, R71, R72, R98, R123, R124, R121	
0 0	3	0	ા ગા	6	1 2	5	0	2	0	1	120K		-				R102 R60, R62, R71, R72, R98, R123, R124,	
0 0	3	0	ા ગા	6	1 2	5	0	2 2	0	1	120K		-		-		RIO2 R60, R62, R71, R72, R98, R123, R124, R121	

## 6.4 B375/M T375 MECHANICAL PARTS

0	८	٥	0	0	Ł	0	.1	į	1	LED 3mm RED		
0	८	0	0	0	1	0	1	5	1_1_	LED 3mm GREEN		
i	2	9	4	1	5	0	0	1	15	1-5 CAP CERAMIC FEED THRU		
4	0	0	5	1	0	0	0	6	1	LOK LOG POT LESS SLU. VIC	MONITOR VOLUME	
4	0	0	5	1.	0	٥	1	2	1	LOK LOG BT, L/S, SLOT SHAFT V6.	TIME TEAC	
4.	0	0	5	1.	0	0	1	5	1	LOX LIN POT L/S SLOT SHAFT VIG	GATE	
4-	4	0	5	5	o	٥	0	3	1	SOK FRESET RES. JOT PAL MIG.	XTIAL TRIM	
6	5	0	0	0	1	٥	ī	3	.15	FERRITE BERD 1.9,0.3,38mm STRCKRUC TD.		
3	0	0	0	0	1	0	0	3	1	TOGGLE SWITCH SPST		<del></del>
	0 1 4 4 4	0 8 1 2 4 0 4 0 4 0 4 4	0 8 0 1 2 0 4 0 0 4 0 0 4 0 0 4 4 0 6 5 0	0 8 0 0 1 2 0 4 4 0 0 5 4 0 0 5 4 0 5 6 5 0 0	0 8 0 0 0 1 2 0 4 1 4 0 0 5 1 4 0 0 5 1 4 4 0 5 5	0 8 0 0 0 1 1 2 0 4 1 5 4 0 0 5 1 0 4 0 0 5 1 0 4 0 0 5 5 0	0 8 0 0 0 1 0  1 2 0 4 1 5 0  4 0 0 5 1 0 0  4 0 0 5 1 0 0  4 0 0 5 5 0 0  4 1 0 5 5 0 0	0 8 0 0 0 1 0 1 1 2 0 4 1 5 0 0 4 0 0 5 1 0 0 1 4 0 0 5 1 0 0 1 4 0 0 5 5 0 0 0	1 2 0 4 1 5 0 0 1 4 0 0 5 1 0 0 0 6 4 0 0 5 1 0 0 1 2 4 0 0 5 1 0 0 1 5 4 4 0 5 5 0 0 0 3	0 8 0 0 0 1 0 1 5 1 1 2 0 4 1 5 0 0 1 15 4 0 0 5 1 0 0 1 2 1 4 0 0 5 1 0 0 1 5 1 4 0 0 5 1 0 0 1 5 1 4 0 0 5 5 0 0 0 3 1	0 8 0 0 0 1 0 1 5 1 LED 3mm GREEN  1 2 0 4 1 5 0 0 1 15 1m5 CAP CERAMIC FEST THRU  4 0 0 5 1 0 0 0 6 1 10K LOG POT LEGG ELU. VIL  4 0 0 5 1 0 0 1 2 1 LOK LOG POT LEGG ELU. VIL  4 0 0 5 1 0 0 1 5 1 LOK LOG POT LEGG ELU. VIL  4 0 0 5 1 0 0 1 5 1 LOK LOG POT LEGG ELU. VIL  4 0 0 5 1 0 0 1 5 1 LOK LOG POT LEGG ELU. VIL  4 0 0 5 1 0 0 1 5 1 LOK LOG POT LEGG ELU. VIL  4 0 0 5 1 0 0 1 5 1 LOK LOG POT LEGG ELU. VIL  5 5 5 0 0 0 1 5 1 FERRITE BEAD 1.970.3738mm STRCKPOLC TD.	0 8 0 0 0 1 0 1 5 1 LED 3mm GREEN  1 2 0 4 1 5 0 0 1 15 1 M5 CAP CERAMIC FEST THRU  4 0 0 5 1 0 0 0 6 1 10K LOG POT LEGG SLU. VIC MONITOR VOLUME  4 0 0 5 1 0 0 1 2 1 LOK LOG BT, L/S, SLOT SHAFT VG. LINE LEVEL  4 0 0 5 1 0 0 1 5 1 10K LINI POT L/S GLOT SHAFT VIG. GRATE  4 4 0 5 5 0 0 0 3 1 50K Ras set Res. 10T PNL MTG. XTAL TRIM  6 5 0 0 0 1 0 1 3 15 FERRITE BEAD 1.970.3738mm STRCKBLC TD.

2	4	0	اه	0	0	1	اه	5	5	1	PLUG ISWAY D RANGE, WIRE WRAP ANS		_	
2	4	_	_			_	_	_		4	SKT CORXING 'N' TYPE		_	
									_			م ا	_	
2	4	의	오	2	Ţ	9		Ţ.	7.		SKT CORXIAL, BNC PNC JACK	frag out.	1	
-3	0	2	0	Q	0	5	4	8	6	3	TRACKET, FEEDTHRU HTG AZM1855		$\dashv$	
3	0	2	٥	0	4-	5	٥	2	5	1	BUSH, CORY RETRINING PAM2143		_	
3	0	3	0	٥	1.	4.		4	0	2.	CHASSIS SIDE PLATE 82M1196		1	
		_					_				COVER, TO KANGE HOLE AMM!230	to degree to complete	┦	
-3											COVER, TO RANGE HOLE HOMESU	man pour a C 37 37	1	
3	0	6	0	0	٥	1	O	1.	0	2	FERRYLE (HANDLE) A4M948		$\pm$	
3	0	8	0	0	o	1.	٥	0	Z	1	HANDLE A4M949	- 1	$\dashv$	
3	1.	1	0	0	0	1	o	L	5	1	KNOTS 15- (SATO K34)	Vo.L		
-2	1	_		_	-	4	72	_	4	1	FRONT PANEL AZA 456 AZM11%		$\dashv$	
		_						L.,					$\neg$	
3	1	6	0	٥	2	1	0	9	1	<u> 3</u>	TANGL, CHASSIS REAR AZMOSI			
3	1	6.	0	0	2	1	1	7	60	4	PANEL CHASSIS FRONT H2M1995		-	
3	1.	6.	0	0	8	5	0	1	5	2.	LOCATING RM, TO RANGE RUG. A4M975		1	
3	1	9	-	0	0		1	4	9	1	SHIELD, VEO BOX ALLOY NOS ALM2229			18/5-247
							L						-	
3	1.	9	_	0	o	1	3	0	9	1	SHIELD, VCO LID 92M1655			
3	L	9	0	0	٥	1	1	-3	4	<u> </u>	SHIELT) SOUDER SIDE A2M2037		$\dashv$	<del></del>
3	1	9	٥	0	0	1	1	-3	5	1	SHIELD COMPONENT SIDE ASMEDS			
3	.1.	9		0	2	0	0	4-	5	1	SUCEVE, STD BASE STATION, A2M970		$\exists$	
_	1		<u> </u>	_	Ĺ	<u> </u>	ļ.,	<u> </u>	Ш		SPACER BUC MIG. A4M1033	, , , , , , , , , , , , , , , , , , , ,	-	
3	4-	5	0	0	0	1	0	0	2	12.	SCREW M2×6mm Part Pozi ET BZ	H		
3	4.	5	-	0	0	1.	0	0	6	4-	SCREW M2 x 16mm CH HD: ST 02	BNC MTG		
3	4	5	0	0	0	4	0	0	6	5	SCREW MBYSMM ANBOU ST BZ	'N' TYPE SKI, OS MIG (90176)		
3	4	5	o	o	0	4	0	0	9	8	SCREW M3 V6mm CSK BZI ST BZ.	SLEEVE	$\dashv$	
-3	4	=				1		1		4-	SEREW M3 - Come Pan Pozi ST BZ	SIDE PLATES CHASSIS REAR		
						Ľ								
3	4	5	Lo	9	0	14	ľ	2	0	4	SCREW M'3x8mm MUSH, SKTH) BLACK	FRONT PANIEL		
3	4-	9	<u> </u>	0	0	2	0	0	5	4-	SCREW 4-40× 4 CSK POZI TRPTITE	SIDE PLATES CHASSIS FRONT		
2	, 4	9	0	0	0	2	. 0	0	3	<del>12</del> 8	SCREW 4-40 - 4 PAN BZI TAPTITE	-PCB/CHASSIS		
	, 4									4-	SCREW M31 40mm ADN BZI TAPTITE	YCO UD		
.3	5 5	2.	0	0	٥	1	0	0	1		NUT M2 HEX	THE SHIELDS	$\dashv$	
		L	L	ļ	_	ļ.			Ļ	<b>.</b>				
[3	5	2	0	0	0	1	0	0	8~	হ	NUT M3 Hex	'N' TYPE SKT, Q5 (80136) MTG	_	
[3	5	2	0	0	0	1	0	2.	9	2	NUT M4 NYLOC	HENDLE	$\dashv$	
-3	5	2	o	0	0	1	0	4	3	2	NUT M3 LOCATING AN A4M793			
3		$\overline{}$	т—		$\overline{}$		$\overline{}$	0	6	12 4	WASHER M2.5 SPRING ZINC PLATED WASHER M2 SHAKEPROOF	RF SHIELDS BNC MTG	$\neg$	87/7 -211 88/7 :329
			<u> </u>		Ľ		L			<b></b>		,	$\exists$	
	5	3	0	0	0	1	0	1	.0.	4	WHEHER MS PLAT	<b>⊙</b>		<u> </u>
1.3	5	3	0	0	0	1	0	7	2	3	WHEHER M3 SPRING	1000000 PIN 2, 05 1		$\longrightarrow$
L	ل	_		1	1		Щ.		1	Ц	I	t	1	1

3	ਙ	3	0	0	0	1	٥	1	3	4	WASHER M3 SHAKEPROOF	N TYPE SKI		
┕			ļ				L							
13	5	4	0	0	೭	1	0	-3.	3	16	M3 PEM PASTENERS	ISSUE TO MAKINFACTURER OF METALWORK		·
<u></u>	<u> 15</u>	G	0	0	9	1	0	٥	3	m	SOUPER TAG M3 LONG.	secures on a unider res.		87/#-440
3		9		0				4		2	SMAP BUSH, BLACK. HEYCO 58-375-4			
3	6	0	0	0	0	1	0	4-	1	<u></u>	SHORTY BUSH, BLACK, HOYCO BIRT-IZE			
3	6	2	0	0	0	1	0	٥	ć	1	MICH INSULATOR TO-126	φ5 (gpαc)	_	
3	6	2	0	0	0	1	0	3	٥	2.	GROMMET 3mm LGD MTG			<del></del> -
-3	6	5	ဝ	0	0	1	0	-3	1	.1.	LABEL WHITE REMOVERBLE		-	
3	6	5	0	0	0	1	1	٥	3	1	LABEL TEST REPORT INSIDE AAA267			87/11-4017
-3	6	5	o	0	0	1	1	4	1	1	LABEL STATIC WARNING 1447320	MASTER TO RESHIELD		*****
3	6	9	0	0	0	1	٥	1	2	1.		FITTED IN V.C.O COIL (CUT BUTTOM END CH		27/4-202
-3	۵	2	٥	0	0	1	Ö	<u>.</u> 1	4	10	CABLE TIE NYLON 100+216m-			
4	.0	0	0	0	0	L	0	7	0	20mu	SLEEVING, Jum PVC	2 7 LOMM , CRALE HARNESS		
4	0	٥	٥	0	0	2	٥	0	1	320 m	SILICON SLEEVING OTHE			
4	1	0	0	0	0	1	0	4	2	1	CARTON, GOMM FXD EQUIP UEBISO96	,		48/1-025
5	3	8	0	0	0	4	0	o	1		GLATDE . FITO EQUIP SINGLE D'SKT.	busy ferred to c 375 M.	$\exists$	2×11
0									0	1	1K8 RESISTOR 5% FILM TV2.5mm	TR42 ON B3535 Rucessok	-	
10	3	읙	0	4	3	9.	٥	1.	٥	1	3к9	RI ON B3535 ALOCESSOR		

6.5 B375/WIRE T375 W	IRE LIST
----------------------	----------

2	0	9	٥	0	0	1	2	0	2	0:5gm	WIRE OFFICE TINNIED COPPER	3 × 30mm, 2 × 35mm, 3 × 40mm, 1 × 60m	
2	0	1	0	0	٥	-3	0	٥	1	440mm	WIRE 1/0.2 PVC BROWN	2 x 220mm	
2	0	1.	0	0	0	3	o	0	2	640mm	WIRE 7/0.2 PVC RED	1 × 60mm, 70mm, 90mm, 200mm, 220mm	
2.	0	1	0	0	0	3	0	0	-3	450mm	WIRE 7/0.2 PVC ORANGE	1 v 20mm, 210mm, 220mm	
2	O	1	0	0	Q	3	0	0	4-	500mm	WIRE 7/0.2 PVC YELLOW	1 - 220mm 250mm.	
2	O	1	0	0	0	3	Q	0	5	Homm	WIRE 7/0.2 PVC GREEN	1 × 110mm	
3	0	1	0	0	0	3	0	0	6	200mm	WIRE 7/0.2 PVC BLUE	1 - 200mm,	57/2-20s
2	0	1	0	0	0	-3	0	0	7	200mm	WIRE 7/0.2 PVC VIOLET	1 v 200mm	377-205
2	0	_1	0	0	0	3	٥	0	દ	58 0mm	WIRE 7/0.2 PVC GREY	( v 80mm, 200mm, 300mm	871-205
2	0	1	0	0	0	3	0	0	9	280 mm	WIRE 7/0.2 PVC WHITE	, 280mm	37/2-205
2,	0	1	0	.0	0	-3	0	1	0	435 mm	WIRE 7/0.2 PVC BLACK	1-35mm, 2,40mm, 1,60m, 260m	
2	0	6	0	0	٥	1	0	1	1	420mm	CORY CARLE RASIGU	1 . 60m, 360mm	\$7/10 · 3\$8
4	0	0	٥	0	0	2	0	0	1	250mm	0.7mm GILICON SLEEVING		

# 6.6 C375/XO T375 STANDARD CRYSTAL OSCILLATOR PARTS

0	0	1	0	0	0	1	2	5	7	1	TBTB 212 VARICAP TOIOTOE	7 <sup>17</sup>	
L													$\overline{}$
e	0	1	0	0	0	1	2	6	3	1	BB409 VARICAP DIODE	D18	
$\vdash$			-			Н	_		$\vdash$	<u> </u>			
										<del> </del>			
٥	1	1.	5	2	1	8	0	0	1	1	18P CAP NISO 5% 50/13V CER	C112	
0	1	1	5	2	2	2	0	0	3	1	22° CAP N150 5/ 50/63V CER	C109	88/3-148
$\sqcup$		J		l		ļ	. i						001.3*148

٥	L	L	5	2	5	6	0	٥	1	1	56P CAP NISO 5	ER CILO	
0	1	,				7			3	-	4m7 cap T/c B	5c cgr C111	
Q	2	8	1			$\rightarrow$		0		<u></u>	17P CAP TRIM NPO	C108	28/3-148
0	13	0	5	4	1	0	ō	2	0	Ł	IK TRESISTOR 5	1-6mm TZ84	
0	-3	0	5	.5	<u>i</u>	0	0	2	0	2_	LoK -	· 7885,7887	
0	3	0	5	5	1	5	0	2	0	1	1514 -	· RSC (0.2" Втон)	
٥	3	0	5	5	4	7	0	2	0		17K "	- R88 (0.4. bitch)	87/10-358
0	4	5	0	4	4	7	0	0	1		4K7 NTC Res. 5m.		

#### 6.7 C375/X5 ADD TO C375/XO FOR 5kHz INCREMENTS

												 	1	
			- 1	7			g						1 1	1
1 217	7   4-1	01	0	οı.	110	10	ଞ୍ଚା	1. 1	CRYSTIAL	10-24 MHZ	TE 9	 	igspace	
		$\neg$	$\neg$	$\neg$	_	11							1 7	
1 1		- 1		ŀ	ŀ	1 1	- 13	i					LJ	

#### 6.8 C375/X6 ADD TO C375/XO FOR 6.25kHz INCREMENTS

								_											•
г							Т	1			1						1	l .	1
ŀ	21	71	4-1	വ	0	$\sim$	14	I o	10	17	1		イク・をMHっ	一てもの				1	
					1	$\sim$		ı×.	<u> </u>			 <u> </u>	32.01						4
ı			- 1			1	1	1			TE .				1	ł	1	1	
		- 1						1			1					E .			

#### 6.9 C375/02 T375 138-174MHz PARTS

0	1	1	5	1	2	2	0	0	1	3	2P2_CAP_NPO ±0.5P 50/63V CER C14, C25, C30	_
0	1	1-	5	1	-3	-3	0	0	1	<u>1</u>	3P3 - : c96	
0	1	1	5 5			7		0	1	4-	4P7	8-1/12-684
0	1	1		1			ļ		1	1	6P8 c.95	
0	Ĺ	1	5	2	Ĺ	0	0	0	1	6	10P CAP NPO 5% 50/63V CER C23, C26, C27, C28, C29, C31	
0	1	1	5	2	1	2	0	0	1_	2	129 " " " 612,615	
0	1	1	5	2	1	5	0	0	1	3	150	
0	_1_	1	ر ا	2	2	2	0.	0	1	3	22P CAP N150 5/ 50/63V CER , C18, C24, C32	
0	1.	1	5	2	2	7	0	0	1	1	27P	
0	1	1	5	2	3	3	0	0	1	1_	33P	
v	1	1.	5	2	3	9	0	0	1	1	396	
O	1	5	2	2	2	2	0	٥	1	- 1	220 CAP NPO 0805 CHIP . CC11	\$7/11-484
0	2	8	0	2	J	8	0	0	1	<u>1</u>	2/187 TRIM CRP PHILIPS 2 TRG. C 98	
			_	-2	_	-	_	2	0		220E RESISTOR 5% FILM 4416mm R14	
	3	ļ	5	<u> </u>	<u> </u>	ļ	<u> </u>	2	ļ		470E	
۲		1	-	"	1	1	٦	广	۲		TUYE	

### 6.10 C375/12 T375 174-225MHz PARTS

0	1	1	0	٥	7	5	٥	Q	1	2.	OP75 CAP PAGO 10:25P 50V CER C15,C30		
$\perp$		Ш					_	$\Box$					·
0	1	1	5	_1_	2	2.	0	0	1	1	2P2 CAP NPO 10.5P 50/3V CER C96	$\vdash$	
		<u>L</u> .							$\sqcup$	ļ <u>-</u>			
ပ	1	1	5	1	2.	7	0	0	1	1	2P7 CAP	<b> </b>	<del></del> -
1				1								اـــــا	<u>.                                    </u>

0	1	1	5	1	3	3	٥	0	1		363	44-4
0	1	1	5	1	3	9	0	0	1	6	3P9 C10, C12, C15, C17, C21, C33	
o	1	1	5	1	4	7	0	0	1	2	467	
0	1	1	5	1	5	6	0	0	1	5	596	88/1.031
0	L	1	5	1	6	8-	0	0	i	4	6PS	
0		1	5	2	1	2	0	0	1	- 1	12P CAP NPO 59/ 50/20 CER C41	
0	1	_1.	5	2	1	5	0	0	3	1	J5P	
0	1.	1	5	2	1	6-	0	0	1	2	18P CAP NISO 5/ 50/3V CER . C18, C40	88/1-631
0	1	1	5	2	2	2	0	0	1	1_1_	220	
o	1	1	5	2	3	9	0	0	J	1	39P CAP NISU 5/ 50/3V CER C34	
0	1	5	2	2	1	8	0	٥	1	1	18P. CAP NPO SOV 0805 CHIP CC11	\$8/1-031 \$7/0-484
0	2	ষ্ট	o	2	1	0	0	0	7	1	2/10P TRUM CAP. 6x8nn PHOSO02. C98	83/5-240

# 6.11 C375/B T375 15kHz IF BANDWIDTH PARTS

0	1	1	5	2	$\overline{I}$	5	0	0	1		15P CAP NPO to 5P 50/3V COR C55	5-1/1-607
<u> </u>	<u>Ļ</u> .		L		<u>L</u> .				L			
٥	1.	1	5	2	5	6	0	0	1	1	56P CAP NISO 5% 50/63V CER C48	
0.	1	ı.Ł	5	3	1	5	0	0	j	_T ,	150P CAP N150 5% 50/63V CER C47	
2	3	0	5	3	8	2.	0	2	0	Ĺ	820E RESISTOR 5% FILM 4V1-6mm TR23	37/11.407
0	3	0	5	4	1.	<u>.c.</u>	0	2	0	1	1K	
0	3	0	5	4.	Ţ	જ	0	2.	0	1.	ike Rai	
	3									1	3K9 - 7R36	
	3			<del>4</del> 5				2		1	5K6 (£34) 22K R40	17/11-409 11/11-413 17/9-144
0	-3	0	J.O	5	6	જ	0	2	0	11	G814	
0	5	6	0	0	0	2	1	0	0	1	343 FAD THO LIS	
2.	7	6	0	0	0	1	0	4	0	T	CRYSTEL FILTER 8P ISKHZ (21F15D) XF1	
2	7	6	0	0	0	1	0	4	7	4	CRYSTAL FILTER 2P 15KHz (21N15A) XF2	

# 6.12 C375/C T375 7.5kHz IF BANDWIDTH PARTS

0	1	1	5	1	3	3	0	Q	1		303 CAP PLOTO \$0.5P 50/63V CER	cs1 c35	1/11-409
		_		ļ. <u>.</u> _			_	<u> </u>					
<b> </b> i	$\dashv$					<b>-</b> -		ļ.		ļ			
0	ı	1	5	1	2.	7	0	0	1	   <del>-</del>	2P7 CAP NPO ± 0.5P 50/C3V CER	€46	
0	1.	1	5	2	5	6	0	0	1		56P CAP NISO 5% 50/63V CER	C4-8	
0	1	1	5	3	1	0	٥	0	1	1	100P CAP NISO 5/ 50/63V CER	C47	
의	3	의	5	3	4	-7	0	2	0	1	ATOE RESISTOR 51/2 FILM 4x 1.6~	R23 87	1/11-409
9	3	의	5	3	ନ	2	0	2	0	1	8205 RESISTOR 5/ FILM 441-6mm	, TR31	
۵	3	٥	5	3	Ĺ	5	0	2	0	_4	1506 " " "	0-4	1/1-403
اب ا	3	٥l	5	4	1	5	0	2	0	1 1	IKS -		

3	ا ه	5 4	12	2	0	2	0	1	2 K 2	4,	4	*	•	R33		-7
-3	0	5 5	4	. 7	٥	2.	۵	1	47.L			-	•	TR40	1 189	9-342
-3	0 !	5 4	2	2	0	2	0	1	220K				-	<b>™94</b>		
7	6	0 9	> 0	1	0	4	1	1	CRYSTAL	 ਸਪਾਵਨ ਨੂ	P 7.5KH2	<u>.                                    </u>	21F7-5D	XF1		
7	6		) c	1	-	4	6	1	CRYSTAL	FILTER 2	P 7.5KH	•	(21N75A)	xF2		
•	3 7	7 6	3 0 5 6 3 0 5 6 7 6 0 6	3 0 5 5 4 3 0 5 6 2 7 6 0 0 0	3 0 5 5 4. 7 3 0 5 6 2 2 7 6 0 0 0 1	3 0 5 5 4 7 0 3 0 5 6 2 2 0 7 6 0 0 0 1 0	3 0 5 5 4 7 0 2 3 0 5 6 2 2 0 2 7 6 0 0 0 1 0 4	3 0 5 5 4. 7 0 2 0 3 0 5 6 2 2 0 2 0 7 6 0 0 0 1 0 4 1	3 0 5 5 4 7 0 2 0 1       3 0 5 6 2 2 0 2 0 1       7 6 0 0 0 1 0 4 1 1	3 0 5 5 4 7 0 2 0 1 47 K 3 0 5 6 2 2 0 2 0 1 220K 7 6 0 0 0 1 0 4 1 1 CRYSTRU	3 0 5 6 2 2 0 2 0 1 220K 7 6 0 0 0 1 0 4 1 1 CRYSTAL FUTER &	3 0 5 6 2 2 0 2 0 1 220K	3 0 5 5 4 7 0 2 0 1 47 K	3 0 5 6 2 2 0 2 0 1 220K	3 0 5 6 2 2 0 2 0 1 220K	3 0 5 5 4.7 0 2 0 1 47.L

## 6.13 C375/M T375 SINGLE 'D' PLUG FITTING PARTS

3	0	3	0	0	2.	-3	0	9	1	1	COVER, D RANGE HOLE ALMIZZO
3	4	9	o	0	0	2	0	0	3	. 2	SERBU 4-40×/4- PAN POZI TRAPTITE THOLE COVER.
5	-3	હ	0	o	0	4	0	Q	1	1	GUIDE POD EQUIP SINGLE D'SKT
1										ll .	

### 6.14 C375/M2 T375 DOUBLE 'D' PLUG FITTING PARTS

0	1	2	0	4	1	5	0	0	1.	15	1-5 CAP, CERPMIC RECOTHRU LEADLESS			
0	6	5	0	0	0	1	0	1	-3	15	FERRITE BERD 1.9 40.9 43.8mm 70			
2	0	1	0	0	0	-3	0	0	5	180mm	WIRE 1/0.2mm PVC GREEN			37/10 -388
2	0	1		0	0	-3	0	0	٦	180mm	WIRE 7/0.2mm -PYC BLUE			
2	O	3	0	0	0	-3	0	0	7	180mm	WIRE 7/0.2mm TVC VIOLET			
2	0	1	0	0	0	-3	0	0	જ	180mm	WIRE 1/02mm PVC GREY		-	"
2	4	0	0	٥	0	1	0	5	5	1	TRUG SEWAY D'RANGE W/W PINS.		_	
3	0	2	0	0	0	5	1	ક	6		FEED THRU BRACKET A2M1855		$\dashv$	
3	1	6	0	0	ક	5	0	1	5	2	LOCATING PIN A4M775			
3	4	5	0	0	0	4-	0	O	9	5	SCREW M3×6mm Hani Pozi ET 82.	BAKT WIG		
3	5	2	0	0	0	1	0	0	જ	2	NUT M3 HEX	BEKT HIG		
3	5	2	0	0	0	1	9	4	3	2	NUT LOCATING PIN A4M793	· · ·		
3	5	3	0	0	С	1	0	1	2.	2	WASHER M3 SPRING	LOCATING ANS		
3	5	-3	0	0	0	3	0	1	3	2	WASHER M3 SHAKEROOF	BRKT MIG.	_	
3	6	9	0	0	0	1	0	1	4	3	CABLE TIE, NYLON. 300 + 2 5 mm.			
4	0	0	0	0	0	2	0	0	3	40000	Jm. SILICON SLEEVING.			
5	3	8	0	0	0	1	0	0	ક	1	GUIDE WITH TWO TO PANCE SKTS.			
1			l					ı		lf .		L	!	

### 6.15 C375/55 T375/55 VERSION PARTS

3	6	5	0	0	0	1	3	2.	5	3	LABEL T375/55 FCC ID A4A544	_	
												L	
а	6	5	٥	0	1	0	0	0	2	1	LABEL BLANK METAL POLY 12.7 + 25 LABEL NO. 247 AUA585, AUST DOC	L	88/5-232
	L											ᆫ	L

## 6.16 B/TA-500/M2 SMD DIODE MEMORY PCB PARTS

0	0	1	1	٥	0	0	0	7	0	32	BAY70 DIODE	Τ	
0	-3	6	1	٥	0	0	0	0	0	9	ZERO OHM RESISTOR OSOU CHIP		88/4-198
2	2	5	0	0	0	1	1	7	1	<u> </u>	PCB TA-500/M2 (6 POBSPEK ARRAY) STENCH REQUIRED FOR MODULTIES		
2	4-	0	0	0	0	2	0	5	7	1	HEADER, LOWAY I ROW PER MIG.	-	
2	4.	0	9	4	٥	2	0	5	7	1	SKT 10 WAY I ROW PERMIC. TOP ENTRY		

# 6.17 B3535 B3535 AUDIO PROCESSOR BASIC PARTS

### 6.17.1 TRANSISTORS

00	0 0	0	0	1	0	6	6			BC 337-40 TRANSISTOR	Φ3, Φ4, Φ5	3
00	0 0	0	0	1	1	1	0			BC548B \	Q2.	1
00	0 0	0	0	1	1	3	0	82/09-371	_	BC557B. "	Q1 01A	2
		<u> </u>										

## 6.17.2 **DIODES**

l	o	<u>)                                    </u>	<u> 110</u>	0	٥	1	1	7	0			1N4001	THATE		74 72 77 74 75		5	ı
	- 1-		_								_		2.020		1 U = U = U = U = U = U = U = U = U = U	i		1
ŀ	O C	211	ļο	0	Q	1	2	0	0	82/09-371		IN 4148	Diode		714		4	Į
t	1	- t		1 1		- 1				, .					+ <del></del>		_1_	Į.
Ė	ı,	ı	.I	∟ŧ										4 + 2	į .			ı

# 6.17.3 INTEGRATED CIRCUITS

- 1	ـ اسـ	I _		t I				1.			1	•		
- 1	<u> </u>	12		_	0	1	4	<u> </u>	0		1	LM377 INT CET	1 7.0	
- 1		1	1 -										<del>  _   _</del>	1
,		<b>-</b>	l				_	<u> </u>	_				1	
ı			10		_		ا ہا	ا ما		j			†	
ŀ	<u> </u>		1	0	$\vdash$	٠.	4+		$\sim$			MLM324P INT CCT	I T.C. 1	1
- 1		1	1 1								ĺ		1,	
- 1		<del>-</del>	•			_		<b>!</b> i	-		_		<u> </u>	
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٠,	. —					٠	'	•				· · · · · · · · · · · · · · · · · · ·	1	

# 6.17.4 CAPACITORS

$\Box$		Γ.	1	r =		Γ		_	т-			
의	1	1	으	3	1.	5	0	9	1		150P CAP N150 63V 5% C4	1
0	į	1.	0	4	1	0	0	0	1		In CAP T/C B 63V 10% C1, C3	2.
0	1	1	0	4	4	7	0	7	3	83/11-1038	4m7 ear T/e B 10% c16, c18, c30	3.
0	9	0	0	7	1	0	0	0	2	\$3/11-1030 84/02-089	14 CAP 50V ELECTRO 5 × 11m VERT C2 , C17, C20, C27	4
0	2	0	0	ક	1	0	٥	0	3		10 M CAP SOV ELECTRO 5×11mm VERT C5, C11, C24	3
0	2	0	0	9	1	0	٥	0	3		100 M CAP 16V ELECTRO 8×11mm VERT CG, C21, C22	3
0	2	٥	0	9	2	2	0	0	1		22011 CAP 16V ELECTRO 10x12.5mm VERT C26, C28	9
	_						_					
					_	-						
0	2	2	٥	4	4	7	0	0	1	-	4~7 cgp 500 Mylar VERT <9	1
0	2	2	0	5	1	0	٥	0	1		101 CAP 50V MYLAR VERT C12, C19	2
0	2	9.	0	5	4	7	٥	٥	1		477 CAP 50V MYLAR VERT C7 C8, C10,	
0	2	9	0	6	1	0	0	0	4		100-7 CAP 50V MYLAR VERT C23, C25, C29	3
0	2	2	0	6	4	7	0	0	1		470 m CAP LOUV PETP VERT CIP	

### 6.17.5 RESISTORS

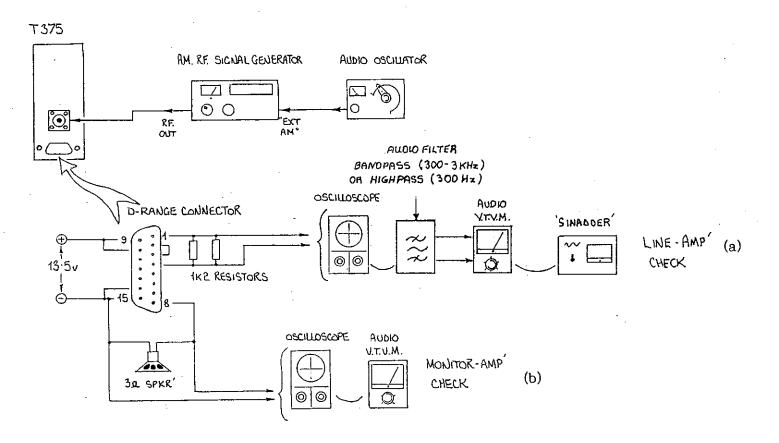
			,			,		,	<del>, ,</del>				
0	3	<u> </u>	0	1	2	2	0	0	0			2E2 RESISTOR 7×2.5mm 5% C/F -1834	1.1
0	3	ା		1	3	3	Ç	0	0			363 " " " R35A	1_1_
0	3	ા	- 	2	3	9	0	0	٥			39E 1735	1
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0	<u>-</u> †		그		7	0	~	0				680E	1
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$\vdash$	$\dashv$	-1	$\dashv$		_	_	_	-	-	-		9405	1
٥	ᆁ	악	의	-3	9	1	٥	0	9			910E	┨╌┸╌┨
$\vdash \vdash$			_			_			├-	22/04-271			╀
0	3	의	0	4	1	0	0	0	O	82/09-371 C/N 2094		1K " " " B2A R2, R2O, R23, R25	<del>∤ .                                   </del>
	_	_					_	<u>L</u> .				R36, R37, R41	8
		_1					_	_		<u> </u>			₩
0	3	اه	0	4	2	2.	0	0	0	C/N 2094		2K2 " " " " R1 When used with T335 and T355 R1 becomes 1K partol m-053/1 rosegate Rit. When used with T375 R1 becomes 3K9 — Refer R375/M.  7K9 R17	11
	7	$\neg$							Ī.,	82/03-080		When used with T335 and T355 R1 becomes 1K part of TA-053/1 roisegate kit.	
	- 1	ᆲ	0	4	-3	0	6	0	6	I		3K9 - R17	1
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		ᆲ	_			-	Ĺ	0				4K7	1
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Н	_	$\dashv$	_	_	_	<del>  _</del>	_	٥	+-	-	_	5K6 - " R16	1
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0	3	의	이	4	6	8	0	10	10	82/09-371		6K8 R3, R6, R10, R3A	┼╬┈┤
<u> </u>		-	$\vdash$			<u> </u>	⊢	⊢	-				<del>  </del>
								0		<u> </u>		8K2 75	-1
0	3	0	0	5	1	0	2	0	0	ļ		10K RESISTOR 7 4 2:5 mm 5% C/F R4, R13, R15	3
						L.		┖					1
0	3	0	0	ษ	1.	9	0	0	0	82/03-080		12K When used with 1335 and 1355, 1214 becomes 560K part of TR-053/1	3
											<u> </u>	when used with 1335 and 1355, 1214 becomes 560k part of 11453/1	<u> </u>
0	-3	0	O	5	-3	9	6	0	0			39K	1
1	T		_				$\Box$	1					<u> </u>
0	굯	긁		=	1	<b>-</b>		0	10		<u> </u>	47K R24, R26	2
۳		∽	⊢		-	<del>                                     </del>	۲	Ť	†				
-	_	ᆜ	<u> </u>		-	L	-	0	t		<b></b> -	100K	1
10	3	0	ုပ္	9	1-1-	۱-	ا ا	10	†Չ	<del> </del>		100%	1
1—	$\dashv$		<del> </del>	<del> </del>		ļ	⊢	╁	┼-	<del> </del>	<del> </del>	100V	3
0	3	0	0	6	1	8	0	0	1으	ļ		180K " R9, R11, R12	1 -
4	_		<b> </b>	<b> </b>	<b> </b>	ļ.—		<b>.</b>	<del> </del>	<u> </u>	_		<del>  _</del>
0	3	0	0	6	4	17	0	0	10	L		470K	4-
			[		1			1	1	I	L.,		

### 6.17.6 MISCELLANEOUS

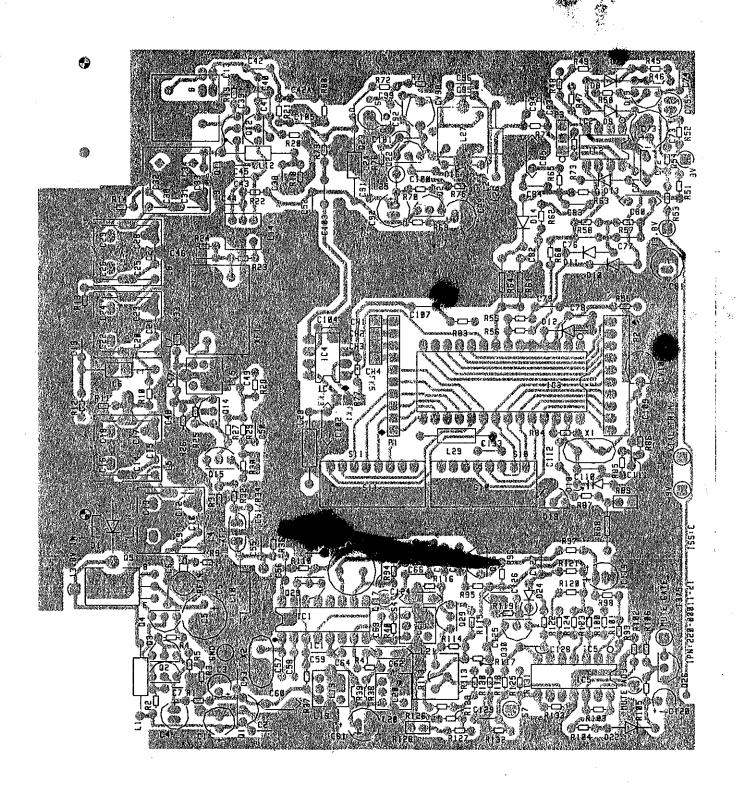
0	0	0	0	0	2	0	0	1	82/09-371	SÍLICON SLEEVING 0.7		50 mm
5	3	0	0	0	1	Ö	1	7		TRANSFORMER T 4030	T1.	i
0	0	o	0	0	1	0	0	5	82/09-371	TINNED COPPER WIRE 0.5	<u> </u>	60 mm
2	٥	0	0	0	1	Ю	ы	9		PCB T3535 RX PUDIO PROCESSOR		1
3	フ	0	0	٥	1_	0	2.	2	83/12-1068	RELAY DIL DADT 12V FLAT ACE MT		1
0	8	0	0	1	3	0	2	4		HERTEINK FOR LM377 A4M 1131		1
	15 O st	5 3 0 0 2 0	5 3 0 0 0 0 2 0 0 3 7 0	5 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	5 3 0 0 0 0 0 0 0 2 0 0 0	5 3 0 0 0 1 0 0 0 0 0 1 2 0 0 0 0 1	5 3 0 0 0 1 0 0 0 0 0 1 0 2 0 0 0 0 1 0	5 3 0 0 0 1 0 1 0 0 0 0 0 1 0 5 3 7 0 0 0 1 0 2	5 3 0 0 0 1 0 1 7 0 0 0 0 0 1 0 5 2 0 0 0 0 1 0 2 1	3 7 0 0 0 1 0 2 2 8412-1068	5 3 0 0 0 1 0 1 7 TRANSFORMER T 4030 0 0 0 0 0 1 0 0 5 82/09-371 TINNED COPPER WIRE 0.5 2 0 0 0 0 1 0 5 9 TCB T 3535 RX AUDO PROCESSOR 3 7 0 0 0 1 0 2 2 83/12-1068 RELAY DIL DADT 12V FLAT RES MT	5 3 0 0 0 1 0 1 7 TRANSFORMER T 4030 T1 0 0 0 0 0 1 0 0 5 82/09-371 TINNED COPPER WIRE 0.5 2 0 0 0 0 1 0 5 9 PCIB T 3535 RX PUIDO PROCESSOR 3 7 0 0 0 1 0 2 2 83/12-1068 RELAY DIL DPOT 12V FLAT PCB MT

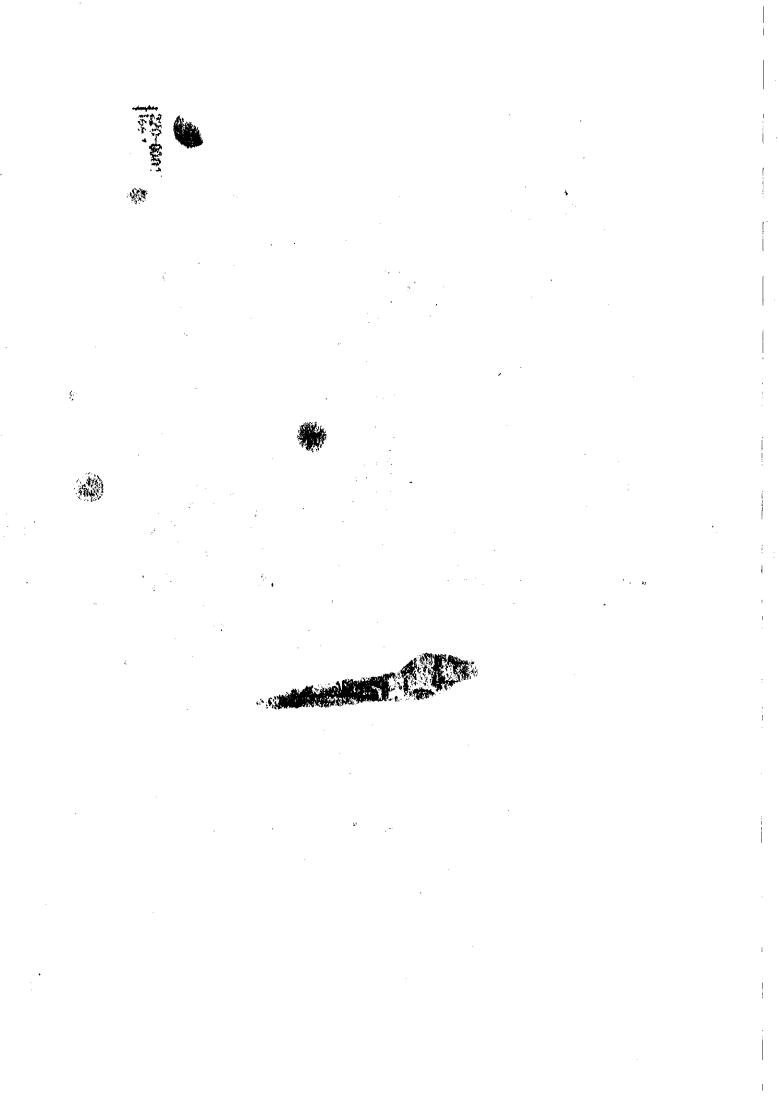
### 6.18 C3535/02 B3535 3.4kHz FILTER PARTS

3	9.	2	0	4	3	3	0	0	1		3m3 CAP 50V MYLAR VERT C15	1
-	2	2.	0	5	4	7	O	0	1		 47n CAP 50V MYLPR VERT C13, C14-	2.
	3	0	0	-3	ন্ত	9_	0	0	0		 820E RESISTOR 7×2.5mm 5% C/F -R21	1
	5	6	0	0	0	2	2	0	2	82/03-087	 FXD IND 65mH TYPE 202 SHIELDED IS	1
	2 3	0	0	4	 2	2	0	0	0	2/N 1704	2K2 RESISTOR 7, 2.5mm 576 C/F R 22	1
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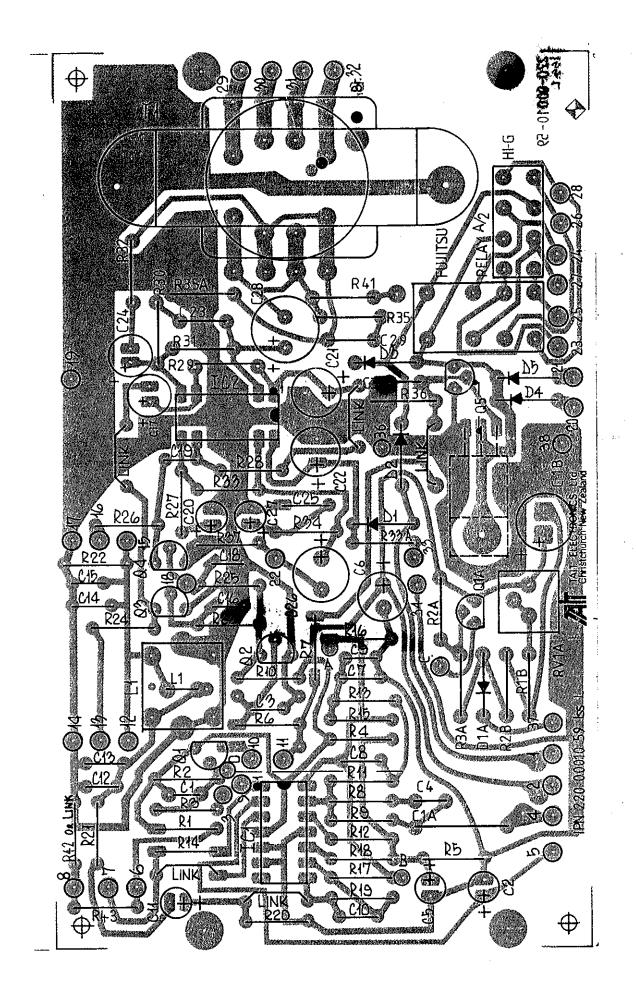
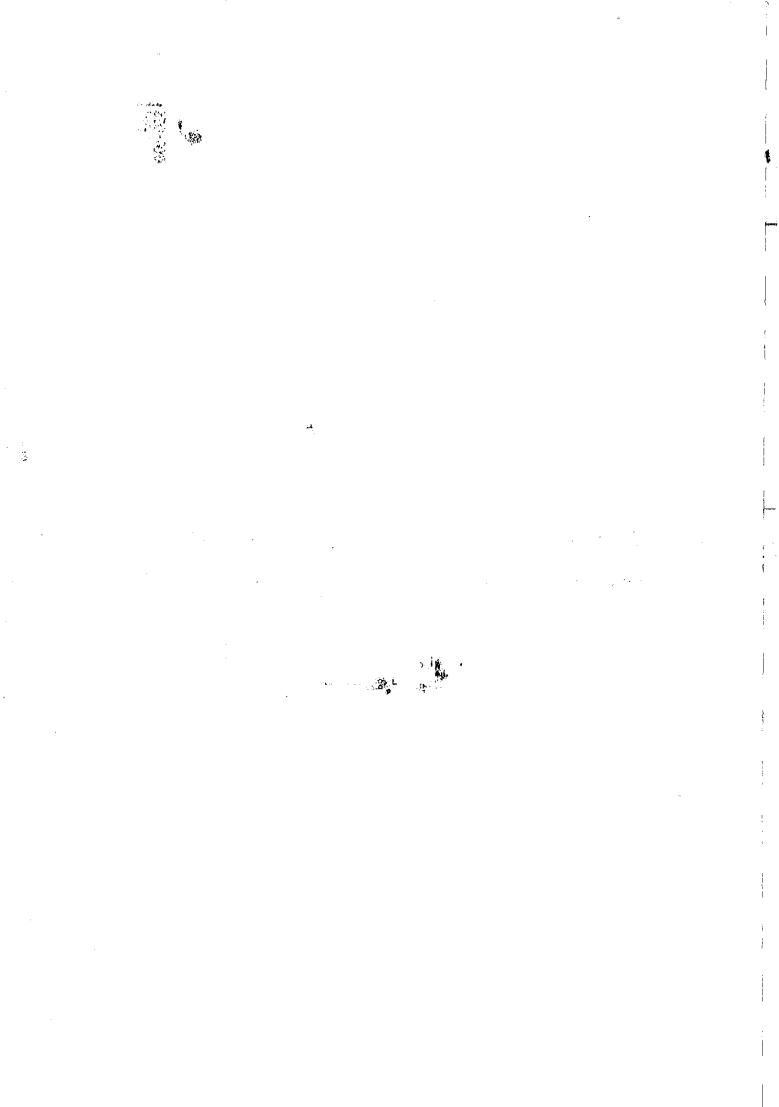
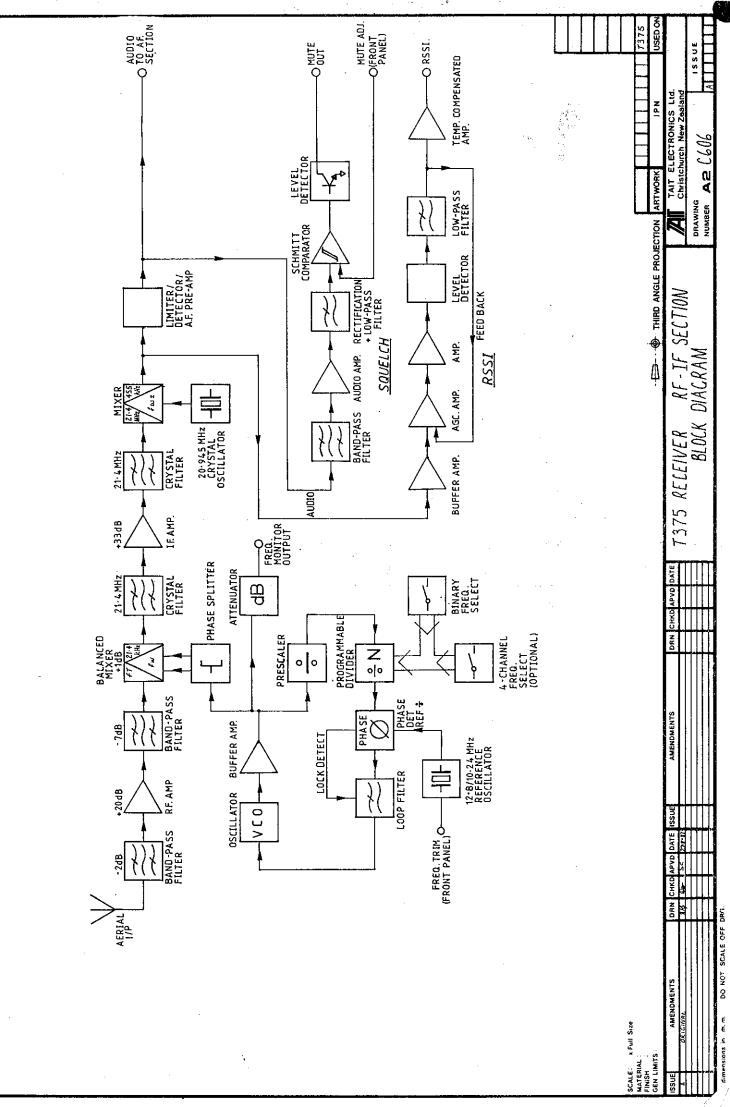
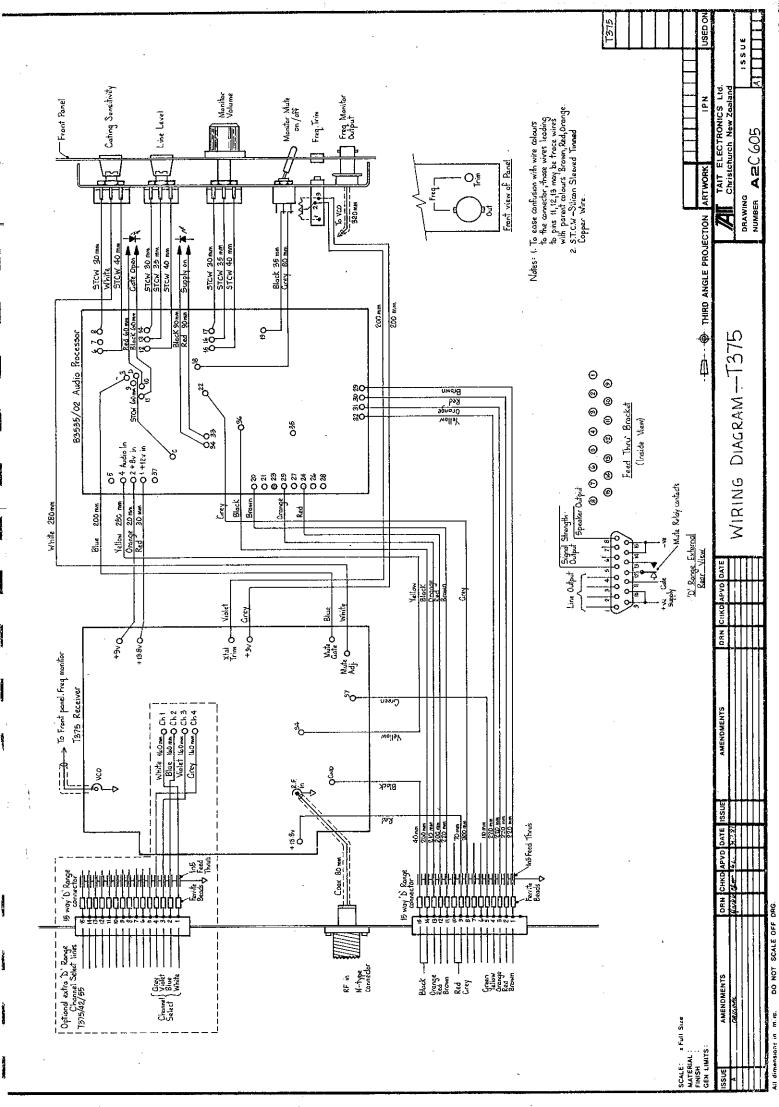


Diagram 3 B3535 Audio Processor PCB Encoding





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