+13.8v. Input
C350 1n
L98 433 band
SR2607
F1 Printed
L1 1µ5
SW1 ON/OFF
+13.8v. Unswitched
+13.8v. switched
2.8v
3.9-3.2
9.0-10.8
13.8v
0v (Ground)
+9v Reg
Busy signal
Signalling squelch
Tx. Reg.
Rx. Reg.
9v. REGULATOR
LED (Tx) [On LED pcb]
LED (Busy) [On LED pcb]
Pressel to CTCSS
Mic Hook
Pressel
Tx. inhibit
Rx Audio
IC1
Tx. TIMER
RECEIVER R.F.
IC6 3357
IC4a
IC4b
IC2a
IC2b
IC2c
IC2d
IC3
AUTOMATIC MODULATION CONTROL
AUDIO PROCESSOR
Mic
Mic Mute
TLY124
Select Tx. tone
Audio output
CTCSS Tx. Tone Input
SPEAKER AMPLIFIER
External Speaker
Speaker
Side tone
Antenna
SYNTHESISER CONTROL
IC8 MC145152
Pulse low if out of lock
Pulse low if VCO freq. low
Pulse low if VCO freq. high
'N' Counter
'A' Counter
DIODE MATRIX PCB
SW2 AUXILLARY
Channel
Channel to CTCSS
LED Flash
LED PCB
IC9 SP8793 MC12016
6.25kHz Duty Cycle Depends on 'A' Counter.
PRESCALER
LOOP FILTER
VCO
Tx. POWER AMPLIFIER
Dependant on Channel spacing (See chart.)

CHANNEL SPACING	12.5 kHz	25 kHz
XF1	21J252	21J282
CF1	CFW455G	CFW455E
C125	15	6p8
C140	4n7	3n3
C141	4n7	3n3
C142	4n7	1n
R106	2K7	5K6
R107	2K7	4K7
R108	1K2	2K2
R140	150k	120k
R141	330	330
R147	180K	270K
R149	12K	8K2
R122	4k	680
L19	629	630

IC LEGEND
IC1 —— 4001B
IC2 —— 324
IC3 —— 4066
IC4 —— TDA1020
IC6 —— 3357
IC8 —— MC145152
IC9 —— SP8793 or MC12016

NOTES
Optional components (Not fitted as std.)
* Dependant on channel spacing (See chart)
† See chart below
◇ Dependant on version

◇	All Versions Except 5/6	5/6
R74	6K8	6K8
R76	10K	6K8
R77	15K	12K
R78	39K	47K
C51	10n	10n
C52	10n	22n
C66	8p2	—

○ = DC Voltages: measured with 10Mᵣ meter & set running normally
R = Receive measured with no signal)
T = Transmit measured @ 25w output. ± 20% tolerance unless otherwise shown.
□ = RF Voltages: measured with diode probe.
~ AC audio peak to peak voltages
R = Recieve measured with volume control fully clockwise and *indicates narrow band RF level of -47dBm modulated by 1kHz to ± 3kHz deviation. Except measurements around squelch circuit are made with no RF signal.
T = Transmit measured in transmit mode with 1mV R.M.S 1kHz signal @ mic input terminals.

★	R235	R229	C183	C228
MC12016	out	in	out	in
SP8793	in	out	in	out

R235

†	DRIVER	FINAL	R274
	BF849	SRFH 1001	10
	BF849	SD4272	47
	MRF297	SRFH 1001	10

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ISSUE	AMENDMENTS	DRN.	CHKD.	APVD.	DATE
D	CH/N 85/12 428 CH/N 85-12-435				11.4.85

ISSUE	AMENDMENTS	DRN.	CHKD.	APVD.	DATE
A	ORIGINAL INH—IIB				
B	C/N 01-0008 01-01 04-01 03-0071 03-0075 03-01 05-01	B.S.	H.O		22.5.85
C	C/N 06/11 07 247, 08 270, 09 311 NB-11B5	D.H.	S.C.	S.C.	16.9.85
D	07 253, 09 346				
E	C/N 01 85/11 417	A.G.	S.C.	S.C.	19.11.85
F	C/N 85/12 427 C/N 85/12 428 C/N 85/12 435	A.G.	P.K.	S.C.	12.12.85

CIRCUIT DIAGRAM — T530

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