



CHANNEL SPACING

IC1	21.2 F2	21.2 B2
CF1	CFW 455 g	CFW 455 E
CF2	4n7	2n9
CF3	4n7	2n9
CF4	4n7	4n7
CF5	2k7	2k7
CF6	4k7	4k7
CF7	1k2	2k2
CF8	50k	20k
CF9	30k	30k
CF10	40k	20k
CF11	40k	20k
CF12	40k	20k
CF13	40k	20k
CF14	40k	20k
CF15	40k	20k
CF16	40k	20k
CF17	40k	20k
CF18	40k	20k
CF19	40k	20k
CF20	40k	20k

- IC LEGEND
- IC1 — 4001B
  - IC2 — 724
  - IC3 — 4066
  - IC4 — 74A1020
  - IC6 — 7957
  - IC8 — MC145152
  - IC9 — SP8799 or MC1216

- NOTES
- # Optional components (Not fitted as std.)
  - \* Dependant on channel spacing (See chart)
  - † See chart below
  - ◇ Dependant on version.

	All Versions Except 15/16/18	15/16	18
R74	6K8	6K8	8x2
R76	10K	6K8	12K
R77	15K	12K	18K
R78	39K	47K	56K
C51	10n	10n	10n
C52	10n	22n	10n
C66	8p2	-	15p

- = DC Voltages: measured with 10mA meter & sat running normally
- R = Receive measured with no signal
- T = Transmit measured @ 25W output ±20% tolerance unless otherwise shown.
- = RF Voltages: measured with diode probe
- ~ = AC audio peak to peak voltages
- R = Receive measured with volume control fully clockwise and ' indicates narrow band
- RF level of -47dBm modulated by 1kHz to ±3kHz deviation. Except measurements around squelch circuit are made with no RF signal.
- T = Transmit measured in transmit mode with 1mV RMS 1kHz signal @ mic input terminals.

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SCALE: MATERIAL: FINISH: GEN. LIMITS:

ISSUE	AMENDMENTS	DRN.	CHKD.	APVD.	DATE
A	ORIGINAL INK-UP				
B	CH/N 05/107 07 247 08 270 09 30				
C	CH/N 05/112 247 08 270 09 30				
D	CH/N 05/112 247 08 270 09 30				
E	CH/N 05/112 247 08 270 09 30				

CIRCUIT DIAGRAM—T530

TAIT ELECTRONICS LTD  
DRAWING NUMBER A1 C506  
ISSUE