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# T545TR Trunked Mobile Two Way Radio

#### VHF FM 175-225MHz

(TM-545-98)

Issue A

#### TECHNICAL INFORMATION

For further information about this Manual or the equipment it describes, contact the Product Distribution Group, Tait Electronics Ltd, at the above address.

# UPDATING EQUIPMENT AND SERVICE MANUALS

In the interests of improving performance, reliability or servicing, Tait Electronics Ltd reserve the right to update their equipment and/or Service Manuals without prior notice.

#### SCOPE OF MANUAL

This Manual contains general, technical and servicing information on the T545TR trunked mobile two way radio.

# Ordering Tait Service Manuals

When ordering Tait Service Manuals, quote the Tait Internal Part Number (IPN) and, where applicable, the version.

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T545TR Service Manual

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### SECTION 1 GENERAL INFORMATION

#### 1.1 INTRODUCTION

The T545TR is a high performance FM synthesised mobile two way radio with a nominal RF power output of 25 watts. It is intended for operation in the 175 to 225MHz frequency range with 12.5kHz channel spacing and  $\pm 2.5$ kHz deviation. The T545TR is for use on trunking systems conforming to DTI Specifications MPT1327 and MPT1343.

Operation of the T545TR is by hand-held microphone and press-to-talk switch, plus six front panel mounted controls: 'Volume', 'Call', three push buttons for ident selection and an 'On/Off' switch. Visual indication of 'Transmit', 'Go', 'System' and ident number is by illuminated front panel display.

The two injection moulded plastic covers and the plastic front panel can be easily removed to expose both of the printed circuit boards for ease of servicing.

The dual conversion receiver employs both discrete components and integrated circuits. It also includes a signal-to-noise ratio operated squelch circuit. The receiver delivers approximately 2 watts of audio power to an 8 ohm speaker.

The VCO provides about 10 milliwatts of frequency modulated RF drive to the four stage broad band RF power amplifier. An audio processor contains modulation level control and deviation limiting circuits. A timer limits transmission duration to approximately one and a half minutes.

A trunking control board is plugged onto the radio unit and together they form a radio complying with trunking specification MPT1327. A display PCB is mounted behind the front panel itself and this is plugged as a unit onto the trunking control board.

The T545TR is light and compact and is supplied with a rugged mounting system which allows easy installation in any vehicle. Mains operation is possible when the T545TR is used with the Tait T508 power supply.

The DC supply to the T545TR must be negative earth and must be between 10.8 and 16 volts. The T545TR is protected against reversal of the DC supply polarity.

# 1.2 SPECIFICATIONS

# 1.2.1 INTRODUCTION

The performance figures given are typical figures, unless otherwise indicated, for equipment operating at standard room temperature (22°C to 28°C). Unless otherwise indicated, the figures apply to all versions.

Where applicable, the test methods used to obtain the following performance figures are those described in the UK Department of Trade and Industry Specification MPT1323.

Details of test methods and the conditions which apply for type approval testing in all countries can be obtained from Tait Electronics Ltd.

# 1.2.2 GENERAL

Modulation Type .. Frequency Modulation

Frequency Range .. 175 to 225MHz

Channel Separation .. 12.5kHz

Frequency Increment .. 12.5kHz

Number Of Channels .. 503 (standard) [system dependent]

Switching Range .. 8MHz

(transmitter & receiver)

Supply Voltage:

Operating Range .. 10.8 to 16V DC Standard Test Voltage .. 13.8V DC

Polarity Protection .. negative earth only .. internal crowbar diode

Supply Current:

Receiver - Squelched .. 250mA Receiver - Full Audio .. 700mA Transmitter .. 5.5A

Antenna Impedance .. 50 ohms

T/R Changeover Switching .. solid state

Operating Temperature Range:

Standard .. -10°C to +60°C

Dimensions:

Weight .. 1.2kg

#### RECEIVER 1.2.3

.. dual conversion superhet Type

I.F Amplifiers:

.. 21.4MHz and 455kHz Frequencies

.. 7.5kHz Bandwidth

Sensitivity:

.. -118dBm (0.28µV pd) 12dB Sinad

.. -1dBuV emf (-114dBm) 20dB Sinad psophometrically weighted

.. 28dB Signal-to-Noise Ratio

(RF: -107dBm, modulated at 1kHz to full

system deviation)

.. 70dB Selectivity (adjacent channel)

Spurious Response Attenuation .. 75dB

.. 75dB Intermodulation Response Attenuation

Spurious Emissions:

.. -60dBm Conducted

.. -57dBm Radiated ( $\frac{1}{2}$  wavelength dipole)

Audio:

Output into internal 8 ohm speaker .. 2 watts Output into external 4 ohm speaker only .. 4 watts

.. 2% Distortion (at rated power)

Minimum Load Impedance .. 2 ohms

.. within +1, -3dB of a 6dB/octave Audio Response

de-emphasis characteristic (ref. 1kHz)

Audio Bandwidth 300Hz to 3kHz

Squelch:

.. 6dB to 20dB Sinad Sensitivity

.. 70dB Ratio

TRANSMITTER 1.2.4

Power Output .. 25 watts

.. system dependent Transmit Timer

.. (ref. 1.2.4) Frequency Stability

Mismatch Capability:

VSWR < 5:1 (all phase angles) Stability 2 minute transmit into infinite Ruggedness

VSWR (all phase angles)

Spurious Emissions:

Conducted

Radiated ( $\frac{1}{2}$  wavelength dipole)

.. -36dBm

.. -36dBm

Adjacent Channel Power

.. 65dB below carrier

Modulation System

.. direct FM

Deviation Response:

In Limiting - 500Hz to 3kHz

.. within +0dB, -4dB of max. system

deviation

Below Limiting - 500Hz to 3kHz

.. within +1, -3dB of 6dB/octave

pre-emphasis (ref. 1kHz)

Frequencies Above 3kHz

.. greater than 25dB/octave roll-off

**Deviation Limiting** 

.. ±2.5kHz (peak) maximum adjustable to ±2.5kHz

Audio Input For Maximum Deviation

.. 1mV rms at 1kHz

Audio Distortion

(modulated at 1kHz to 60% of maximum

deviation)

.. 2%

Hum & Noise

(below ±3kHz peak deviation)

.. 45dB

#### 1.2.5 FREQUENCY REFERENCE

Crystal Type:

 $\pm$  5ppm (-10°C to +60°C)

.. TE/9

Oscillator Frequency

.. 12.8MHz

#### 1.2.6 TRUNKING CONTROL BOARD

Data Modulation

.. as per MPT1317

Data Deviation (Tx)

.. 1.5kHz

#### 1.3 VERSIONS

1. T545TR-39 (UK)

174-225MHz frequency range

7.5kHz IF bandwidth

12.5kHz frequency increments

2.5kHz deviation

Trunking board not supplied

2. T545TR-96 (HK)

174-225MHz frequency range

7.5kHz IF bandwidth

12.5kHz frequency increments

2.5kHz deviation

Trunking board (MPT1327 band III, sub-band I)

3. T545TR-98 (UK)

174-225MHz frequency range

7.5kHz IF bandwidth

12.5kHz frequency increments

2.5kHz deviation

Trunking board (MPT1327 band III, sub-band II)

# 1.4 UK BAND III

Band III (174-225MHz) is itself divided into sub-bands I, II and III. The operating frequencies of sub-bands I and II are set out below (12.5kHz channel spacing).

		Mobile Tx (MHz)	Mobile Rx (MHz)
Sub-band I			
Lowest valid channel:	1	191.525	176.525
Highest valid channel:	480	197.5125	182.5125
Sub-band II			
Lowest valid channel:	1	193.2125	201.2125
Highest valid channel:	503	199.4875	207.4875

# 1.5 OPERATING INSTRUCTIONS

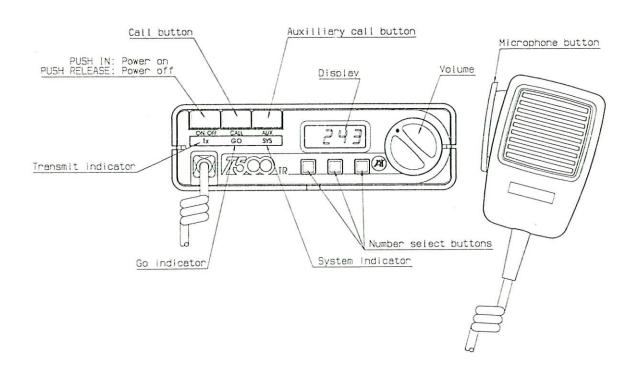


Figure 1 T545TR Front Panel

### 1.5.1 GENERAL

The following instructions assume that the radio is correctly installed (see Section 4). Some aspects of radio operation are dependent on the trunking system in use; contact your supplier or service facility in case of difficulty.

# 1.5.2 SWITCHING ON

The 'On/Off' switch turns the radio on when it is pressed, and pressing it again turns the radio off. When the radio is turned on, the software version number is initially displayed, followed by a momentary display of the mobile unit number.

The system indicator ('SYS') will light if the radio is in range of the trunked system and is receiving valid digital information from the control channel transmitter.

CAUTION:

A functioning T545TR is always active and can respond automatically to an incoming call. It is therefore advisable to switch the T545TR off with the 'On/Off' button when entering a petrol station.

The T545TR should always be switched off if the aerial is disconnected.

# 1.5.3 MAKING A CALL

1. Enter the number of the mobile or despatcher to be called by pressing the appropriate call number buttons.

Pressing each of the buttons alters the appropriate digit of the display in single digit increments upwards. Attempts to call invalid idents or idents of mobiles or despatchers not available to the user will cause the characters 'UA' to be displayed.

2. Momentarily press the 'Call' button or PTT switch.

The transmit indicator will light and may flash on and off while the call is being established. If the called party is available, an audible indicator will be heard from the speaker and the display will indicate a countdown of the call time remaining in minutes and seconds.

- 3. If the call is not connected immediately, an audible indicator is sounded. If the indicator changes to a lower frequency, this means the call has been queued by the system and will be connected shortly.
- 4. To talk, press the PTT switch on the microphone.

Speak directly into the microphone, holding it approximately 100mm from the mouth.

To listen, release the PTT switch.

Adjust the volume control clockwise to increase and anticlockwise to reduce the volume.

5. The call can be cleared by returning the microphone to the hook or by pressing one of the three call number buttons. During the last few seconds allowed to a call, a pip will sound each second as a warning. Cleardown will take place automatically at the termination of the countdown time.

# 1.5.4 MAKING A GROUP CALL

Group calls are made (if the radio is programmed for them) by entering the group call number on the front panel and making the call as normal.

# 1.5.5 RECEIVING A CALL

Incoming calls are audibly indicated by the ringing tone.

Lift the microphone off the hookswitch and acknowledge the call in the normal manner, pressing the PTT switch to talk and releasing it to listen.

The ident number of the mobile or despatcher making the call will be displayed for five seconds on the front panel, followed by the "call time remaining" display.

Note: A call may be cleared down if neither party transmits within a specific time period.

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# SECTION 2 CIRCUIT OPERATION

Refer to the Block and Circuit Diagrams at the rear of this Manual.

### 2.1 SYNTHESISER

The dual modulus synthesiser of the T545TR features separate on-frequency VCOs for receive and for transmit. The transmit VCO is frequency modulated by the application of audio to the varicap diode D51.

A crystal provides a stable reference frequency of 12.8MHz which is divided down to 12.5kHz and fed to one input of a phase comparator within IC8.

The VCO frequency is divided by the 40/41 prescaler, IC9, and then further divided within IC8 to provide the other input to the phase comparator. The division ratio in IC8, and hence the channel frequency, is determined by a 16 bit binary word, input to IC8.

The phase comparator output (pins 7 & 8 of IC8) is fed to both VCO varicaps via the speedup circuit (Q27, Q28) and the loop filter (R181, C177, R183, C178). Then to either varicap D35 via R187 & C186 on receive, or to varicap D50 via R233 & C226 on transmit.

A lock detect signal is made available to the trunking control board (TCB).

#### 2.2 RECEIVER

The RF signal from the PIN switch is amplified by Q15 and fed to the balanced mixer (Q16, Q17) via a triple tuned circuit. 10mW from the receive VCO is fed in antiphase to the gates of the two mixer J-FET's.

The IF output from the mixer passes through the 21.4MHz crystal filter and is amplified by Q18 before being fed to IC7. It is also fed to the TCB for RSSI purposes.

IC7 provides the following functions: IF conversion from 21.4MHz to 455kHz with external crystal X1 (CF1 sets the 455kHz IF bandwidth); amplitude limiting; quadrature detection with CD1; and squelch. Q19 provides additional limiting gain.

#### 2.3 SQUELCH

An input signal to the squelch circuit is obtained from the audio output of IC7 via RV149. This signal has a noise level which is inversely related to the level of an RF signal at the receiver input.

An op-amp within IC7 is used in a band pass filter configuration to select and amplify noise frequencies above the audio band. The centre frequency is approximately 4.5kHz.

The band pass filter output is rectified by Q20 to give a positive going DC voltage which is an inverse function of the RF signal strength.

This DC voltage is then fed to a threshold detector within IC7, in such a way that pin 14 of IC7 is high in the presence of noise and low in the absence of noise. The threshold point occurs at approximately 0.7 volts.

The switching signal from the threshold detector is then inverted by Q7. D9, C17 (when fitted) and R26 provide an extended tail time (to prevent squelch closure during rapid fades) while maintaining a fast opening time.

# 2.4 TRANSMITTER

# 2.4.1 RF STAGES

The 10mW output of the frequency modulated transmit VCO is amplified to a level of 25 watts by a 5 stage broad band amplifier (Q40, Q41, Q42, Q46, Q47). High level RF then passes via the aerial PIN switch through the low pass filter to the aerial connector.

The transmit power output is set at 25 watts by RV256 which, with Q44 & Q45, controls the collector voltage of Q42, and hence the gain of the broad band amplifier. The circuit utilises a power detector, D61, and a feed back loop to hold the transmitter power to 25 watts under conditions of varying supply voltage.

Transistor Q43 prevents the transmitter turning on when the synthesiser is out of lock. The TCB can also control the transmitter output via Q43.

# 2.4.2 AUDIO PROCESSOR

Transistor Q10 provides microphone preamplification while IC2 provides the necessary gain limiting and filter functions for the audio signal. An automatic level control (ALC) function is performed by detector Q11 and shunt elements D15 and D16. The analogue switches within IC3 allow either transmit or receive audio to be directed through the audio processor. The microphone is muted during FFSK signalling periods.

# 2.5 POWER SUPPLY

#### 2.5.1 GENERAL

Note: The T545TR is suitable for negative earth applications only.

The unit is protected by a crowbar diode (D1) which will blow the fuse if the supply is reverse connected.

DC is connected to the audio output IC and the transmitter final, driver, and power turn-down stages whenever the T545TR is connected to a supply.

#### 2.5.2 CONTINUOUS SUPPLIES

DC from the on/off switch supplies the audio output IC enable and the short circuit protected 9 volt regulator. A continuous 9 volts is applied to the audio processor and synthesiser.

#### 2.5.3 RECEIVE

When the PTT switch is open, IC1 turns Q5 on and Q4 off, enabling the following circuits:

receive VCO receiver squelch control receive diode in the aerial switch.

# 2.5.4 TRANSMIT

When the PTT switch is closed or the TCB enables the PTT, IC1 turns Q4 on and Q5 off, enabling the following circuits:

transmit VCO low power transmitter stages transmit diode in the aerial switch

Closing the PTT switch also initiates a timer circuit around IC1 which will return the T545TR to receive after 1.5 minutes of transmission.

# 2.6 FREQUENCY INFORMATION

The synthesiser programming is controlled by the microprocessor. 16 data bits are presented to the synthesiser A & N lines. Refer to Table 1 in Section 5.4.3 to check the synthesiser programming data.

## 2.7 TRUNKING CONTROL BOARD

#### 2.7.1 ANALOGUE INTERFACES TO THE RADIO

# 2.7.1.1 Received Signal Strength Indicator (RSSI)

The RSSI is an analogue DC voltage produced by a detector in the receiver. The voltage enters the PCB at SKT-3 pin 3 and is fed to PE7 of the microcomputer via R95 and C85.

#### 2.7.1.2 Lock Detect

This analogue voltage produced on the main board is fed to the TCB at SKT-3 pin 12. The signal is then fed via IC2 to microcomputer port PA2, thus enabling the synthesiser lock status to be monitored.

#### 2.7.1.3 FFSK (Including Modem)

The received FFSK signal enters the PCB at SKT-1 pin 2 and is AC coupled to a pre-amplifier, IC2. FFSK is then passed to the modem chip via IC14 pin 16, which results in a logic '1' at IC14 pin 15 and decoded data at IC14 pin 14.

For transmit data, the modem provides a Tx sync signal at pin 3. The transmit data which originates at microcomputer port PA3 (pin 31) is applied to the modem at pin 7. It appears as FFSK at pin 5 and leaves the PCB at SKT-1 pin 5. RV1 controls the FFSK deviation, which is set a 1.5kHz peak by sending a pattern of alternate '1's and '0's while in the test mode.

#### 2.7.1.4 Sidetone

Audible 'pips' and 'ringing' sounds are produced from the speaker during various stages of call set-up. These are generated in the microcomputer and appear at port PA4. The signals are then passed via C50/RV2 to SKT-3 pin 4 to the radio where they are mixed into the speaker amplifier. RV2 adjusts the tone level.

# 2.7.2 DIGITAL INTERFACE CIRCUITS TO THE RADIO

# 2.7.2.1 Pressel (PTT)

This signal takes one of three forms:

- (a) A 12k ohm resistance to ground from the radio to the PCB when the microphone is 'on hook', i.e. when the stud on the microphone is grounded.
- (b) A short circuit to ground from the radio to the PCB when the pressel is pushed.
- (c) A short circuit to ground from the PCB to the radio to command the transmitter to key during signalling.

The PTT/hook signal is fed to microcomputer port PE4 (pin 44). This port samples the analogue input and the microcomputer determines the status. Tx-CRL is driven low by Q3 to enable transmission.

# 2.7.2.2 Microphone Mute

This signal passes from the TCB to the radio. The microphone is muted during signalling by Q2 turning on. The microphone mute signal leaves the board at SKT-3 pin 2.

#### 2.7.2.3 Receive Audio Mute

This signal passes from the TCB to the radio and disables received audio to the speaker.

# 2.7.2.4 Synthesiser Control

This is a 16 bit parallel interface from the TCB to the radio to control the synthesiser frequency. The supply voltage to the synthesiser is 9V; thus IC3 and IC4, which perform the serial-parallel conversion, also run from 9V. IC10 provides the level shifting from 5V to 9V. The synthesiser data is controlled via synchronous communications with the microcomputer. Port lines PA5, PA6 and PD2 provide the serial clock (SYC), the serial data (SYD) and the latch (SYL) respectively to IC3 and IC4. The transfer command originates at microcomputer port PD2 and is also level shifted in IC10.

#### 2.7.2.5 PA Disable

This is a signal from the TCB to the radio which disables the transmitter. The PA will be disabled when this line goes high.

#### 2.7.2.6 Call Button

The 'Call' button initiates call requests and is passed from the radio to the TCB. It enters the board at SKT-3 pin 1 and is applied to microcomputer port PD4 (pin 24).

#### 2.7.2.7 AUX Button

This button is connected via SKT-3 pin 14 to microcomputer port PA7 (pin 27); its function is software definable.

# 2.7.2.8 Emergency

This wire is taken from SKT-3 pin 13 to the rear panel power connector pin adjacent to the ground return wire. On the TCB it is connected to microcomputer port PE3 (pin 49).

# 2.7.3 DIGITAL INTERFACE TO THE FRONT PANEL

# 2.7.3.1 Clock, Data and Enable

These lines come from ports PA5 (pin 29), PA6 (pin 28) and PD5 (pin 25) respectively of the microcomputer. The lines leave the TCB at SKT-8 pins 8, 9 and 10. The data is in serial format with a synchronous clock; the enable line is necessary to update the display.

#### 2.7.3.2 Buttons

The front panel buttons which are used to enter the called address and clear down are connected via the front panel to SKT-8 pins 3, 6 and 7 on the TCB.

# 2.7.4 MICROCOMPUTER CIRCUITS

### 2.7.4.1 The Microcomputer (IC12)

The heart of the TCB is the MC68HC1141, which controls all radio functions via software resident in the memory devices.

### 2.7.4.2 Memory Devices

The RAM (IC9) is a zero power device with its own internal battery.

The EPROM (IC8) is mounted in a socket for easy software updating.

The EEPROM is resident in the microcomputer (IC12).

#### 2.7.4.3 Miscellaneous Functions

Miscellaneous functions are:

(a) 'GO' LED

This LED indicates traffic channel allocation for a call.

(b) 'SYS' & 'TX' LED's

The 'SYS' LED is switched on when the radio is receiving valid digital information from the control channel transmitter.

The 'TX' LED is switched on during transmit key.

# 2.7.4.4 Test Mode

The 'test mode' pins will pull microcomputer port line PD5 low when fitted with a shorting link. When shorted together on switch-on, the TCB goes into test mode to aid servicing.

#### 2.7.4.5 Clock Circuit

The clock is generated by an on-chip oscillator and external crystal (XTAL 1). The clock is divided by 4 within the microcomputer and appears as 'E' on pin 5.

# 2.7.5 MICROCOMPUTER SUPERVISORY CIRCUIT

RES is driven by a three terminal LVI chip (IC15). An internal bandgap reference and external resistors R66/R67 ensure a RESET when the 5V supply falls below 4.8V.

#### 2.7.6 POWER SUPPLIES

The supply to the TCB from the radio is taken from both the 13.8V switched and 9V regulated supplies. 13.8V is fed to a discrete regulator in the front panel to supply the 'SYS' and 'GO' LED's and IC100. 9V is further regulated by another 78L05 (VREG1) and this supplies all other TCB hardware.

# 2.8 FRONT PANEL DISPLAY

Data from the TCB is presented via SKT-8 to the 14499 display driver (IC100), which controls the four 7-segment displays.

### T545TR Ancillary Equipment

#### SECTION 3 ANCILLARY EQUIPMENT

#### 3.1 T508 POWER SUPPLY

The T508 Power Supply will allow operation of a T545TR two way radio from a 230V (nominal) 50Hz or a 115V (nominal) 60Hz mains supply (specify voltage when ordering):

Type Numbers:

115V Supply .. T508/115 230V Supply .. T508

The T508 is an attractively styled unit which matches the T545TR two way radio. The radio can be mounted on the T508 to give a compact desk top installation, or they can be separately wall mounted to save desk space.

The T508 provides a 13.8V DC 6.5A (intermittent) regulated supply for the T500 Series II two way radios and incorporates current limiting and thermal protection.

#### 3.2 T220/2 REMOTE SPEAKER ASSEMBLY

The T220/2 remote speaker assembly may be used with the T545TR. It comprises a heavy duty 4 watt speaker mounted in a rugged enclosure which pivots on its mounting bracket. The 3.5 ohm voice coil of the speaker is connected by a short lead terminated in a 2 pin cord mounted connector. The enclosure is compact and easily mounted in any convenient position.

# 3.3 T500-20 SERIAL DATA INTERFACE

This is used to interface the T545TR with an IBM\* XT or AT PC (or compatible) for identity programming. Circuitry to level shift the RS232 signals is built into the interface.

\*IBM is a registered trademark of International Business Machines.

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# T545TR Installation

# SECTION 4 INSTALLATION

# 4.1 VEHICLE INSTALLATION

Installation instructions (IPN 409-54500-00) are packed with each radio.

CAUTION: The T545TR is suitable for negative earth installation only.

# 4.2 T508 POWER SUPPLY UNIT

When using the T545TR with a T508 power supply, it is essential that the RF power output is adjusted so that the station effective radiated power (ERP) from the antenna complies with the licence conditions in force.

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### SECTION 5 SERVICING

#### 5.1 GENERAL

#### 5.1.1 NOTES

If further information is required about the T545TR or this Manual, it may be obtained from Tait Electronics Ltd or accredited agents. When requesting this information, please quote either the equipment type number (e.g. T545-98), or serial number (found adjacent to the aerial connector at the back of the set). In the case of the Circuit Diagrams quote the 'Title' and 'Issue' and for the Service Manual quote the internal part number (IPN) and Issue, e.g. TM-545-98, Issue A.

### CAUTION: SWITCH OFF IN EXPLOSIVE ENVIRONMENTS

A functioning T545TR is always active and can respond automatically to an incoming call. It is therefore advisable to switch the T545TR off with the 'On/Off' button when entering potentially hazardous areas, such as petrol stations, or when in close proximity to quarries or tunnelling works where remote controlled explosive charges may be in use.

#### CAUTION: CLEANING

This is a plastic based product with a secondary finish on the front panel. Use a cloth dampened with warm, soapy water to clean. If solvent cleaners are to be used for stubborn stains, test first on a part of the set normally out of sight. Do not use solvent cleaners on the front panel.

# CAUTION: AERIAL LOADING

The equipment has been designed to operate over a wide range of aerial loading conditions. However, it is strongly recommended that the transmitter is not operated in the absence of a suitable load. Failure to observe this precaution may result in damage to the transmitter power output stage.

#### CAUTION: BERYLLIUM OXIDE & POWER TRANSISTORS

The RF power transistors in current use all contain some beryllium oxide. This substance, while perfectly harmless in its normal solid form, can become a severe health hazard when it has been reduced to dust. For this reason the RF power transistors should not be scratched, mutilated, filed, machined, or physically damaged in any way that can produce dust particles.

### CAUTION: CMOS DEVICES

The equipment contains CMOS devices which are susceptible to damage from static charges. Care when handling these devices is essential. For correct handling procedures refer to the manufacturers data books, e.g. Philips data books covering CMOS devices, or Motorola CMOS data books, Section 5 'Handling', etc.

#### 5.1.2 TECHNICAL INSTRUCTIONS

From time to time 'Technical Instructions' (TI's) are issued by Tait Electronics Engineering Division. These TI's may be used to update equipment or information, or to meet specific operational requirements.

#### 5.2 MECHANICAL

#### 5.2.1 POSIDRIV RECESS HEAD SCREWS

Posidriv screws are the preferred standard on all Tait manufactured equipment. The very real advantages of this type of screw will not be realised unless the correct screwdrivers are used by servicing personnel.

Posidriv No 1 screwdrivers will fit the posidriv screws used in the T545TR. Philips cross-head screwdrivers are not satisfactory for use on these screws.

#### 5.2.2 DISASSEMBLY INSTRUCTIONS

- Note 1: To carry out alignment procedures it is necessary to remove only the bottom cover as given in 5.2.2.1 below.
- Note 2: To assist in separating the top and bottom covers, a thin plastic strip (such as a plastic rule) may be inserted between the covers and used as a lever.

#### 5.2.2.1 To Gain Access To The Component Side Of The Radio PCB

Place the T545TR upside down on the bench.

Remove the 4 bottom cover retaining screws.

Gently lift both ends of the bottom cover until it clears the front panel and heatsink.

Lift away the bottom cover.

Remove the front panel as instructed below (Section 5.2.2.3).

With the power removed from the radio, carefully lift off the logic PCB.

#### 5.2.2.2 To Gain Access To The Track Side Of The PCB

Remove the bottom cover as in 5.2.2.1 above.

Turn the T545TR over on the bench, taking care not to bend or damage any header pins on the main PCB.

Remove the 2 top cover retaining screws.

Gently raise both ends of the top cover until it clears the front panel and heatsink.

# 5.2.2.3 To Remove The Front Panel

Remove the microphone cord relief grommet from its seat.

Unplug the microphone.

Remove the bottom and top covers as instructed above.

Slide the front panel forward.

It is not necessary to remove the knob - it may be left in place.

# 5.2.2.4 To Gain Access To The PA Components

To gain access to the PA, remove the screws retaining the two PA cavity lids.

Remove the component side lid towards the right hand side of the PCB (as viewed from the front of the set) so that it clears the power supply feedthrough capacitor.

# 5.2.2.5 Speaker Removal/Refitting

The speaker in the T545TR is held in place with four "push-on fix" spring clips (IPN 357-00010-09, Spire No. SFP 3253) which may cause problems when the speaker is removed.

To remove the speaker, cut the spring clips off the plastic locating pegs with wire cutters. Do <u>not</u> attempt to prise off the spring clips as this will damage the pegs.

Fit four new clips when refitting the speaker.

#### 5.2.3 VCO CAN

CAUTION: When loosening or tightening the 4 retaining screws of the VCO can, support the can from the component side as undue pressure on the PCB may fracture some of the chip capacitors.

#### 5.2.4 REASSEMBLY

Reassembly is carried out in the reverse order of the above.

Replace the PA covers.

Replace the logic PCB carefully, ensuring that all header pins are located in the bottom entry sockets. The plastic pillar will click into place, securely locking the PCB in place.

Slide on the front panel.

Fit the top cover:

Gently press the cover into position, taking care to position the rim at the rear of the cover into the heatsink groove. Ensure that the rim of the front panel fits into the groove round the front of the top cover.

Replace the two "Taptite" screws at the rear of the cover.

Fit the bottom cover:

Invert the T545TR.

Gently press the cover into position, taking care to position the rim at the rear of the cover into the heatsink groove. Ensure that the rim of the front panel fits into the groove round the front of the bottom cover.

While fitting the bottom cover, check that the right hand retaining screw pillar slides into the hole in the LED PCB.

Replace the two "Taptite" screws at the rear of the cover and the two "Plastite" screws at the front of the cover.

Plug the microphone back in and reseat the cord relief grommet.

# 5.2.5 PA - SPECIAL INSTRUCTIONS

<u>CAUTION:</u> As the location of certain components in the PA is critical to performance, it is important that any components removed or disturbed be refitted in exactly the same location.

# 5.2.5.1 To Replace The PA Transistors

Unsolder the tabs by heating them with a soldering iron, then lifting them up towards the transistor with a thin stainless steel spike or screwdriver. Unscrew the transistor mounting screws or stud nuts and remove the transistor.

Trim the tabs of the replacement to make them similar to the faulty item, then lightly tin the underside of the tabs.

Smear the underside of the transistor with heatsink compound.

Screw the transistor tightly to the heatsink then solder the tabs.

<u>CAUTION:</u> Do not solder the tabs before tightening the screws or nut, as this will fracture the device.

### 5.3 REPAIR

#### 5.3.1 COMPONENT CHECKS

If a transistor is suspected of faulty operation, an indication of its performance can be assessed by measuring the forward and reverse resistance of the junctions. First make sure that the transistor is not shunted by some circuit resistance (unless the device is completely unsoldered). An AVO model 8 or equivalent meter should be used for taking the measurements, using only the medium or low resistance ranges.

The collector current drawn by multijunction transistors is a further guide to their operating performance.

If an integrated circuit (IC) is suspect, the most reliable check is to measure the DC operating voltages. Due to the catastrophic nature of most IC failures, the pin voltages will usually be markedly different from the recommended values in the presence of a fault. These values can be found on the Circuit Diagram, or in the component data catalogue.

#### 5.3.2 COMPONENT REPLACEMENT

Whenever components are removed from, or fitted to the printed circuit track, care must be taken to avoid damage to the track. If it is necessary to remove a component from the track, the following procedure is recommended:

Remove the solder from the component leads using a solder wick. Loosen the individual leads from the printed track. Withdraw the component from the top of the PCB.

Because of the delicate nature of the printed track, the use of solder suckers is not recommended.

Do not remove the component from the PCB while the solder is still molten.

Keep all soldering operations, and the heat and solder applied, to a minimum. A thermally controlled, fine tip soldering iron should be used. Ensure that the iron is earthed back to the frame of the set.

# 5.3.3 CRYSTAL FILTER REPLACEMENT

Should it become necessary to replace the crystal filter, both cans should be replaced together as the new parts are supplied as matched pairs. Observe polarity when fitting.

# 5.3.4 CHIP COMPONENT REMOVAL/REPLACEMENT

Note: The temperature of the soldering iron must be maintained at 320-370°C (600-700°F) and a low temperature solder should be used.

# 5.3.4.1 Component Removal

- 1. Place the soldering iron tip directly on the component in order to melt the solder and glue as shown in Figure 2. Remove the component with tweezers or long nose pliers.
- 2. Completely remove the old solder from the PCB, using a solder wick. Application of a small amount of flux will greatly aid in the removal of old solder. The use of 'solder suckers' is not recommended.

# 5.3.4.2 Replacement

- 1. After a component has been removed and the PCB pattern cleaned, apply a small amount of solder on the PC pattern and allow to cool, as shown in Figure 3.
- 2. Insert the new components and apply the soldering iron tip to the PC pattern as shown in Figure 4 (a), (b) and (c).

CAUTION:

As patterns and components are close to each other, extreme care must be excercised when soldering so as not to damage components or bridge the PCB pattern paths. High soldering iron temperatures can cause component damage. Do not apply the soldering iron tip to the new component during installation.

SCUERING IRON TIP

SOLDER

CHIP COMPONIENT

Figure 2

COPPER TRACK SOLDER PC B

Figure 3



0

CHIP COMPONEUT

CHIP COMPONENT SOLDERED
IN POSITION
COPPER TRACK

b

Figure 4

# 5.3.5 INTER-BOARD CONNECTIONS

To assist circuit tracing, all plugs and connections are shown on the outer edge of the Wiring Diagram, where the 'Function' is shown.

# 5.3.6 MICROPROCESSOR REPLACEMENT

The microprocessor (IC12) is factory programmed and permanently bonded to the PCB. If this device fails, the trunking logic PCB must be replaced.

# 5.4 SETTING UP

Note: The T545TR software enables a "test mode" function which allows ease of service without requiring interaction with a trunking system. The purpose of this test mode is to enable the normal radio functions to be set up. However, a trunking test set (e.g. Marconi TF2960) or Schlumberger 4040/4041 system is required to test the trunking signalling protocols.

It is recommended that the serviced radio is field tested by the serviceman on the customer's trunking system before being returned to the customer.

#### 5.4.1 TEST EQUIPMENT REQUIRED

1. Digital multimeter (e.g. Fluke 77)

2. DC electronic voltmeter (e.g. Tech TE65)

- 3. RF power meter 30 watts FSD usable to 520MHz with 5 and 30 watt elements (e.g. Bird Model 6154 or 611).
- 4. Power Supply output adjustable between 9 and 16 volts DC with a capacity of at least 8 amps.

5. Modulation meter (e.g. Sayrosa 252)

6. Sinad meter (e.g. Helper Instruments Sinadder)

- 7. VHF signal generator. Good quality FM. Useable from 0.1μV (-127dBm) to 200mV (0dBm) pd. (e.g. HP 8640B).
- 8. VHF frequency counter accurate to within 2ppm.

- 9. 10.7MHz Crystal marker (second harmonic gives beat for 21.4MHz IF)
- 10. Audio oscillator, 10Hz to 10kHz (e.g. HP 204C/D)
- 11. Tone Box: Audio amplifier, with about 1.5 watts output, to drive a small speaker which can be coupled to the T545TR microphone. An adaptor should be made which will hold the speaker and microphone close together.
- 12. AC millivoltmeter
- 13. Calibrated oscilloscope
- 14. Speaker 4 ohm voice coil
- 15. RF power attenuator, total attenuation 50dB (e.g. Weinschel 40-40-33 30dB 150W, plus Coline 1200 85 20dB 1w)
- RF diode probe

(e.g. Coline M12DM modular RF detector probe)

#### 5.4.2 TUNING HINTS

- 1. Diagram 1 shows the test set-up for receiver and transmitter alignment.
- 2. For accurate tuning, the test cable connecting the signal generator or power meter to the T545TR should be as short as practical and fitted with a 'mating' BNC connector. Do not use adaptors, 'sniffer' couplings, etc, which introduce changes to cable impedance and errors in test results.
- 3. Non-metallic tuning tools must be used for the alignment of all coil slugs to avoid the tuning errors introduced by the use of metallic tools. Tuning tools need to be of correct size to avoid the damage to slugs which results from the use of incorrect tuning tools.
  - Tuning tool WT 11 (Tait IPN 9360112) is suitable for adjusting trimming capacitors.
- 4. When using the RF diode probe, the earth return should be kept as short as possible and connected as close as possible to the point at which the measurement is being taken. This is to minimise stray pick-up which may affect the reading.
- 5. The front panel 'on/off' switch removes power from the regulated supplies only. The RF power amplifier, the audio output IC and the DC hash filter are not controlled by this switch.
- Check for obvious mechanical faults in the printed circuit board, controls, microphone etc.

# 5.4.3 CHANNEL PROGRAMMING

Note: Transmit VCO operation is restricted to an 8MHz switching range within the band covering 175 to 225MHz. Receive VCO operation is restricted to an 8MHz switching range within the band covering 153 to 204MHz.

To check that the synthesiser is programmed correctly, refer to Table 1 below.

A logic level 1 on the pins listed adds that frequency increment to the total VCO frequency.

A logic level 0 on the pin means that it is not added.

Table 1

Frequency Increment MHz	Code	Pin No. IC8 (MC14152)	Connector
256.0 128.0 64.0 32.0 16.0 8.0 4.0 2.0 1.0 0.5 0.4 0.2 0.1 0.05 0.05 0.025	N9 N8 N7 N6 N5 N4 N3 N2 N1 N0 A5 A4 A3 A2 A1 A0	20 19 18 17 16 15 14 13 12 11 10 25 24 22 21 23	SKT-3, pin 5 SKT-3, pin 6 SKT-3, pin 7 SKT-3, pin 8 SKT-3, pin 9 SKT-3, pin 10 SKT-4, pin 6 SKT-4, pin 7 SKT-4, pin 8 SKT-4, pin 9 SKT-4, pin 10 SKT-4, pin 10 SKT-4, pin 1 SKT-4, pin 1 SKT-4, pin 2 SKT-4, pin 4 SKT-4, pin 5 SKT-4, pin 5

#### 5.4.4 TEST MODE

#### 5.4.4.1 General

The test facility enables the TCB to emulate a multichannel radio, utilising the frequencies reserved for trunking. On transmit the TCB emits a stream of 1's and 0's to set the data modulation rate.

# 5.4.4.2 To Enter Test Mode

Switch the radio off.

Remove the bottom cover (refer to Section 5.2.2).

Place a temporary short across the two pins labelled "TEST MODE".

Switch the radio on.

Check the the front panel flashes "----".

Remove the short.

Note: When in "test mode", connect the aerial socket to a dummy load to prevent interference with trunking systems and avoid testing on channels in use locally.

# 5.4.4.3 Function Selection

Use the digit entry buttons to set up the desired function number from the list below:

modem control:	10 11 12 13	continuous zeros continuous ones preamble modem Tx off
mute control: -	20 21 22 23	mute the receive audio unmute the receive audio mute the microphone audio unmute the microphone audio
PA control:	$\frac{30}{31}$	inhibit the PA enable the PA
RSSI threshold set up:	61 62 63 64 65	set up L1 threshold value set up L2 threshold value display RSSI level (averaged) display L1 threshold value display L2 threshold value

Press the AUX button to execute the function.

For radio control functions, an "A" will appear in the display to indicate that the number is valid and that the function has been executed.

To set an RSSI threshold value:

Apply a signal to the radio at the threshold level.

Select the required function.

Two dashes will appear in the display while averaging of the RSSI signal is taking place.

When averaging is complete, the result is displayed (in decimal, full scale = 255) and stored in the radio's database. The database checksum is automatically updated.

Function 63 displays the averaged RSSI level and also indicates when the threshold values have been exceeded. The leftmost decimal point lights when L1 is exceeded, the rightmost when L2 is exceeded.

#### 5.4.4.4 Channel Selection

Use the digit entry buttons to set up the desired one, two or three digit channel number.

Press the CALL button to execute the channel change. A "-" will appear in the display to indicate that the channel number is valid and that the channel change has been executed.

The radio can be incremented to the next channel by grounding the "EMERGENCY" line (available on the power connector). Channel incrementing starts from the last programmed channel. When the highest valid channel is reached, the radio will reset to the lowest valid channel at the next increment.

### 5.4.4.5 Power Up State

In test mode the radio powers up in the following state:

- modem Tx off (13)
- receive audio unmuted (21)
- microphone audio unmuted (23)
- PA enabled (31)
- receive mode (PTT released)
- lowest valid channel

#### 5.4.4.6 General

An invalid number selection is indicated by a "UA 2" message in the display.

The microphone pressel functions as per normal radio operation. The 'TX' LED indicates that the radio is in transmit mode but does not necessarily mean that it is transmitting (e.g. the PA could be inhibited).

The 'SYS' LED indicates the synthesiser lock status, and is on whenever the synthesiser is out of lock.

The 'GO' LED indicates the squelch status and is on whenever a signal is detected on the selected channel.

#### 5.5 VCO ALIGNMENT

Connect the T545TR to a dummy RF load.

Plug a UHF frequency counter onto radio test plug TP3:

Connect:-

centre pin to ground left pin to Rx VCO

right pin to Tx VCO

Enter "test mode" as described in Section 5.4.4.2.

Monitor the loop voltage (centre pin of radio TP2) with a high impedance voltmeter (0-10V range).

Select the highest channel.

# 1. Receive Mode

Adjust CV191 for >6.25V and <6.5V at radio TP2.

Check that the frequency is:

Sub-band I

Sub-band II

207.4875MHz

Sub-band III

#### Transmit Mode (PTT switch closed)

Adjust CV232 for >6.25V and <6.5V at radio TP2.

Check that the frequency is:

Sub-band I

Sub-band II

199.4875MHz

Sub-band III

Note: A loop voltage of less than 0.6V or more than 7.5V indicates the synthesiser is out of lock.

#### 3. Select channel 1.

Check that the voltage at radio TP2 is more than 3.5V in both transmit and receive modes.

# 5.6 REFERENCE FREQUENCY ADJUSTMENT

The 12.5kHz reference frequency must be accurately set. This is measured indirectly by monitoring the VCO frequency.

Connect a frequency counter to the VCO output (radio TP3).

Select channel 1.

Adjust L30 for the correct VCO frequency +100Hz.

Repeat this measurement for both receive and transmit.

# 5.7 TRANSMITTER ADJUSTMENTS

### 5.7.1 ALIGNMENT

Connect a power meter to the aerial socket.

Set RV256 (power control) fully clockwise (as viewed from the component side).

Enter "test mode" as described in Section 5.4.4.2.

Select the middle channel.

Close the PTT switch.

Adjust CV272 for maximum power.

Adjust CV289 for maximum power consistent with best efficiency.

Note: For optimum efficiency, the settings of CV272 and CV289 should be checked on the lowest, middle and highest channels.

Adjust RV256 to reduce the power output to 24W.

Check that the middle channel frequency is within 100Hz.

Select the highest channel.

Close the PTT switch.

Check that the power output is >23W and <25W.

Select channel 1.

Close the PTT switch.

Check that the power output is >23W and <25W.

Check that the transmit current does not exceed 5A for 25W output with 13.8V at the set.

# 5.7.2 MODULATION ADJUSTMENT (SPEECH)

Enter "test mode" as described in Section 5.4.4.2.

Select channel 1.

Disable the modem as described in Section 5.4.4.3.

Connect an audio signal source of approximately 600 ohms impedance across the microphone (level 25mV PD).

Close the PTT switch and measure the deviation.

Sweep the audio frequency between 300Hz and 3kHz to check for maximum deviation.

Note the audio frequency at which the maximum deviation occurs, and then check the deviation on the middle and highest channels.

Set the channel with the highest deviation to ±2.5kHz maximum.

# 5.7.3 MODULATION ADJUSTMENT (SIGNALLING)

Select the middle channel.

Disconnect the audio generator and enable "Preamble" from the modem as described in Section 5.4.4.3.

Mute the microphone as described in Section 5.4.4.3.

Set the data deviation to  $\pm 1.5$ kHz via RV1 on the TCB.

Check the lowest and highest channels to ensure that the deviation is between  $\pm 1.3$ kHz and  $\pm 1.7$ kHz.

#### 5.7.4 RF POWER ADJUSTMENTS

Note: To comply with licence requirements when used in a fixed station role with the T508 power supply unit (e.g. as a despatcher), the T545TR should have the RF power output reduced.

Connect a power meter to the aerial socket.

Set RV256 (power control) to adjust the RF power output to the required level.

For use with the T508 power supply unit, the RF power output should be adjusted to the level required to achieve the correct rating (ERP) as permitted in the relevant licence document for the installation, allowing for antenna gain and feeder/connector losses.

Do not adjust the RF power output level for less than 5W.

#### 5.8 RECEIVER ADJUSTMENTS

### 5.8.1 RECEIVER COIL TAP CHANGES

For receiver operation between 175 and 200MHz:

Disconnect receiver coil links 'b' and connect receiver coil links 'a'.

For receiver operation between 200 and 225MHz:

Disconnect receiver-coil links 'a' and connect receiver coil links 'b'.

#### 5.8.2 MIXER ALIGNMENT

Enter "test mode" as described in Section 5.4.4.2.

Select the middle channel.

Align L15 for a maximum reading, using the method described in Section 5.9.2.3.

Select the lowest channel and note the mixer injection reading.

Select the highest channel and adjust L15 for the same reading as the lowest channel.

Repeat the two previous instructions until the mixer reading is identical on the highest and lowest channels.

#### 5.8.3 RECEIVER ALIGNMENT

Turn RV149 (squelch) fully clockwise (as viewed from the component side of the board).

Do not further adjust L15.

Select the middle channel.

Connect a signal generator modulated to ±1.5kHz at 1kHz AF.

Connect a Sinad meter across the speaker terminals.

Increase the signal generator output until 12dB Sinad is reached.

Tune L11, L12, L13 and L14 for best Sinad.

Repeat the above instruction while reducing the signal generator level until it reads better than -117dBm for 12dB Sinad.

Select the lowest channel.

Set the signal generator to -94dBm and monitor the RSSI voltage (SKT-3, pin 3).

Tune L11 and L13 for maximum RSSI voltage.

Select the highest channel.

Tune L14 for maximum RSSI voltage.

Check that the receiver sensitivity is better than -117dBm on the lowest, middle and highest channels.

Adjust RV149 so that the squelch opens at 12dB Sinad on the middle channel.

### 5.8.4 RSSI ALIGNMENT

Complete the receiver alignment as instructed in Section 5.8.3.

Select the middle channel.

Enter the L1 and L2 levels as described in Section 5.4.4.3.

Set the L1 threshold level with an input signal of -108dBm.

Set the L2 threshold level with an input signal of -94dBm.

Check the L levels as follows:

Enter "63" on the display and push "AUX": the average RSSI level will be displayed.

When the RSSI level exceeds L1, the leftmost decimal point will light.

When the L2 level is exceeded, the rightmost decimal point will light.

Ensure that the L2 level is reached with a signal generator input of  $-94dBm \pm 6dB$  across the band.

### 5.9 RADIO FAULT FINDING

#### 5.9.1 GENERAL

During servicing it may be necessary to measure specific performance parameters as a means of verifying the presence of a fault condition.

The following performance tests provide a means for checking the various two way radio parameters. When used in conjunction with the voltage level test points which are given on the Circuit Diagrams a fault can be readily pinpointed.

## 5.9.2 RECEIVER PERFORMANCE TESTS

Carry out the following checks in "test mode" after the alignment has been completed.

#### 5.9.2.1 To Check The Squelch Operation

Select the middle channel.

Connect a Sinad meter across the speaker terminals.

Connect a VHF signal generator to the aerial input terminal.

Increase the signal generator level until the squelch just opens (approx. -118dBm for 12dB Sinad).

Reduce the signal generator level until the squelch just closes.

The difference should be approximately 6dB.

### 5.9.2.2 To Check The Audio Output Level

Connect an AC mV/meter and an oscilloscope across the speaker terminals.

Connect a VHF signal generator to the aerial input socket, with the output set to -107dBm (1 $\mu$ V) modulated to  $\pm 2.5$ kHz deviation at 1kHz.

Set the volume control to the onset of clipping.

The receiver output should be 3.7V across 4 ohms at +13.8V supply.

Check the distortion with the aid of a distortion analyser connected across the speaker terminals.

The distortion should not exceed 5%.

## 5.9.2.3 To Check The VCO Injection To The Mixer

Connect a DC EVM to the junction of R107/C112 via a 33k ohm isolating resistor at the probe tip.

Short the two gates of the mixer J-FET's together.

Check that the EVM reads approximately 2V DC.

Remove the short and note the increase in EVM reading.

The EVM reading should increase by approximately 0.5V at the band edges and 2V at the band centre.

### 5.9.2.4 To Check The Sinad Sensitivity

Connect a Sinad meter across the speaker terminals.

Connect the signal generator to the aerial input terminal.

Set the signal generator accurately to the receive frequency.

Couple a 21.4MHz reference oscillator loosely into the receiver IF stage, tune the signal generator for a zero beat, then uncouple the reference oscillator.

Set the signal generator deviation to ±1.5kHz at 1kHz.

Set RV149 fully clockwise, as viewed from the component side of the main PCB.

Note: It is important that the modulating frequency matches the notch of the Sinad meter.

Set the signal generator output level to zero.

Increase the signal generator output level until a Sinad of 12dB is reached.

The signal generator output should not be greater than -117dBm and is typically -119dBm.

Reset RV149 so the squelch opens at 12dB Sinad.

## 5.9.2.5 To Check The Signal+Noise To Noise Ratio

Set the signal generator output level to -107dBm (1 $\mu$ V) modulated to +2.5kHz deviation at 1kHz.

Replace the Sinad meter with a mV/meter.

Set the volume control for a reading of 0.8V ('0'dB) on a convenient scale on the mV/meter.

Switch the signal generator modulation off.

Note the reading on the mV/meter.

The fall in reading when the modulation is switched off should be at least 27dB.

### 5.9.2.6 To Check The Ultimate Signal To Noise Ratio

Note: A good quality low noise RF signal generator should be used for this check (e.g. HP8640B or 8656).

Set the signal generator to give an 'on channel' signal, modulated to  $\pm 2.5 \text{kHz}$  with a 1kHz tone.

Set the signal generator output level to -47dBm.

Connect an AC mV/meter across the speaker terminals.

Adjust the volume control for a reading of 0.8V ('0'dBm) on a convenient scale.

Turn the signal generator modulation off.

Note the reading on the mV/meter.

The fall in reading when the modulation is switched off should be at least 45dB. (A low reading could be caused by a faulty IC7 or a noisy VCO).

### 5.9.3 TRANSMITTER PERFORMANCE TESTS

Carry out the following checks in "test mode" after the alignment has been completed.

### 5.9.3.1 Audio Processor

#### (a) TO CHECK THE LIMITER CIRCUIT

Connect an oscilloscope to monitor the waveform at pin 14 of IC2d.

Provide an audio signal to the audio processor as in Section 5.7.2.

Set the frequency of the audio signal generator to 1kHz.

Slowly increase the signal generator output level until the waveform begins to distort (squaring), indicating that limiting has commenced.

Any further increase in signal generator output level should not increase the amplitude of the waveform.

#### (b) TO CHECK THE AUDIO ALC OPERATION

Set up the audio signal as described above (Section 5.7.2).

Set the oscilloscope to monitor the waveform at pin 1 of IC2a.

Connect an EVM to the junction of C49/R52.

Increase the output level of the signal generator to 10dB above the limiting level [Section 5.9.3.1 (a)]. Note the amplitude on the oscilloscope, then increase the signal generator output level by another 10dB.

Check that the amplitude of the waveform does not increase or distort significantly.

The EVM should show a 'positive DC' reading.

### (c) TO CHECK THE GAIN OF THE AUDIO PROCESSOR

Provide an audio signal to the audio processor as in Section 5.7.2.

Connect the T545TR antenna output through a 50dB RF power attenuator (see Section 5.4.1 item 15) to a modulation meter.

Connect a mV/meter across the microphone terminals on the main PCB (to monitor the input to the audio processor).

Set the frequency of the audio signal generator to 1kHz.

Check the deviation control, RV79, as in Section 5.7.2.

Slowly increase the output level of the audio signal generator until a deviation of  $\pm 3 \, \text{kHz}$  is reached.

Check that the mV/meter reads approximately 1mV rms.

Note: The audio processor gain must be checked at a level below that at which the audio ALC or limiting are influencing the measurements.

#### 5.9.3.2 Modulation Characteristics

### (a) TO CHECK THE ABOVE LIMITING RESPONSE

Connect the T545TR aerial output via a 50dB RF power attenuator to a modulation meter.

Provide an audio signal to the audio processor.

Increase the audio signal generator output level to 20dB above the limiting level [Section 5.9.3.1 (a)].

Vary the frequency of the signal generator between 0.3 and 10kHz.

Note the deviation on the modulation meter.

Between 300Hz and 3kHz the deviation should be within 4dB of maximum.

Above 3kHz the deviation should decrease in excess of 25dB/octave.

### (b) TO CHECK THE BELOW LIMITING RESPONSE

Decrease the audio signal generator output level to 10dB below the limiting level [Section 5.9.3.1 (a)].

Vary the frequency of the audio signal generator between 0.3 and 10kHz.

Note the reading on the modulation meter.

From 300Hz to 3kHz the deviation should increase at the rate of 6dB/octave (+1, -3dB relative to 1kHz).

Above 3kHz the deviation should decrease in excess of 25dB/octave.

### 5.9.3.3 To Check The RF Power Control Circuit

Connect an RF power meter to the transmitter output.

Close the PTT switch.

Ensure that the transmitter is correctly tuned (Section 5.7).

Vary the supply voltage between 10 and 16V.

Above 13.8V the RF power output should not increase by more than 2W.

At 10.8V the RF power output should be more than 10W.

#### 5.9.3.4 To Check The VCO Control Range

Plug a frequency counter onto the VCO test plug (TP3).

Short the middle pin on radio TP2 alternately to each of its outer pins.

The frequency shift should be more than 10MHz.

## 5.9.4 SYNTHESISER FAULT FINDING

Carry out the following checks in "test mode" after the alignment has been completed.

#### 5.9.4.1 If The VCO Gives No Output

Ensure the frequency counter is connected to the correct pin of radio TP3.

Check the supply voltages at R191 (6.5V) and L38 (8V) for the Rx VCO, and at R230 (6.5V) and L55 (8V) for the Tx VCO.

Remove the VCO box and check for shorts inside.

Check the gate and source voltages as per the Circuit Diagram.

## 5.9.4.2 If The Synthesiser Does Not Lock Up

Check the VCO control range following the instructions in Section 5.9.3.4.

If the control range is less than 10MHz, check the circuit for faults between radio TP2 and the varicaps. The voltage on the varicaps must be the same as the loop voltage.

Tune the Rx VCO until its programmed frequency is within the switching range.

If the loop voltage is still either less than 0.6V or more than 7.5V, check pin 7 and pin 8 of the synthesiser (IC 8):

(Under normal operating conditions the loop voltage is between 1.75 and 6.5V and both pin 7 and pin 8 are high, except for very narrow pulses [100ns] at the same rate as the reference frequency.)

- (a) If pin 7 pulses low and the loop voltage is low (TP2), or if pin 8 pulses low and the loop voltage is high, check the circuitry between R176/D32 and TP2. The voltage at C176 (use a 10M ohm probe) and TP2 should differ by no more than 200mV. If not, check the behaviour of the buffer amplifier (Q29, Q30).
- (b) If both stay high and the loop voltage is high, check the crystal oscillator.

Measure the VCO frequency.

Measure the prescaler output frequency (pin 3).

Check that fprescaler = fVCO/40

Note: The prescaler should not be loaded with 50 ohms - a 1M ohm input counter must be used.

Check that the input voltage of the synthesiser (pin 1) is more than 500mV pp.

## 5.9.4.3 To Check The VCO Output Frequency Stability

If the synthesiser locks up but does not reach a stable VCO output frequency, or if the VCO output frequency is a few channels off frequency, check:

- (a) that the input power to the prescaler from the VCO is not too low; (Check the VCO output power and the circuitry between the VCO and the prescaler.)
- (b) that the modulus control pulse (pin 1 of the prescaler) is more than 4.0V;

## 5.9.4.4 To Check The Transmitter Switch-On

If the synthesiser locks up but there is no transmitter power, check:

- (a) that, if the synthesiser is locked, the lock detect output (IC8, pin2) is high (this output pulses low if the synthesiser is out of lock);
- (b) that the voltages are as shown in the Circuit Diagram (Q25, Q43).
- (c) the PA inhibit circuit.

## 5.10 TRUNKING CONTROL BOARD FAULT FINDING

#### 5.10.1 GENERAL

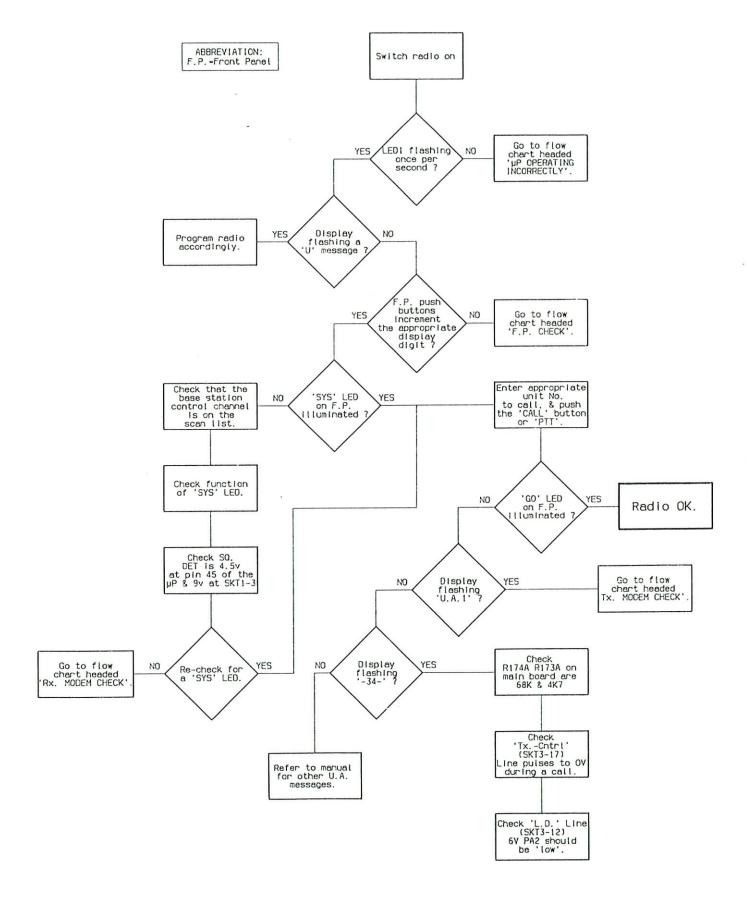
Five flow charts are provided in this Section as an aid to fault finding the TCB. The aim of these charts is to enable simple faults to be traced quickly to a certain area of the board.

If there is a regular fault that requires replacement of the TCB, please contact the Product Support Group at Tait Electronics Ltd.

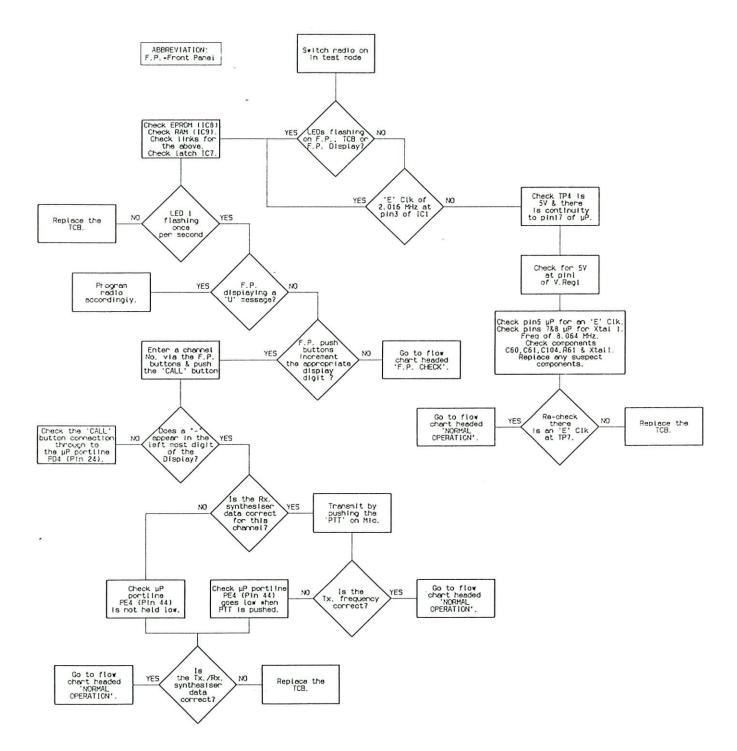
Start the fault finding by referring to Section 5.10.2, "Normal Operation", referring thereafter to the other flow charts as directed (removal of the radio covers is described in Section 5.2).

Note: When servicing is complete, always check that the "radio personality" is correct for the user. A before and after check of the "radio personality" is recommended.

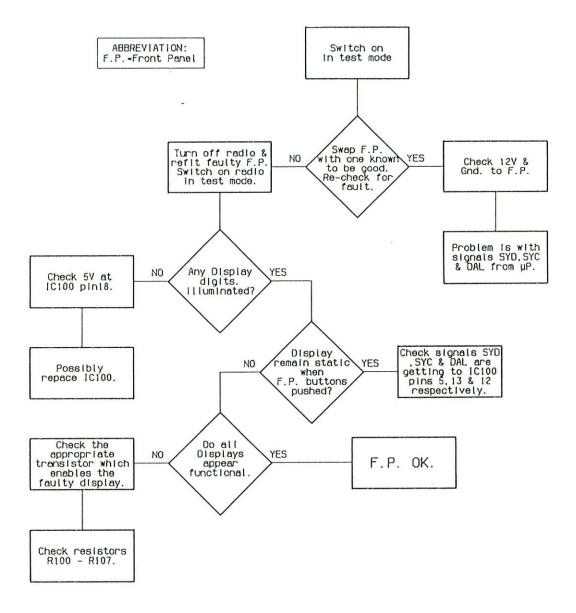
### 5.10.2 NORMAL OPERATION



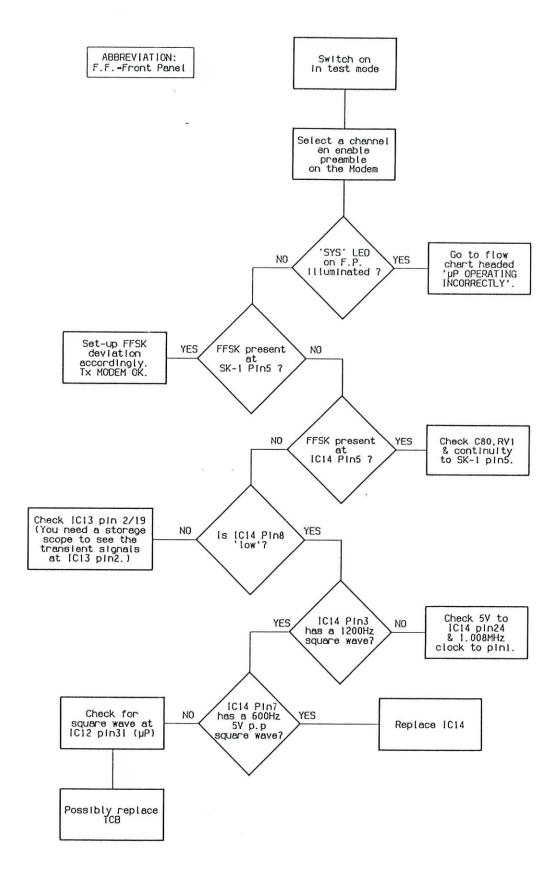
## 5.10.3 MICROPROCESSOR OPERATING INCORRECTLY



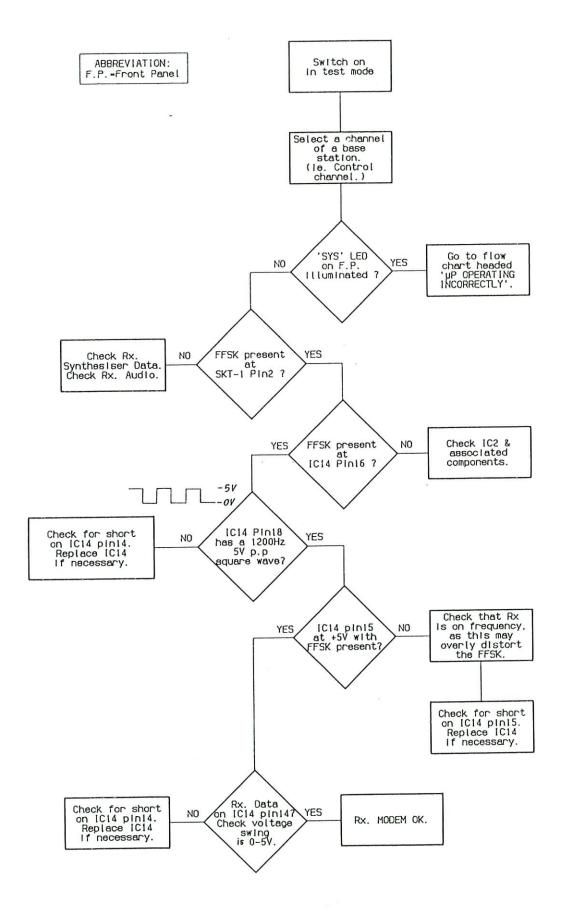
### 5.10.4 FRONT PANEL CHECK



## 5.10.5 TX MODEM CHECK



### 5.10.6 RX MODEM CHECK



•	2 8 8	
	8	
	*	

## SECTION 6 PARTS LIST

## 6.1 GENERAL

The 10 digit numbers (000-00000-00) in this parts list are "internal part numbers" (IPN's).

Your spare parts orders can be handled more efficiently if you quote: equipment type, circuit reference and IPN, along with a brief description of the part.

## 6.2 B545-91 MAIN PCB MANUAL INSERTION

## 6.2.1 TRANSISTORS

IИТ	ERN	IAL	. F	AR	T	<b>40</b> .		-		QTY/SET	DESCRIPTION	REFERENCE	CH/N
0	0	0		0	0	1	1	7	0	2	BD136 TRANSISTOR	φ3, φ45	
0		0		0				3			BF509	Q15	
0	0	0	0	0	0	2	2	3	0	1	2N4427	Q42	
3	6	2	0	0	0	1	0	0	હ	1.	GASKET, SILICONIE RUBBER 70-39	φ42	
0	0	0	0	0	0	3	1	.1	0	١	85987	Q18	
0	0	0	0	0	0	3	1	7	5	2	35K67K	φ34, φ37	

### 6.2.2 DIODES

0	0	1	0	0	0	3	7	6	0	T	5R2607 DIODE	72.1	
0	0	1	0	0	O	1	2	5	0	1	um9401 .	D62.	
0	0	1	0	0	0	1	2	5	3	3	88405 VARICAP DIODE	D35, D50, D51	
0	0	1	0	0	0	1	2	6	6	1	HP 5082 - 3080 DIODE	р63	
0	0	.1	0	0	U	1	3	4	5	1	15597/2 DICDE .	P61	

## 6.2.3 INTEGRATED CIRCUITS

0	0	2	0	0	0	1	4	4	0	1	MLM 324 INT CCT	Tc2
0	0	2	0	0	0	1	4	7	0	J	mc3357	Ic7
0	0	2	0	0	0	1	4	9	1	1	40018	Jc 1
0	0	2	0	0	0	1	5	7	0	١	40663	TC3
U	O	2	0	0	0	1	7	5	0	1	mc12016	IC9
0	0	2	0	0	0	1	7	6	0	1	MC1 45152	Ic &

## 6.2.4 CAPACITORS

0	1	1	0	0	5	0	0	0	1	_3	OPS CAP \$0.25P PLOU SOV CERAMIC	c286	
0	1	1	0	3	6	ક	0	0	1		680P CAP 10% NIKS 63V CERAMIC	c55	
0	1.	1	0	4	4	7	0	1	3	0.001	4m7 cmp 10% 1/e B 50V DISE CERRANIE	cT cT	
0	1	7	1	5	4	7	0	O	1	1	47m CAP 20% 50V SURFACE BARRIER	c253,	35.0 75
0	2	0	0	7	1	0	0	0	5	4-	LU EAP SOV ELECTRO 4×7mm	C4, C17, C85, C86	
0	2	0	0	8	4	7	0	0	5	10	474 CAP 164 ELECTRO 6:3×7mm	C5, C44, C49, C90, C127, C171 C179, C151, C152, C251	
0	2	0	0	9	1	0	0	0	4	5	1000 CAP 100 ELECTRO 63 49mm	C107, C10B, C10C, C10D, C16	
0	2	0	0	9	2	2	0	0	5	1	2204 CAP 160 ELECTIZO 6.3×11mm	C92	
0	2	0	1	9	1	0	0	0	2	1	1000 LAP 16V ELECTRO 12+20mm	C2	
0	2	5	0	6	2	2	0	0	1	3	2200 CAP 35V TANT BEAD	C41, C183, C227	
0	2	5	0	7	3	3	0	0	2	_11	343 CAP 16V TEINT BEAT	c143	

## 6.2.5 RESISTORS

0	3	2	3	1	4	7	0	0	0	1	4ET RESISTOR 5% RUR M/F LOX4mm	TR270
0	3	2	3	2.	1	0	0	0	0	1	10€	R271
a	4	.2	0	4	2	2	0	O	1	2.	2K2 PRESET KES JOHN FLAT	RV79, RV256
0	4	2	0	5	1	0	0	0	1	1_	Tok "	Rv149
0	4	2	0	5	4	7	0	0	1	1	4714	RV157

# 6.2.6 COILS

0	5	0 0	0	0	1	6	2	9	1	COIL TAIT No 629	L16	
0	5 0	0 0	0	O	1	6	4	0	_1	COIL TRIT No 640	L51	
0	5 0	0 0	0	0	1	6	4	1	1	COIL TRIT No 641	L36	
0	5 0	0 0	0	0	1	6	4-	2	5	COIL TAIT No 642	L11, L12, L13, L14, L15	
0	5 1	2 0	8	1	2	5	1	5	í	COIL A/W 1.57 2.5mm HOR	LIB	
0	5 2	2 (	8	1	3	0	1	5	2	coic A/W 1.57/3.0mm HOR	L57, L59	
0	5 :	2 0	8	1	3	0	2	5	2	COIL A/W 2.57/3.0mm HOR	L56, L70	
0	5 1	2 0	8	1	3	0	3	5	1	COIL A/W 3.5T/3.0mm HOR	L62	
0	5 2	2 0	8	1	3	0	5	5	2	COIL A/W 5.5T/3.0mm NOR	LSG, L87	
0	5 8	2 0	8	1	3	5	1	5	1	COIL 19/W 1.5T/3.5mm HOR	L67	
0	5 2	2 0	8	1	3	5	2	5	1	COIL A/W 2.5T/3.5mm HOR	L79	
0 5	5 5	2 0	8	1	3	5	3	5	1	COIL A/W 3.5T/3.5mm HOR	L65	

0	5	2	0	8	1	4	0	6	5	1	CUIL A/W 6.5T/4.0mm HOR	L77
0	5	2	0	8	1	5	0	2	5	2	COIL A/W 2:5T/5:0mm HOR	L65, L68
0	5	2	O	8	1	5	0	5	5	1	coil A/W 5.57/5.0mm HOR	L54-
0	5	6	0	0	0	4	0	1	7	1.	FXD IND TAIT NO 17 (GT on 38)	L75
0	5	6	0	0	0	2	1	0	1	6	1.5 MH FYD INDUCTOR	12, 140, 158, 161, 182, 183
0	5	6	0	0	0	2	1	0	2	3	100mll .	L31, L33, L45
0	5	6	0	0	0	2	1	0	4	2	330mll	L55, L60
0	5	6	0	0	0	2	1	0	7	11	33uH	123
0	5	6	O	0	0	2	1	5	2	5	520mH FYD INDUCTOR (NO MAGNETIC)	135, 137, 138, 150, 152

# 6.2.7 MISCELLANEOUS

0	6	2	0	0	0	1	0	1	1	5	CFIN 14mm2 ×19mm	FOR L11, L12, L13, L14, L15	
0	6	5	0	0	0	1	c	O	4	2	F8 FERRITE BEHD 4-2-5	176 1, FIT ONE IN PURCE OF RIS	8S/9 449
0	6	5	0	0	0	1	0	0	8	3	453 FERRITE BEATD 3x0.7 + 10mm	L64, L65, L74	
2	0	0	0	0	0	1	0	0	4	0.19-	0.7mm TINNED COPPER WIRE	20mm FOR FS BEND 'RIS'	
2	2	0	0	0	0	1	1	1	2		PRINTED CCT BORRED 7545-39		
2	4	0	0	0	0	2	0	5	9	1	HEADER 3WAY 1 ROW	TP3	
2.	4	0	O	0	0	2	0	7	2	_1	HEADER 2WAY ULTREY	P. C	
2	4	O	0	0	0	2	6	0	3	2	HEADER BURY 1 Row 26mm	72.7 TP2.	
2	4	0	o	0	0	2	6	0	5	2	HEADER SWAY 1800 26mm	PLI PLZ	
2	4	0	0	0	0	2	6	1_	0	3	HEMPER JOWAY 1 Kow 26mm	PL+	
2	4-	0	0	0	0	2	6	1	ક	1	HEADER ISWAY I ROW 26mm	PL3	
2.	7	4	0	0	0	1	0	0	2	1	CRYSTAL. 20.945MHZ	×i	
		1	1				1		н	1	XTAL 12.8MH2 TAIT SPEC TE9.		81 p-res
2	7	6	0	0	0	1	0	1	2	1	CERAMIC DISCRIMINATOR CDB 45507	CD1	
2	7	6	0	0	0	1	0	1	3	1	CERAMIC FILTER CFW455G	CF1	
2	7	6	0	0	0	1	0	4	4	1	CRYSTAL FILTER 21.4MH 7.5KIN 4RE (2 Pes	XF1A, XF1B	
3	4	9	0	0	0	2	0	3	3	8000	TESPSPONCE 12 3mm 5/A ONE SIDE		20/p-42

## 6.3 B545-92 MAIN PCB AUTO INSERTION

## 6.3.1 TRANSISTORS

0	0	5	0	0	7	0	6	0	2	BC327 TRANSISTOR	Q4,Q5
0	0	5	0	0	1	1	1	0	13	8<5478	Q1, Q2, Q7, Q10, Q19, Q20, Q21
											φ22, φ27, φ29, φ31, φ43, φ44
0	U	5	0	0	1	1	3	0	દ	B<557B	φ6, ψ11, ψ25, ψ26, ψ28, ψ30
-						-		-		•	Q32, Q35
0	0	5	0	0	2	0	1	8	2.	8F247A	916,917
0	0	5	0	0	3	1	9	5	2.	MP53646	940,941
0	0	5	0	0	3	3	1	0	2.	3.310	933, 936
	0 0 0	0 0	0 0 5			0 0 5 0 0 1	0 0 5 0 0 1 1	0 0 5 0 0 1 1 3	0 0 5 0 0 1 1 1 0	00500111013	0 0 5 0 0 1 1 1 0 13 8c5478  0 0 5 0 0 1 1 3 0 8 8c5578  0 0 5 0 0 2 0 1 8 2 8F247A  0 0 5 0 0 3 1 9 5 2 MPS3646

## 6.3.2 DIODES

0	0	1	5	0	0	1	2	0	1	0.016	1N4148 DIODES	D2, D6, D7, D8, D14, D15, D16	
												D22, D23, D24, D25, D31, D31A	
			_	-								D32, D33, D65	
0	0	1	5	0	0	1	5	0	9	1	B2X79/C3V9 ZENER	D3	
0	0	1	5	0	0	1	5	1	1	.1	BZ×79/c5v1	D4-8	
0	0	1	5	0	0	1	5	1	4	1.	BZx79/c6V8	D21	59/6 3K

## 6.3.3 CAPACITORS

0	1	1	5	1	2	2	0	0	1	_1	2P2 EAR 10:25P HPO 50/63V CERAMIC C300
0	_1_	1	5	1	2	7	0	0	1	3	2P7 ±0.5P C114, C192, C234
0	1	1	5	1	3	9	0	0	1	_1_	3P9 <194-
0	1	1	5	1	ઇ	2	0	0	1	1	SP2
C	1	1	5	2	1	0	0	0	1	3	10P <198, C199, C250
0	_1	1	5	2	1	2	0	0	1	2	12P CAP 5% NPO 50/3V CERAMIE C116, C190
0	1	1	5	2	1	5	0	0	4	4	15P
0	.1	1	5	2	1	5	0	0	6	L.	15P CAP 5% N750 50/63V CERAMIC C174
0	1	1	5	2	2	2	0	0	6	1	22P " N750 " C173
0	1	1	lo	2	2.	7	0	0	1	4-	27P CAP 5% N150 50/3V CERAMIC C130, C229, C243, C244
0	1	1	5	2	3	3	0	0	1	4-	33P " "
0	1	1	5	2	3	9	0	0	1	1	398
0	1	1	5	2	4	7	0	0	1	2	478 CAP 5% NISO 50/34 CERAMIC C46, C196
0	1	_1	5	2	6	8	0	0	1	11	66P
0	1	1	5	3	1	0	0	0	1	5	1000
0	1	1	5	3	1	2	0	0	1	2	1206

0	- 1	. 1	-1	- 1	1		_		. 1	8	1506	c135, c247, c248, c249,	T	
0	1	1	5	3	1	5	0	0	3	8	15012	c254, c255, c269, c84	_	
			1	1			-		_		220P CAP 10% N750 50/3V CERAMIC	-15 - 51 p - 5239 - 5242 - 5247	-	
0	1	1	5	3	2	2	0	0	1	7	2207 CMP 10/ N750 5013V CERNAMIC	C 276, C2 84		
4	1	-	-	+		$\perp$	-	_	_		470P CFIP 10% T/CB 63V CERIAMIC	<93	+	
0	1	1	5	3	4	7	C	0	2	1	47019 ZEITS 10% YEB 53V EERCHING			
0	1	1	5	4	1	0	0	0	1	15	Jm " " " " " " " " " " " " " " " " " " "	c7, c5, c11, c109, c129, c195	_	
												c220, c221, c224, c225, c236	+	
			4	-								c240, c246, c263, c265	+	
2	+	1	=				_	4	2	0:017	1 THEW 4-7 CAP 10 1/eB 50V CERIMIE	c6, c9, c42, c118, c121, C89	39	16-302
	1	7	2	4	7	/		x_	3	0017	717 - 11	c123, C125, C125, C132, C140		
												c141, C142, C172, C180, C241	-	
4	_	_	-	-								C285	-	
-	1	-	2	-	-	0	0	0	-		IPO CAP NPO SOV OGOS CHIP CER	cc97		
0	1	5	5	1	7	0	O		7	1	THE CHIP INTO SET SEE			
0	1	5	2	1	1	8	0	0	1	2	168	cc100, cc104-	-	
-	4	_		-	_	_	_	_	,		309	cc111, cc235		
0	1	5	2	7	5	2	0	0	7	2	31-9	CCSSS		
0	1.	5	2.	1	5	6	0	0	1	6	5P6	cc 98 , cc 99, cc 101, cc 103	_	
											,	ce106, ce107	-	
-	-	-		-	_		_	_				cc102, cc105	+	
0	1	5	2	1	6	8	0	0	1	2	6P\$	26107, 26105		
0	1	5	-2	1	8	2	0	0	1	1	862	cc94		
													_	
0	1	5	2	2	1	0	O	0	1	2	100	cc96,cc230	-	
	-	_		_	_	_		_	1	1	120	cc95		
0	-1	5	2	2	1	12	0	0	1-	7	121-			
0	1	5	2	2	1	8	0	0	1	3	188	cc112	_	
													-	
0	1	5	2	2	2	2	0	C	1	1	22.P	ce115	-	
	-	_	-	-	1	17	0	-	1	1	47P	cc237		
Ü		_											_	
0	1	5	2	2	5	6	0	0	1		56P	CC258	+	
-		_		1	-	-	-	-	8	3	1m capp XTR 50V 0805 CHIP	cc193 cc233,cc275		
	2								2	1	IN CAP SOV ELECTRE SYLLING.	C 87		
0	2	0	5	8	1	0	0	C	3	2_	LOU CAP SOV ELECTRU SVIIM-	C110, C268	+	
_		_	_	-	-	-	-	-	2	1	474 CAP 16V ELECTRO 6x11mm	c279		
0	2	0	5	8	-4	1	1	-	1 6	1	474 CHT 18V SCC 1885 B711			
0	2	2	5	4	1	c	c	1	0	4	In CAP 10% 634 MYLAR POTTED	C184, C186, C226, C226A	5	39/06293
-		-	_	-	-	+	-	+	-	<b>H</b>	2~~ 9	C50, C88	-	n/a 293
0	2.	2	5	4	2	12	10	1	0	2	2~2			
0	2	2	5	1	4	7	C	1	0	3	4~7	c45, c178, c238	9	34KP XI3
_		-		-	-	-	-	+	+		1000 " " 63V MYLAR POTTED	c64, c152, c153, c177, c264		
0	2	2	5	5	1	10	0	-	1 0	6	tom " " 63V MYLAR POTTED	c54		
													_	
0	2	2	5	5	1	5	0	1	0	2	15m	c51, c52.	-	
-	-	-	-		1	+	-	-	-	-	22~	c47, c53		
0	2.	2	5	5	2	- 2	10	1 3	0	2_	22~ "	, ===		
U	2.	2	5	5	4	- 7	, 0		10	41	47~	c43, c58, c68, c134, c136	$\dashv$	
					-		1	$\perp$	1	1		C170, C170A, C222, C275, C280,	-	93/1
0	2	2	5	5	6	18	1	2 1	0	1	68m " " " "	CG2 (C242		39/6 246
0	2	1-2	5	6		1	0 0	,	10	4	100m " " " "	e91, c151, c176, C122		37/6 302
F			ľ				I	I						

## 6.3.4 RESISTORS

		545-15					_						_,
0	3	0	5	1	2	2	0	2	0	3	262 RESISTOR 50/ FILM 441.6mm	734, R60, R89	
0	3	0	5	1	4	7	0	2	0	.f	4E7	2272	
0	3	0	5	1	8	2	0	2	0	1	8€5 , , , , ,	2264	-
	7	0	_	2	-	0		2		7	10E	3104, RIII RII2 RII3 R244	
				_	-	Ŭ		_				R251 R261	
0	3	0	5	2	2	2	0	2	0	٤	225	R262	-
	_	_	-		_	-	_	2	-	4	47F	K110, R225, 7247, 74269	-
0		0	5	2		-/	0			-4	475	R110, K115, K147, R167	
0	3	0	5	3	1	0	0	2	0	5	100E	<155, R191, R193, R229, R250	
	_		_		-	_		-					-
0	.3	0	5	3	1	5	0	2	0	3	150E	R119, R226, R260	
0	3	0	5	3	1	8	0	2	0	J	180E 18	3106	
0	3	0	5	3	2	2	0	2	0	3	2.20E R	243, T249, T252	-
0	3	0	5	3	7	3	0	2.	0	3	330€	<107, R118, R224	_
0	3	0	5	3	3	9	0	2	0	3	390E T	R141, TR276, TR277	
	_		-	-	-	-	-	-	-		1000	2015	
0	3	0	5	3	4-	7	0	2	0	2_	470E RESISTOR 5% FILM 4x1.6mm T	R245, R246	
0	3	0	5	3	5	6	0	2	0	2	5606	F1263, R265	
									_				
0	3	0	5	3	6	8	0	2	0	10		25, R10, R32, R40, R44, R101	+
-		-	-	-	-	-	-	-	-		. R	198, R243, P259, P274	+
0	3	0	5	4	1	0	0	2	0	12	1K R	R3, R8, R16, R52, R64, R127	29/9-472
		_										151 R154 R177, R220	21/2:412
												2248, R267	
0	3	0	5	4	1	2	0	2	0	2,	11/2	22,72117	-
0	3	c.	5	41.	4	5	0	2	0	5	1×5 T×	483,72120, R121, R188, R253	
												, , , , , , , , , , , , , , , , , , , ,	
0	3	0	5	4	2	2	0	2	0	10	2 1/2	R9, R11, R16, R41, R45, R73, R123	
-	_		H			-	-		-		R.	145, R222, R257	-
	2	0	-	4	2	7		2			2×7	4115, RILG	-
0	٥		ر	-1	_	-	Ĭ	É		_2_	. K	312   1126	
0	3	0	5	4	3	3	0	2	0	3	3K3 R	1175, R190, R231	
	-	_	-		2		_	2			71/0	27	-
0	5	0	5	4	3	9	0	2	0	11	3K9 R	5/	
0	3	0	5	4	4	7	0	2	0	13	4KT RESISTOR 5% FILM 4.1.6mm R	R1, R6, R27, R29, R63, R100, R150	np-412
											e.	173, R221, R258, R275, R280,	
			_		-	-		1200				นาวค	-
0	3	0	5	4	5	6	0	2	0	3	5KG R	U52, R184, R227	
0	3	0	5	4	6	8	0	2	0	3	6K8 73	274, R170, R192	
0	3	0	5	4	8	2	0	2	0	2_	BK2 T	K 50, R223	
	7	_	-	-	-	-		2				241 830 845 845 853 851 851	-
U	3	U	2	9	+	J		7		21		76, RS1, R103, R108, R109, R124	-
												126, R142, R144, R146, R171	
												2176, R178, R179,	
			_		77.0			- 3	2				
0	3	0	5	5	1	2	0	2	0		12K R	2148	-
0	3	0	5	5	1	5	0	2.	0	1		275, RISO, R278	
								-		5			•

5	3	0	5	5	1	5	0	2	0	1	1514	R77	
٥	3	c)	5	5	2	-1	0	2	0	4	22K	720, R61, 7159, 8228	
C	·3	0	5	5	2	7	0	2	0	2.	27K	K102, R152	
0	3	0	5	5	3	3	0	2	0	2.	33K RESISTOR 5% FILM 4.1.6mm	R50, R\$6	
0	3	0	5	5	3	9	0	2	0	2.	39K	-1-78, 1818,1	
0	3	0	5	5	4-	7	0	2	0	9	47K	R4, R25, R31, R33, R46, R64, R68, R125, R156	
	_		5								56K	R183, R187, R233	
0	3	0	5	6	6	8	0	2	0	5	100K	R174A 7219, 7252, 7287, 12196, 72240	ecto-ms.
0	3	0	5	6	1	2	0	2	0	2	120K	-R26 , 166	31/6-302
0	3	0	5	6	1	5	0	2	0	1	150K	7R14O	
0	3	0	5	6	1	જ	0	2	0.	2	18014	TR67, R147	
0	3	0	5	6	2	2	0	2	0	2	220K	T2122, T2155	
0	3	0	5	6	2	7	0	2	0	1	27014	78174,	mhium
0	3	0	5	6	4.	7	c	2	0	7	470K	R48, R49, R51, R65, R72, R88, R143	
0	3	0	5	7	1	0	0	2	0	5	JM	73.17, TR28, TR62, R153, R232	
0	3	6,	_1_	3	3	0	0	0	0	_1	100E RESISTOR 5% M/F 0805 CHIP	Re234	
0	3	6	1	3	2.	2	0	0	0	_1	220E RESISTOR 5" W/F OSUS CHIP	Rc236	
			1							<b></b>	470E	Rc 19A	
			1								TK	Rc172	
			1								516	Rc195, Rc238	
0	3	6	1	6	i	0	0	0	0	3	100K	KC197, RC235, RC237	

6.4 B545-93 T545TR MISCELLANEOUS & MECHANICAL

		_	-		_	-	_							
0	0	0	0	0	0	2	3	2	3		S'RFH1001 TRANSISTOR	Q47	-	
0	0	0		0	0	3	0	6	5	1	2N 60 GO TRANSISTOR	946		
													4	
0	0	2	0	0	0	1	3	7	0	_1	TDR 1020 INT CCT	TC4	+	
0	1	2 (	0	4	1	0	0	0	1	_3	M CAP CERAMIC FEED WIRL LEADLESS			
0	1	2	0	4	1	0	0	0	2	1	IM CAP CERAMIC FEEDTHALL WITH LEAD		4	
0	1	5	0	3	2	2	0	0	2	2	220P CAP 5% NPO, HQ 1210 CHIP	cc282, cc283	7	
0	1	5	1	2	1	0	0	0	1	2	10P CAP 200V 5×0.9mm WEDGE	c295, c299	#	
o	1	5	1	2	2	2	0	0	1	2	22P CAP " " "	c296, c298		
0	1	5	1	2	2	4	0	0	1	<u> 1</u>	24P CAP " "	c297	=	
0	1	5	1	3	2	2	0	0	1	2	220P CAP - " "	c290, c293		
	1	7	1		4	7	0		-	1	47n CAP 20% 50V CERTAMIC SURFACE BARRIER	C281	+	
		-	-	3	7		Ŭ							
0	2	8	0	2	1	0	0	0	6	_2	2/10P TRIM CAP HURATA 6×8	cv191, cv232	- 3	1/05-243
0	2	8	0	2	1	જ	0	0	1	1	2/18P TRIM COP " "	cv289	-	
0	2	8	0	2	3	0	0	0	3	1	5/30P TRIM CEP, N750 GREEN, MUR TE			
	2									4	15P CAP 5mm CASE MICA		-	
0	2	9	0	2	2	2	0	0	2	11	22P CAP 5mm CASE MICA	C270 (0N TOP OF C277)		
0	2	9	0	3	1	0	0	0	2	2	JOOP CAP 5mm CHGG MICH	C277, C288	-	
0	3	2	3	3	1	જ	0	0	0	1	180E RESISTOR 5% M/F 10:4mm	TR273		
0	4	0	0	5	1	0	0	1	3	_1	JOK LOG POT, LESS SW, PEB MITE	RV45	_	
0	5	0	0	0	0	1	6	1	7	1	COIL TAIT NO 637	L30	$\exists$	
0	5	1	0	0	0	0	6	0	3	3.	FEEDTHRU LEFID HAM2230			39/10 - SU 8109-404
	6	_			_	-	0		-7	3	FERRITE GEFID 4+2+5mm 453 KED		$\dashv$	
0	G	2	0	0	0	1	U		-/-	_ 3	LEKKILE REFIN #45742WW #22 PED			
2	0	0	0	0	0	1	0	0	4	0.59m	WIRE OFMM TINNED COPPER	25mm DC PWR FEED, 20mm COAX CONNECTOR		
2	0	1	0	0	0	3	0	0	4	360mm	WIRE 7/0.2mm PVC YELLOW	180mm SPKR/ULTRSY CONDUCTOR	-	
												90mm PCB/EXT SPKR		
2	0	1	0	0	0	3	0	1	0	210 mm	WIRE 7/0.2mm PVC PLFICK	180mm SPKR/ULTREX CONVECTOR		alba-usu
		_			_	_	_	_						
2	3	2	0	0	0	1	0	1	9	1	SWITCH PUSH LATCHING PER MTG	sw1		
2	3	2	0	0	0	1	0	2	0	2	SWITCH, RIGH MOMENTARY PEB MTG	sw2, sw3		
2	3	2	0	0	0	2	0	5	3	3	KEYBASE , ALPS SW			
2	3	2	0	0	0	2	0	2	5	3	KEYTOP , ALPS SW HLACK ASMELY			
2	4	0	0	0	0	1	0	6	0	1.	PLUG HOUSING, 4WAY PAL MTG	20 - 2 - 2 - 2		
2	4	0	0	0	0	1	0	6	1	4	Rug TERMINALS, SOLDER TAG	Power commestor		
			0	2	1	0	0	1	1.	. 1	SOCKET COAXING BUCKHEITD MITG			
2	4-	0		-					1					
					0		0	7	2		MITREX 2 MAY SKT HOUSING	1		
2	4	0	0	4		2			2		ULTREX 2WAY SKT HOUSING	SPERKER COMPRECTOR		

2 5 7 0 0 0 0 1 0 1 0 1 4 1 1 PRINTING PRINTS ALTS ALTS ALTS ALTS ALTS ALTS ALTS AL	2	5	ol	اه	ol	ol	4	0	1	4		SPETKER & OFF		- [	
3 0 2 0 0 4 0 0 4 2 3 Fran Summer Transition Process P		4	$\perp$											-	
3 0 2 0 0 0 4 5 0 3 5 1 THERED BOSS AND 14 FINITIS AND 15 1 THE COURT COVER FINITIS AND 15 1 THE COVER FINITIS AND 15 THE COVER FINITIS A	2	5	2	0	0	0	1	0	3	6	_1_	MICROPHONE, GOUR FOSTER, GUMY RUG 4 GROMMET		+	
3 0 3 0 0 2 0 0 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1	3	0	2	0	0	4	0	0	4	2	3	PLISH BUTTON PLASTIC ASMISSS			
3 0 3 0 0 2 0 0 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1	-	_	-	-	4		$\perp$	_						+	
3 0 3 0 0 2 0 0 2 1 1 1 Symmon Course Silving	3	9	2	9	9	4	5	0	3	5	11	THREADED BOSS A4M2148		_	
3 0 3 0 0 5 0 0 7 1 1	3	0	3	0	0	2	0	0	2	0	1	TOP COVER FIM1934		_	
3 0 3 0 0 5 0 0 7 1 1	1	-	-	-	-					_		- State of the sta		+	
3 0 5 0 0 1 3 0 6 6 1 1 MENTSHAM, TREVER, RAFFERD  3 0 5 0 0 1 3 0 7 1 1 1 MENTSHAM, TREVER, RAFFERD  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP32  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 2 0 0 0 1 0 1 1 4 1 1 LID, TSX5 PB SCHERK SEE SHIP33  3 1 4 6 0 0 0 6 3 7 2 1 LIP STX5 PB SCHERK SEE SHIP33  3 1 4 6 0 0 0 6 3 7 2 1 LIP STX5 PB SCHERK SEE SHIP34  3 1 4 6 0 0 0 6 3 7 0 6 4 1 1 SHIP5 PB SCHERK SEE SHIP34  3 1 1 0 0 0 0 1 1 1 5 1 LIP SHIP5 PB SCHERK SEE SHIP34  3 1 1 0 0 0 0 1 1 1 5 1 LIP SHIP5 PB SCHERK SEE SHIP34  3 1 1 0 0 0 0 1 1 1 5 1 LIP SHIP5 PB SCHERK SEE SHIP34  3 1 1 0 0 0 0 1 1 1 5 1 LIP SHIP5 PB SCHERK SEE SCHERK	3	0	3	0	0	3.	0	0	2	1		BOTTOM COVER HIM1935		$\exists$	
1   1   0   0   1   3   7   1	3	0	3	0	0	5	0	0	7	1	11	CLIP FEEDTHRU A4M2008		+	
1   1   0   0   1   3   7   1	3	0	8	0	0	1	3	0	6	6	1	HEATSINK DRIVER A4M1830			
1 1 1 0 0 0 1 0 3 3 1 1 NNOS 971584  2 1 2 0 0 0 1 0 1 4 1 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 0 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 2 0 1 0 1 4 1 LID TEMS PR SCHER SEE REMINED  3 1 1 0 0 0 1 0 1 1 LID TEMS PR SCHER SEE REMINED  3 1 1 0 0 0 1 1 1 LID TEMS PR SCHER SEE SEED PR  3 1 1 0 0 0 1 1 1 LID TEMS PR SCHER SEED SEED SEED SEED SEED SEED SEED SE														-	
3   2   0   0   1   1   4   1   LIP, TEME THE SOLDER SIDE REMINED 2 3   2   0   0   1   0   1   4   1   LIP, TEME THE SOLDER SIDE REMINED 3 3   2   0   0   1   0   1   5   1   LIP, TEME THE SOLDER SIDE REMINED 3 3   2   0   0   1   0   1   1   1   LIP, TEME THE SOLDER SIDE REMINED 3 3   1   0   0   0   1   0   1   1   LIP, TEME THE SOLDER SIDE REMINED 3 3   1   0   0   0   1   0   0   1   LEME, LEDY ARREST, STEEL SOLDER SIDE REMINED 3 3   1   0   0   0   1   1   1   LIP, TEME THE SOLDER SIDE REMINED 3 3   1   0   0   0   1   1   1   LIP, TEME THE SOLDER SIDE SIDE SIDE SIDE SIDE SIDE SIDE SIDE	3	0	8	O	0	1	3	0	_7	1	1	HEATSINK DIECAST AIM1931		-	
1   2   0   0   1   5   1   LID TENS PR COMPONENT SIDE NOM1933   1   2   0   0   1   0   4   1   1   LIDS, DIGITAL DISPLEY LINEAU	3	1	1	0	0	0	1	0	3	3	1	KNOB A3M1584			
1   2   0   0   1   5   1   LID TENS PR COMPONENT SIDE NOM1933   1   2   0   0   1   0   4   1   1   LIDS, DIGITAL DISPLEY LINEAU			_					_						-	
3 1 2 0 0 0 1 0 0 1 1 0 2 1 LENS, DELTA DEPLOY LABOR 30M226  3 1 2 0 1 0 1 0 1 1 LENS, LEDS ARREST RAYLES 6  3 1 6 0 0 0 6 3 7 2 1 FRONT PRINCE, 15 15 TRUMBURG 28 13 13 3, 5K 12  3 1 6 0 0 0 6 3 7 2 1 FRONT PRINCE, 15 15 TRUMBURG 28 13 13 3, 5K 12  3 1 6 0 0 0 7 0 6 4 1 PRILAR HIMM PASTE SHAPIN  3 1 9 0 0 0 1 1 4 9 1 SHEED TWO YOU BREAZES  3 1 9 0 0 0 1 1 4 9 1 SHEED TWO YOU BREAZES  3 1 1 0 0 0 1 1 5 1 1 SHEED TWO YOU BREAZES  3 1 1 0 0 0 1 1 1 5 3 1 SHEED TWO YOU BREAZES  3 1 1 0 0 0 1 1 1 5 3 1 MIC SHEED TWO PRINCES AND AND ASSOCIATION OF THE PRINCES AND AND ASSOCIATION OF THE PRINCES AND AND ASSOCIATION OF THE PRINCES AND AND AND ASSOCIATION OF THE PRINCES AND AND ASSOCIATION OF THE PRINCES AND AND ASSOCIATION OF THE PRINCES AND A	3	1	2	0	0	0	1	0	1	4	1	LID T5X5 PA SOLDER SIDE H2M1932		•	
3   2   0   4   4   0   0   1   LENS, LEDS ARREST RAWES &			2	0	0	0	1	0			1_1			_	
3 1 6 0 0 0 6 3 7 2 1 FRONT PRINTER, TSYS TRUMING A3H323, SX h2  3 1 6 0 0 8 7 0 6 4 1 PRILAR HIATE PARTY SAMPLE OF	3	1	2	0	0	0	1	0	4	1	1	LENS, DIGITAL DISPLAY WINDOW, A3M2286	-		
3   6   0   0   6   5   0   9   4   1   TAIL MOUNTING PLATE SHAPE IN  3   1   6   0   0   8   7   0   6   4   1   TAILAR HILD MASTE SHAPE IN  3   1   0   0   0   1   1   4   9   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   7   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   7   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   7   1   SHIELD TWO PLATE SHAPE IN SHAPE SHAPE SHAPE IN SHAPE SHAPE IN SHAPE	3	1	2	0	1	0	4	4	0	0	1	LENS , LEDS . A48 631 A4M1586			
3   6   0   0   6   5   0   9   4   1   TAIL MOUNTING PLATE SHAPE IN  3   1   6   0   0   8   7   0   6   4   1   TAILAR HILD MASTE SHAPE IN  3   1   0   0   0   1   1   4   9   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   7   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   7   1   SHIELD TWO PLATE SHAPE IN  3   1   0   0   0   1   1   5   7   1   SHIELD TWO PLATE SHAPE IN SHAPE SHAPE SHAPE IN SHAPE SHAPE IN SHAPE	-						-	-	_	-				-	
3   6   0   0   7   0   6   4   1   PILLAR	3	1	6	0	0	0	6	3	7	2	1_1_	FRONT PANEC, T5×5 TRUNKING, #3H2323,381	16.2		
1	3	4	6	0	0	8	5	0	9	4	1	BNC MOUNTING PLATE 94M2160			
1	-	_	-	_		-	-	-	-	1	1 .	The of the Carrie CNRC IN		-	
3 1 9 0 0 0 1 1 5 1 5 1	3	1			1				1		1				
3 1 9 0 0 0 1 1 5 7 L SHED COLORED SIDE AND 320 18/1 12 3 1 9 0 0 0 1 1 3 5 1 SHED COLORED SIDE AND 320 18/1 12 3 1 9 0 0 0 1 1 3 5 1 SHED PRIVER SKT AND 320 1 SHED AND 320 1 SHEDWARD 320 1 SHEDDAY 320 1 SHEDAY 320 1 SHEDDAY 320 1 SHEDAY 320 1 SHEDDAY 320 1 SHEDDAY 320 1 SHEDAY 320 1 SHEDDAY 320 1 SHEDAY 320 1 SHEDDAY 320 1	3	i		0	0	0	1	1		1		SHIELD TIN PLATE A3M2236	SOLDER TAG MUST BE CUT OFF		31/6 303
3 1 9 0 0 0 11 3 5 1 5 MIC SHIELD, POWER ENT AM 2551 3 1 9 0 0 0 1 1 5 8 1 MIC SHIELD ABAY2521 3 4 9 0 0 0 1 0 2 5 2 SCREW M3 12 MIN SWA PRIVATE 3 4 9 0 0 0 1 0 2 5 2 SCREW M04 13 MIN SWA PRIVATE 3 4 9 0 0 0 1 0 2 6 2 SCREW M04 13 MIN SWA PRIVATE 3 4 9 0 0 0 1 0 2 6 2 SCREW M04 13 MIN SWA PRIVATE 3 4 9 0 0 0 1 0 2 6 2 SCREW M04 13 MIN SWA PRIVATE 3 4 9 0 0 0 2 0 3 1 9 SCREW M3 10 MIN PRIVATE 3 4 9 0 0 0 2 0 3 1 9 SCREW M3 10 MIN PRIVATE 3 4 9 0 0 0 1 0 2 0 3 2 22 SCREW M3 10 MIN PRIVATE 3 4 9 0 0 0 0 2 0 3 2 22 SCREW M3 10 MIN PRIVATE 3 4 9 0 0 0 0 2 0 3 2 22 SCREW M3 10 MIN PRIVATE 4 MEATSINK COVERS 16 3 5 2 0 0 0 1 0 0 8 2 NUT M3 HEX 4 9 3 5 2 0 0 0 1 0 3 5 2 NUT M3 HEX 5 5 2 0 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 93 5 4 0 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 94 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 0 1 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 0 1 1 1 2 SCREW M3 10 MIN SHOW THETITE 95 10 0 0 1 0 1 0 1 1 1 1 1 1 1 1 1 1 1 1	3	1								_	1				39/6 3:3
3 4 9 0 0 0 1 0 2 6 2 SCREW NO 4 1 STAN SUPE PROPERTY OF THE THE TENT COVERS 4  3 4 9 0 0 0 1 0 2 6 2 SCREW NO 4 1 STAN SUPE PROPERTY OF THE TENT COVERS 4  3 4 9 0 0 0 1 0 2 6 2 SCREW NO 4 1 STAN SUPE PROPERTY OF THE TENT COVERS 4  3 4 9 0 0 0 2 0 3 1 9 SCREW M3 & 10mm Am Row That The The Tent Tent Covers 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	3	1	-					_							
3 4 9 0 0 0 1 0 2 5 2 SCREW NO 4 13 THN SUPS PROMATE  3 4 9 0 0 0 1 0 2 6 2 SCREW NO 4 13 THN SUPS PROMATE  3 4 9 0 0 0 2 0 3 1 9 SCREW M3 x 10mm FAN FOR TRATTITE VCO GOX/LID 4 RASTROWERS 4  3 4 9 0 0 0 2 0 3 2 22 SCREW M3 x 10mm FAN FOR TRATTITE VCO GOX/LID 4 RASTROWERS 4  3 4 9 0 0 0 2 0 3 2 22 SCREW M3 x 10mm FAN FOR TRATTITE VCO GOX/LID 4 RASTROWERS 4  3 4 9 0 0 0 2 0 3 2 22 SCREW M3 x 10mm FAN FOR TRATTITE VCO GOX/LID 4 RASTROWERS 4  3 5 2 0 0 0 1 0 0 8 2 NUT M3 HEX  3 5 2 0 0 0 1 0 3 5 2 NUT M3 HEX  3 5 2 0 0 0 1 0 3 5 2 NUT M3 HEX  3 5 2 0 0 0 1 0 1 1 2 WASHER M3 FLAT  3 5 3 0 0 0 1 0 1 1 2 WASHER M3 SHAKEPROST  3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROST  3 5 7 0 0 0 0 1 0 0 1 2 SOUPER TAG 3mm SHAR!  3 5 7 0 0 0 1 0 0 4 1 1 COMPANY SEP 3253  3 5 7 0 0 0 1 0 1 2 2 FURNITURE FOOT  3 5 7 0 0 0 1 0 1 2 2 FURNITURE FOOT  4 0 0 0 0 0 2 0 0 3 2 20 3 20 MM SHILLERM SELECTION  4 0 0 0 0 0 2 0 0 3 20 3 20 MM SHILLERM SELECTION  4 0 0 0 0 0 2 0 0 3 20 3 20 MM SHILLERM SELECTION SELECTION  4 0 0 0 0 0 2 0 0 3 20 0 3 20 MM SHILLERM SELECTION SELE		1			0	0					11		Τ. Δ.	-	372-041
3 4 9 0 0 0 1 0 2 6 2 SCREW M3 X10mm PAN PRI FOX. TAPTITE VCU COX/LID 4 PARSICAVERS 4  3 4 9 0 0 0 2 0 3 1 9 SCREW M3 X10mm PAN PRI FOX. TAPTITE VCU COX/LID 4 PARSICAVERS 4  3 4 9 0 0 0 2 0 3 2 22 SCREW M3 X10mm PAN PRI FOX. TAPTITE HEATSINK/PCO 2 047 2 39 13:  3 5 2 0 0 0 1 0 0 8 2 NUT M3 HEX 93 TESTINK COVERS 16  3 5 2 0 0 0 1 0 3 5 2 NUT M3 HEX 93 TESTINK COVERS 16  3 5 3 0 0 0 1 0 1 3 2 LIMSHER M3 FLAT 93 TESTINK  3 5 3 0 0 0 1 0 1 3 2 LIMSHER M3 FLAT 93 TESTINK  3 5 7 0 0 0 1 0 0 1 2 SOUTH TAB SHAKEPROST 93 TESTINK  3 5 7 0 0 0 1 0 0 9 4 PASH ON FIX SEP 3253 SPAR MTS  3 5 7 0 0 0 1 0 1 4 1 1 PASH ON FIX SEP 3253 SPAR MTS  3 5 7 0 0 0 1 0 1 0 1 2 FURNITURE FOOT VCO  3 9 9 0 0 0 1 0 1 0 1 2 PASH ON FIX SEP 3253 SPAR MTS  3 6 9 0 0 0 1 0 1 0 1 2 PASH ON FIX SEP 3253 SPAR MTS  4 0 0 0 0 0 0 1 0 1 2 PASH ON FIX SEP 3253 SPAR MTS  4 0 0 0 0 1 0 1 0 1 2 PASH ON FIX SEP 3253 SPAR MTS  4 0 0 0 0 1 0 1 0 1 2 PASH ON FIX SEP 3253 SPAR MTS  4 0 0 0 0 1 0 1 0 1 2 PASH ON FIX SEP 3253 SPAR MTS  4 0 0 0 0 1 0 1 0 1 1 2 PASH ON FIX SEP 3253 SPAR MTS  4 0 0 0 0 0 0 0 0 0 0 1 0 1 1 1 1 1 1 1	_3	19	13	0	0	0	1 4	0	0	18	1				
3 4 9 0 0 0 2 0 3 1 9 SCREW M3 × 10mm PAN PRI TRATITE VCO BOX/LID 4 PLASTIC CHERS 4  3 4 9 0 0 0 2 0 3 2 22 SCREW M3 × 10mm PAN PRI TRATITE HEATSINIK/PER 2 947 2 991 13:  HEATSINIK COVERS 16  3 5 2 0 0 0 1 0 0 8 2 NUT M3 HEX 93, TE4  3 5 2 0 0 0 1 0 1 1 2 LURSHER M3 FLAT 93. TE4  3 5 3 0 0 0 1 0 1 1 2 LURSHER M3 SHAKEPROOF 93 TE4  3 5 3 0 0 0 1 0 1 2 SOLDER THE 3mm SHARI PROOF 93 TE4  3 5 7 0 0 0 1 0 0 9 4 PLISH ON FIX SEP 3253 SPIKE MTE  3 5 7 0 0 0 1 0 1 2 2 FLIANTURE FOOT VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC GREE 200. 3250mm 4 0 0 0 0 0 2 0 0 3 20mm SHARI S	3	4	9	O	0	0	1	C	2				PLASTIC COVER 2	_	- (W : 57)
3 4 9 0 0 0 2 0 3 1 9 SCREW M3 X 10mm AN BY TRATITE YOU BOX/LID 4, RASHE WERS 4 03 1  3 4 9 0 0 0 2 0 3 2 22 SCREW M3 X 10mm AN BY TRATITE HEATSINK (PED 2 047 2 37/1 10)  4 9 0 0 0 2 0 3 2 22 SCREW M3 X 10mm AN BY TRATITE HEATSINK (PED 2 047 2 37/1 10)  4 9 0 0 0 0 1 0 0 0 0 2 0 3 2 22 SCREW M3 X 10mm AN BY TRATITE HEATSINK (OVERS 16 10)  3 5 2 0 0 0 1 0 0 3 5 2 NUT 5-32 UNC 046, 047  3 5 3 0 0 0 1 0 1 1 2 WASHER M3 FLAT 03 TC4  3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROF 93 TC4  3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROF 93 TC4  3 5 7 0 0 0 1 0 0 1 2 SOLPER TAG 3 mm SHORE BY SHAKEPROF WCO  3 5 7 0 0 0 1 0 0 1 2 SOLPER TAG 3 mm SHORE BY SHAKEPROF WCO  3 5 7 0 0 0 1 0 1 2 2 FURNITURE FOOT VCO  4 0 0 0 0 0 1 0 5 6 1 PLASTE BAG 200 3 150mm WCO  4 0 0 0 0 0 2 0 0 3 20 Mm Imm SILICONE SLEEVING SILICON RIBER BY SHORE BY	3	4	9	.0	0	0	1	C	2	1		SCREW NO. 1.5 HD PAN SUPA POLYMATE			
3 4 9 0 0 0 2 0 3 2 22 SCREW M3 & STAN FRW FOX! TAPTITE HEATSINK/PEG 2, 947 2 39/13:2  3 5 2 0 0 0 1 0 0 8 2 NUT M3 HEX 93, TE4  3 5 2 0 0 0 1 0 1 1 2 WASHER M3 FLAT 93 TC4  3 5 3 0 0 0 1 0 1 1 2 WASHER M3 SHAKEPROOF 93 TC4  3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROOF 93 TC4  3 5 6 0 0 0 1 0 0 1 2 Solper TBG 3mm SHOR; PRUER SKT SHICLT) STANGER SKT SHICLT) STANGER SKT SHICLT SKT SKT SHICLT SKT SKT SKT SKT SKT SKT SKT SKT SKT SK					Ī			Ì	Ė						
3 4 9 0 0 0 2 0 3 2 22 SCREW M3 & STAN FAN FOR TARTITE HEATSINK / PEB 2 047 2 94/ 13:  1 9 0 0 0 2 0 3 2 22 SCREW M3 & STAN FAN FOR TARTITE HEATSINK COVERS 16  1 10 0 0 0 2 0 3 2 2 NUT M3 HEX  93, TE4  93, TE4  3 5 2 0 0 0 1 0 1 0 3 5 2 NUT F-32 UNC  946, 947  3 5 3 0 0 0 1 0 1 1 2 UMASHER M3 SHAKEPROFF 93 TC4  3 5 3 0 0 0 1 0 1 2 2 WASHER M3 SHAKEPROFF 93 TC4  3 5 6 0 0 0 1 0 0 1 2 2 SOUPER TAG 3 MM SHOR! PRIME SKT SHIELD SHAWAR STAN SHOR! PRIME SKT SHIELD SHOW THE STAN SHOR! PRIME SKT SHIELD SHOW THE STAN SHOR! SPER MTG  3 5 7 0 0 0 1 0 4 4 1 0 CLIP NILDY 3.2 WASHER FOOT VCO  3 9 9 0 0 0 1 0 1 2 2 FURNITURE FOOT VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC BAG 200.250 MM  4 0 0 0 0 2 0 0 3 20 M 10 MM SHOR! SUCCON S	3	4	9	c	0	C	2	0	3	1	9	SCREW M3 × 10mm PAN BOU TAPTITE		-	
3 5 2 0 0 0 1 0 0 8 2 NUT M3 HEX 93, TE4  3 5 2 0 0 0 1 0 3 5 2 NUT \$-32 UNC 946, 947  3 5 3 0 0 0 1 0 1 1 2 WASHER M3 FLIAT 93 TC4  3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROFF 93 TC4  3 5 6 0 0 0 1 0 0 1 2 SOUDER TAG 3MM SHOREI BUER SKT SHIELT STAUGH.  3 5 7 0 0 0 1 0 0 9 4 RISH ON FIX SFP3253 SPAR MTG  3 5 7 0 0 0 1 0 0 4 4 1 PCLIP NYLON 3.200 SPAR MTG  3 6 9 0 0 0 1 0 1 2 2 FURNITURE FOOT VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC GAG 200.250mm  4 0 0 0 0 0 2 0 0 3 20mm Jam Silveone Success Success Unice  4 0 0 0 0 0 2 0 0 3 20mm Jam Silveone Success Success Unice  4 0 0 0 0 0 2 0 0 3 20mm Jam Silveone Success Su	7	4	9	0	0	6	2	0	) 3	2	22	SCREW M3 + 8mm FAN FOZ. TAPTITE			341 303
3 5 2 0 0 0 1 0 1 1 2 WASHER M3 FLAT  3 5 3 0 0 0 1 0 1 1 2 WASHER M3 SHAKEPROF  3 5 3 0 0 0 1 0 1 1 3 2 WASHER M3 SHAKEPROF  3 5 6 0 0 0 1 0 0 1 2 SOLDER TAG 3MM SHOR!  3 5 7 0 0 0 1 0 0 9 4 RISH ON FIX SFP 3253 SPAR MTG  3 5 7 0 0 0 1 0 4 4 1 0 CLIP NYLON 3.2~  3 6 9 0 0 0 1 0 1 2 2 FURNITURE FOOT  4 0 0 0 0 0 1 0 5 6 1 PLASTIC BAG 200, 1250mm  4 0 0 0 0 0 1 0 0 1 0 1 0 1 0 1 0 1 0 1						L	1		L	-	<u> </u>		HEATSINK COVERS 16	_	
3 5 2 0 0 0 1 0 1 1 2 WASHER M3 FLAT  3 5 3 0 0 0 1 0 1 1 2 WASHER M3 SHAKEPROF  3 5 3 0 0 0 1 0 1 1 3 2 WASHER M3 SHAKEPROF  3 5 6 0 0 0 1 0 0 1 2 SOLDER TAG 3MM SHOR!  3 5 7 0 0 0 1 0 0 9 4 RISH ON FIX SFP 3253 SPAR MTG  3 5 7 0 0 0 1 0 4 4 1 0 CLIP NYLON 3.2~  3 6 9 0 0 0 1 0 1 2 2 FURNITURE FOOT  4 0 0 0 0 0 1 0 5 6 1 PLASTIC BAG 200, 1250mm  4 0 0 0 0 0 1 0 0 1 0 1 0 1 0 1 0 1 0 1	-	-	10	-	+	-	+	+	+	+		NUT M3 HEV	03 Te4		
3 5 3 0 0 0 1 0 1 1 2 WASHER M3 FLAT 93. IC4  3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROOF 93 IC4  3 5 6 0 0 0 1 0 0 1 2 SOLDER TAG 3MM SHOR!) POWER SKT SHIELD STANDARD STANDAR		2	-							Ť	1				
3 5 3 0 0 0 1 0 1 3 2 WASHER M3 SHAKEPROOF 93 IC4  3 5 6 0 0 0 1 0 0 1 2 SOLDER TAG 3mm SHOR! POWER SKT SHIPLTY STEP 3253  3 5 7 0 0 0 1 0 0 9 4 PUSH ON FIX SFP 3253 SPKR MTG  3 5 7 0 0 0 1 0 4 4 1 PCLIP NYLON 3.222 VCO  3 9 9 0 0 0 1 0 1 2 2 FURNITURE FOOT VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC BAG 200, 25Dmm  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SUCCO SURRER LTD  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SUCCO SURRER CANDON STEP STEP SUCCO SURRER CANDON STEP SUCCO SURRER CANDON SUCCO SUCCO SURRER CANDON SUCCO SU	-3	5	2		0	C	1		2 3	5 5	2	NUT 6-32 UNC	Q46, Q47	-	
35300010132 WASHER M3 SHAKEPROOF 03 TC4  35600010012 Solder tag 3mm short Power skt shicld Spring  3570001009 4 Posh on Fix sep 3253 Spring  3570001044 1 Polip Nylon 3.2 WCO  3990001012 E FURNITURE FOOT VCO  3990001056 1 Plastic Bag 200, 250mm  4000002003 20mm Imm silicone silicon sugges L76  4000002003 20mm Imm silicone silicon rugges R238C281 Spling  400000000000000000000000000000000000	-	5	3		00	1	5 1	1. 0		1 1	_ 2	WASHER M3 FUAT	93. TC4		
3 5 6 0 0 0 1 0 0 1 2 SOLDER TAG 3mm SHOR.)  3 5 7 0 0 0 1 0 0 9 4 RUSH ON FIX SEP 3253  3 5 7 0 0 0 1 0 4 4 1 P CLIP NYLON 3.2 VCO  3 9 9 0 0 0 1 0 1 2 2 FURNITURE FOOT  4 0 0 0 0 1 0 5 6 1 PLASTIC BAG 200, 250mm  4 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 1 0 0 1 0 0 1 0 0 0 1 0	-		1	1	_	1	-			-	1			-	-
3 5 7 0 0 0 1 0 0 9 4 PUSH ON FIX SEP 3253 SPAR MTG  3 5 7 0 0 0 1 0 4 4 1 0 CLIP NYLON 3.2-  3 6 9 0 0 0 1 0 1 2 2 FURNITURE FOOT  VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC BAG 200, 250mm  4 0 0 0 0 0 1 0 0 5 6 1 PLASTIC BAG 200, 250mm  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER LTD  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER  4 0 0 0 0 0 1 0 0 1 0 0 1 1 TSUSTR WITH LETTO HAROBOOK  51/10-53	-	3 5	3		0	1	2 1	L	2 1	13	2	WASHER M3 SHAKEPROOF	Q3_IC4		
3 5 7 0 0 0 1 0 4 4 1 P CLIP NILON 3.2~ 315-12.  3 6 9 0 0 0 1 0 1 2 2 FURNITURE FOOT VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC BAG 200x 250mm  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER LTG  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.	-	3 5	6	(	0	5	2	1	0 0		2	SOLDER THE 3mm SHORI	POWER SKT SHIELD		alp. a
3 5 7 0 0 0 1 0 4 4 1 P CLIP NILON 3.2~ 315-12.  3 6 9 0 0 0 1 0 1 2 2 FURNITURE FOOT VCO  3 9 9 0 0 0 1 0 5 6 1 PLASTIC BAG 200x 250mm  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER LTG  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER RESERVING.	-	-	-	+	+	+	+	+	+	+		0	ERKR MTG	$\vdash$	
3 9 9 0 0 0 1 0 1 2 2 FURNITURE FOOT  4 0 0 0 0 0 1 0 5 6 1 PLASTIC BAG 200, 250mm  4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER LIB 4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER 4 0 0 0 0 0 2 0 0 3 20mm Imm SILICONE SILICON RUBBER 4 0 0 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 0 1 0		_	_		_	_			_				51.71.1		33/3-12
4000002003 20mm SILICONE SILICON RUBBER L76 XAPA-1164 400002003 20mm SILICONE SILICON RUBBER L76 400002003 20mm SILICONE SILICON RUBBER L76 4000000000000000000000000000000000000													VCO	-	-
4000002003 20mm SILICONE SILICON RUBBER L76 XAPA-1164 400002003 20mm SILICONE SILICON RUBBER L76 400002003 20mm SILICONE SILICON RUBBER L76 4000000000000000000000000000000000000	-	1	1	+	+	+	-	+	1	1	4	P. 9571C BBC 270x 250mm			
4000002003 20mm 1mm SILICONE SLEEVING. 40000020000000000000000000000000000000		$\neg$		_				$\neg$			11		416		31/20-11
4095450000 1 TS4STR WEID HOUSE		4 0	2 0	2	0 0	2	0 1	2	0 0	2	3 20m	Imm SILICONE SLEEVING	0212 8 0 2 81	1	Photo
													I SALVE STACE		
THOOOLS TO BUC CENE	F													+	maho eco
The second control of		24	10	1	2	1	1	3	011	+	2	PICK BING (EMB		1_	1

6.5 B545-94 CONTROL PCB MANUAL INSERTION

0	0	2	0	0	0	1	7	2	0	1	MC14499 INT CCT	Ie 100 ,	-
0	0	2	0	0	0	1	g	0	8	_11	27C256 INT CET	J.c &	
0	0	2	0	0	0	1	В	3	0	_1	MK 4-82 12 -20 INT CCT	769	
0	0	В	0	0	0	1	0	1	5	2	LGD, 3mm GREEN	LED1 LED2	
0	0	ક	0	0	0	1	4-	7	2	3	LED, 10 +5mm RED (HLMP 2300)	LEDIOO, LEDIOI, LEDIO2	
0	0	8	0	0	0	2	0	1	0	4-	LED DIGIT HDSP7503	LED 103, LED 104, LED 105, LED 106	
0	5	6	0	0	0	2	1	0	1	3	FXD IND, 1.5, ell.	L1, L2, L3	
2	3	2	0	0	0	1	0	2	3	3	SWITCH KEYBOARD, SP PLUG IOWAY IKON PC MTG HEADER	Swiro, swioi, swioz	31/1 - 41
2									4	1	HEADER 4 WAY 1 ROW (15.5 mm Rus)	PL-9	
2	4	0	0	0	0	2	1	1	0	1	HEADER LOWAY IROW (18.5 MAS)	PL-8	
2	4	0	0	4	0	2	0	5	5	22	SOCKET SWAY 1 ROW, BOTTOM ENTRY	SKT-1, SKT-2	
2	4	0	0	4	0	2	0	5	6	1	SOCKET SOWAY I ROW BOTTOM ENTRY	SKT-4	
2	4	0	0	4	0	2	0	6	4	0.440	SICT, 0.98mm PIN JACK, 64WAY CARRIER	28 KEDWIRETS	
2	4	0	0	4	0	2	0	9	1	1	SOCKET, 6WAY PHONE JACK PORTY MOLD	SKT -10	
2	4	0	0	4-	0	2	1	1	3	T	SOCKET 3WAY SROW PEBLITG, BOTTOMENTRY	5KT-7	
2	1	0	0	4	0	2	1	1	8	_ff	SOCKET IS WAY I HOW PEB MIG POTTEM BUTRY	CK1-13	
2	4	0	0	4	0	2	1	2	4	1	SOCKET 4 WHY EDGE PEB MIE SIDE ENTRY	5KT-9	
2	4	c	0	4	0	2.	1	2	7	1	SCEKET JOWAY EDGE PER MIE, SIDE ENTRY	SKT-8	
2	7	4	0	0	0	1	0	3	6	1	CRYSTIAL 6-064 MHZ.	xc1.	
3	0	4	0	0	0	7	0	2	0	1	DAMPER A4M 1817 (LOVED ENSEAL)		

## 6.6 B545-95 CONTROL PCB SMD

0	0	0	1	0	0	0	4	1	0	1	MJD41C TRANSISTOR	Φ11	3/2
0	0	0	3	0	0	0	8	4	8	14	BCW60 BC848 TRANSISTOR	φ2, Q3, Q4, Q5,	33/1 331
		_			Ť			_	_			012, 013, 0100, 0101, 0102, 0103	
	7	7		_								Q 104	81/5-249
0	0	0	1	0	0	0	8	5	7	2	BCW70/BC857 TRANSISTOR	Q1, , Q14-	37/7-37/
	_	_									·		
0	0	1	1	0	0	0	0	7	0	4	BAV70 DIODE	D63, D64, D65, D66	-
0	0	1	1	0	0	8	4	6	2	1	BZ×64/C6V2 ZENER	D62	
0	0	2	1	0	0	0	3	2	4	1	LM324 INT CCT SMD	TC2	-
0	0	2	1	0	0	0	4	1	9	1	F×419	TC 14	
0	0	2	1	0	0	4	0	2	7	11	40278 - "	rel.	_
	0	2	7	0	0	4	0	9	4-	2	4094 " "	Te3 Te4	
		-	-1			-			4		4031		
0	0	2	1	0	0	4	1	0	4	1	4104	Te40	
	0	2	-		_	7	-	_	_	1	78405	v Reg I	
		ے	1			-/			3		7,8203	7 KeG1	
0	0	2	1	0	3	4	0	6	4-	1	MC34064D INT CCT SMD	I=15	
	0	_	-		c	0	_	-	-	1	68HC111-1	7<15	
0		_	7	0	G.	5	3	1	1		0017-11114	» → » €	
0	0	2	7	4	9	0	0	0	0	2	74нсоо	IC6, IC 16	39/6 343
-	-	_	-	-		_	_					_	
0	0	2	7	4	9	0	1	3	19		74HC139	Ic S	
0	0	2	7	4	9	0	5	7	3	1	74HC573	I<7	
	-	-			- 1			_					
0	0	2	7	4	9	0	5	7	4-	<u>.</u>	74HC 574 · · · · · · · · · · · · · · · · · · ·	Tc 13	
											4/		
0	1						_			5	100m CAP 10%, XTR 50V 1206 CHIP	, C62, C78, C37, C88, C100	37/7-3-11
0								0			18P CAP 5% NPO SOU 3805 CHIP	C61	97/1-37/
0	1									<u> </u>	22P CAP 5% NPO 50V 0605 CHIP	C 105	377-445
0								0	1	1	33P	, C.76	337-371
0	1	5	2	3	1	0	0	0	1	_ 1	1000	< 45	अभिन्य
	L								1_	1_	279 "	C10+	84/2-34
0	1	5	2	4-	1	0	0	0	8	7	in - 10% X7R 50V 0805 CMP	c10, c11, c12, c13, ., c83,	14/2-31.
		_						$\vdash$	-	<u></u>	-20 11 1 21 21 21	C\$4, c90,	31/5-36
0	1	5	2	2	3	9	0	0	1	1	39P CAP SI NPO SON 0805 CHIP	Cho	37/1-030
0	1	2	2	כ	1	0	0	0	8	7	10m CAP 10% X7R 50V 0805 CHIP	c65, c66, c67, c75, c95, c96	
		$\neg$	1 10					$\vdash$	$\vdash$			C101	1
0	1	6	0	7	1	0	0	0	1	5	JU CAP 16V ELECTRO 4×57mm SMD	CSO, , C68, C77, C80, C86	34/2-3-11
H						_		-	-				
0	1	G	0	8	7	0	0	0	1	4	104 CAP 16V ELECTRO 4.5.7mm SMT)	-, c43, c89, c102, c103	+-,-
0	2	8	1	2	2	0	0	0	1	L	200 CAP TRIMMER SMD	CVI	54/5-269
		$\perp$				_		-					
		$\dashv$		-	-	-	-	-	-		<del></del>		157/7-3-11
0	3	6	1	2	1	8	0	0	0	3	18E RESISTOR 5% M/F 0805 CHIP	R110, R111, R112	
0	3	6	1	2	4	7	0	0	0	2	47€	TR125, R126	
0	3	6	1	2	1	5	0	0	0	1	150E · · · · ·	TR66	
						Ĺ	Ĺ	Ĺ	Ĺ				
0	3	6	1	3	2	7	0	0	0	10	270€	782, R84, R100, R101, R102	_
$\vdash$	-	$\dashv$			H	-	-	$\vdash$	H			R103, R104, R105, R106, R107	
									-				87/2-37
0	3	6	1	3	6	8	0	0	0	2	650E	R12, R74	31/7-331
							L	1				l	

0	3	6	1	4	1	0	0	0	0	1	IK RESISTOR 5% M/F 0505 CMP TIGO	
0	3	6	1	4	1	5	0	0	0	2	1K5	1-
0	3	6	_1	4	2	2	0	0	0	ð	2K2 " R5, R6, R7 R6, R9 R67, R124,R11	*1/2.43
0	3	6	1	4	4	7	0	0	0	2	4K7 767, RI2I	87/9-53 83/2-43
												31/7-37
0	3	6	1	5	1	0	0	0	0	2.2	10K	
1											R\$3, R91, R92, R93, R95, R116 R119, R122, R127, R130	11/5-249 31/7-39/
0	3	6	1	5	1	2	0	0	0	T	12K	
0	3	6	1	5	2	2	0	0	0	1	22K	
C	3	۲.	1	6	1	0	0	0	0	1,1	100K	
0	3	6	1	6	2	7	0	0	0	1_	270K R118	19/5-241
٥	3	6	7	6	5	6	0	٥	0	11	SLOK DESISTOR SI ORDS CHIP MIE R86.	397-37
0	3	6	١	7	1	0	0	0	0	2	1M	
0	+	2	_1	5	2	0	0	0	1	2	ZOK PRESET RESISTOR SMD. THVI RVZ.	
2	2	0	0	0	0	1	1	6	3	1	PRINTED CET BOMED TELTRICENTROL.	

## 6.7 B500/RC T545TR CRADLE/INSTALLATION/PACKING

2	0	5	0	0	0	1	0	0	6	3.0M	AUTO CABLE 153 2/28/0.3	-	31/7-372
	Ξ.	,			-		-	_	-				
2	4	0	0	2	0	1	0	6	0	1	SKT HOUSING, CHIRY MOLEY	E	
2	4	0	0	2	0	1	0	6	1	2	SKT RECEPTACLE , 152 AUTO		
2	4	0	0	2	0	1	0	6	2	2	SKT RECEPTACLE , 1/0.2	_	
2	5	2	0	0	0	1	0	0	2	_ 1	CLIP, MIC MTG	37	01-313
2	6	5	0	0	0	1.	0	1	7	1	FUSE, 10 AMP CARTRIDGE 6.32m-	-	
3	.0	3	0	0	3	0	0	4	7	1-	CRADLE ASSEMBLY ASMISSO HIMISSI ASMISSE		
-3	0	3	0	0	3	0	0	4-	9	2	CLIP, PLASTIC PRIMIPEZ	ļ	
-3	0	3	0	0	-3	0	0	5	2	2.	KEY, PLASTIC FIGHT 1925		
-3	-4	0	0	0	0	1	0	1	0	11	FUSE HOUDER, IN LINE BOOK YOUSING		
-3	4:	0	0	0	0	_1	0	1	1	2	BODK FUSEHOLDER CRIMP TERMINAL		
-3	4	9	0	0	0	1.	0	4	9	4	SCREW No10 × 2 PAN PRZI SELFTAP CRADLE MTG	- 1 -	
_			_				_	_					
-3	5	-3	0	0	0	1	0	3	2	4-	WASHER ME SHAKEPROF, EXTERNAL CRADLE MIG.		
3	6	97	0	0	С	1.	0	2	7	2	NYLON CRBLE TIE 140mm; 2.6mm ERTTERY LERD TIE		<u> </u>
3	2	9	O	0	0	1	0	5	1		TPLESTIC SPG 75×100		
4	1	0	0	0	0	1	0	3	7	1	-17500 PACKAGING, CARD SLEEVE ANTISLA	_	
4	1	0	0	a	o	1	0	5	0	_1	TSOUR PACKAGING, IBLYSTYRENE . 2765. AM2027		
4	1	O	0	0	0	1	0	5	5	0.1	PACKAGING, CARTON, 10 1500 RADIO'S	. —	17/10 113
						-							
3	5	9	0	0	0	1	0	3	7	4	RIVET 315mm ST FLET HD TIMENS NOT SUPPLIED TO HENGER CF		

•	a ± ±			
		*		
		-		
		*		
*1				

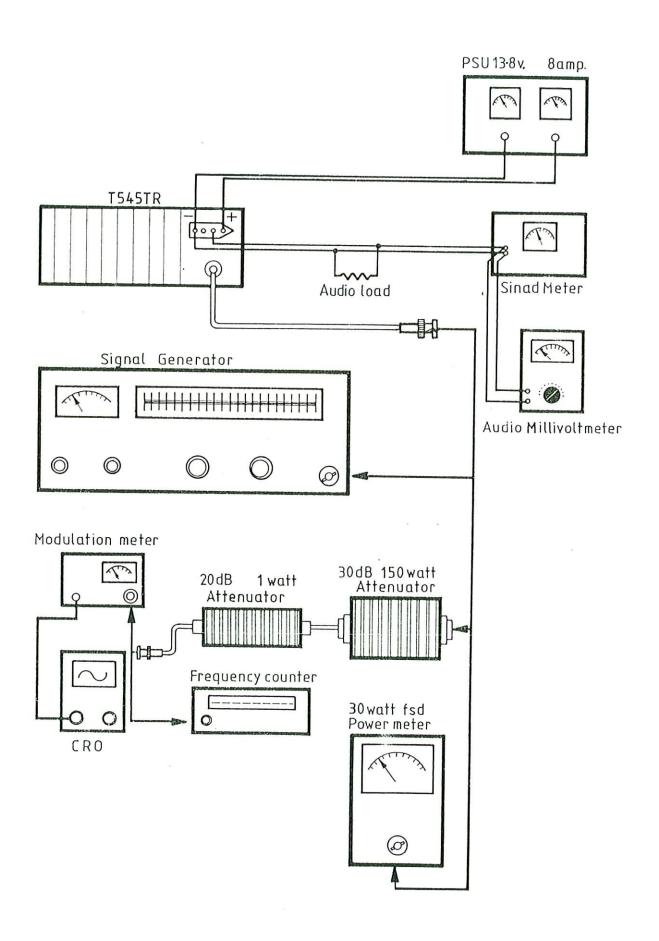


Diagram 1 Suggested Test Equipment Set-Up

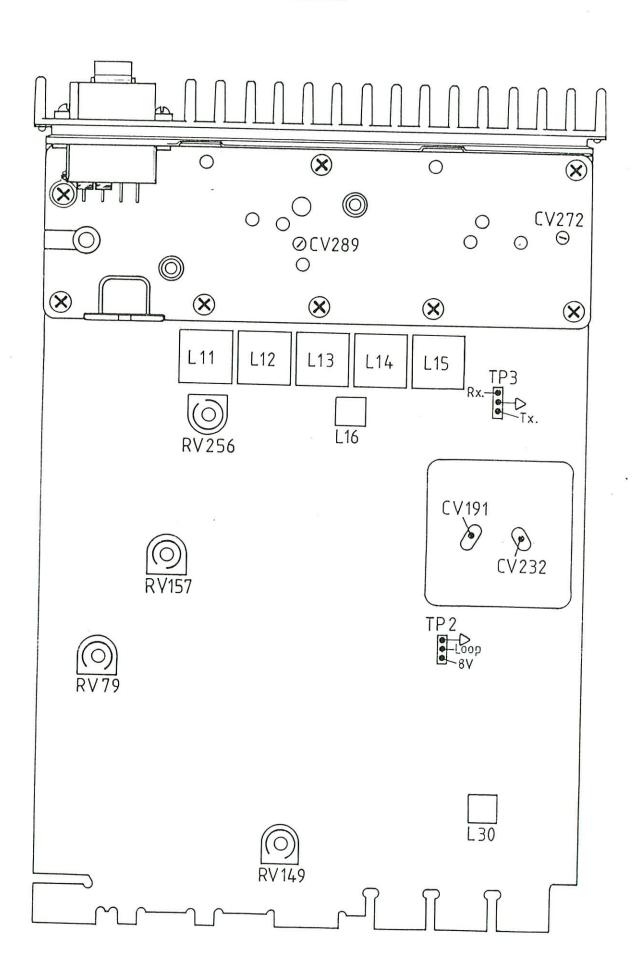


Diagram 2 T545TR Tuning Points

#			

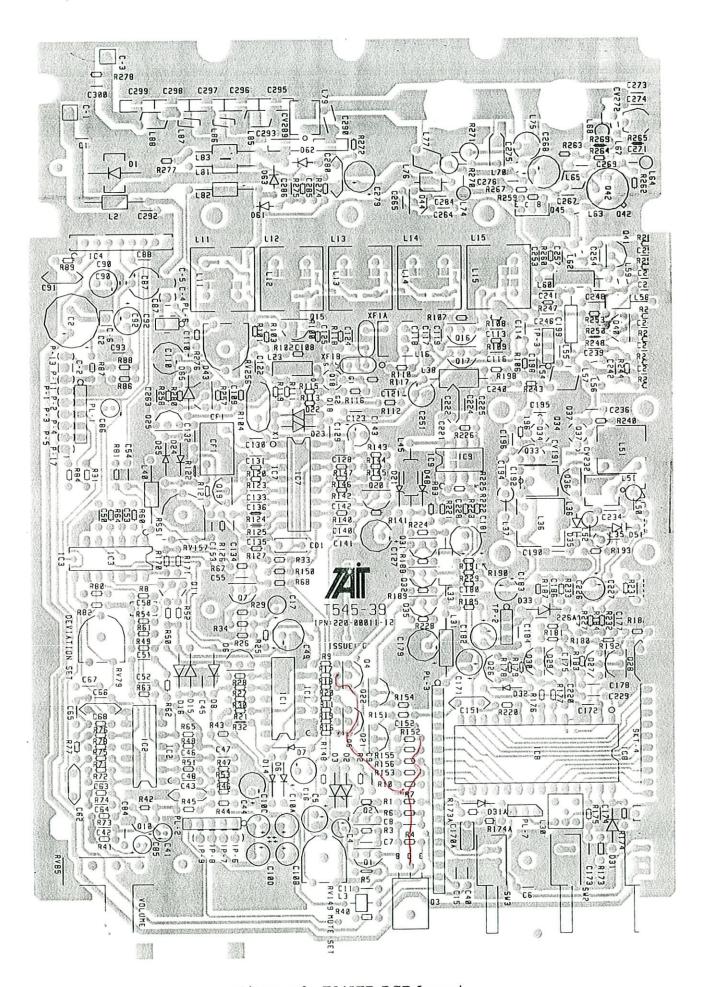


Diagram 3 T545TR PCB Layout



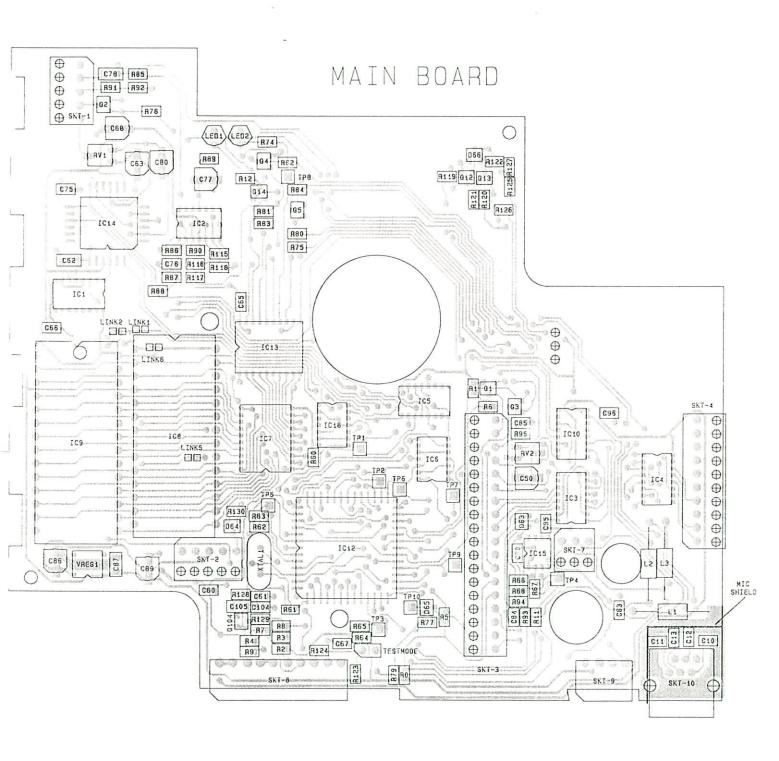


Diagram 4 Trunking Control Board Layout

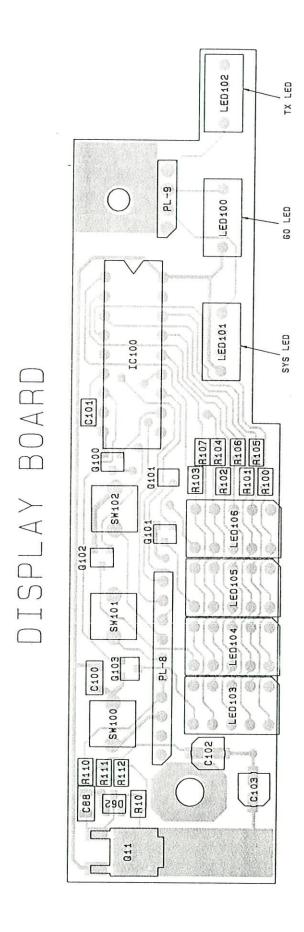


Diagram 5 Trunking Display Board Layout

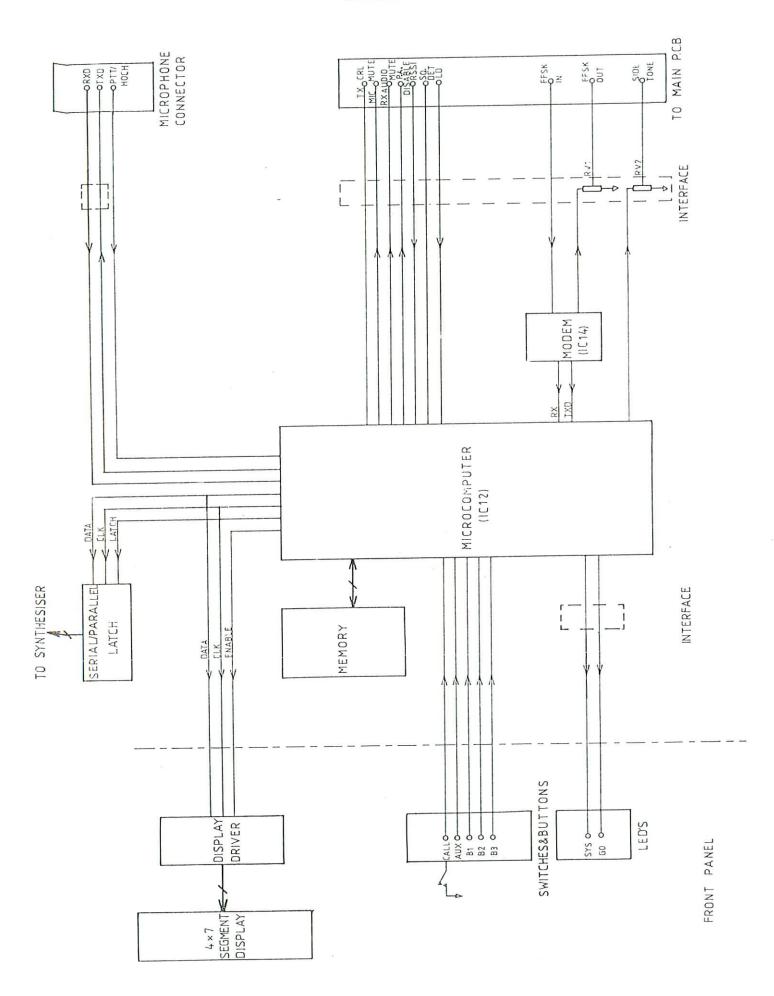


Diagram 6 Trunking Control Board Block Diagram

